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# Extending propositional separation logic for robustness properties

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# Separation logic and program verification

- Separation Logic (Reynolds'02) is used in Hoare proof systems for program verification of languages with pointers.
- Hoare calculus: axioms and rules reason about triples:

$$\{\varphi\} \mathbf{P} \{\varphi'\}$$

Any (memory) model that satisfies  $\varphi$  will satisfy  $\varphi'$  after being modified by the program  $\mathbf{P}$ .

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- Tools: Infer (Facebook), SLayer (Microsoft)...

Also, see “Why Separation Logic Works” (Pym et al. '18)

# Memory states

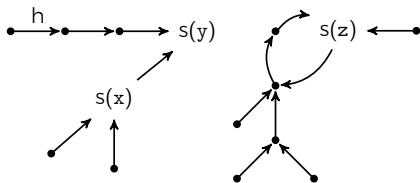
Separation Logic is interpreted over memory states  $(s, h)$  where:

■ store,  $s : \text{VAR} \rightarrow \text{LOC}$

■ heap,  $h : \text{LOC} \rightarrow_{\text{fin}} \text{LOC}$

where  $\text{VAR} = \{x, y, z, \dots\}$  set of (program) variables,

$\text{LOC}$  set of locations.  $\text{VAR}$  and  $\text{LOC}$  are countably infinite sets.



here,  $h(s(x)) = s(y)$

■ Disjoint heaps:  $\text{dom}(h_1) \cap \text{dom}(h_2) = \emptyset$

■ Sum of disjoint heaps  $(h_1 + h_2)$   
is defined as the sum of partial functions.

# Propositional Separation Logic $\text{SL}(*, -*)$

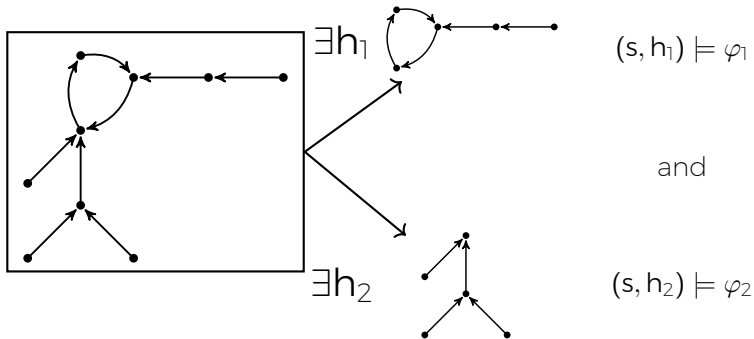
$\varphi ::= \neg\varphi \mid \varphi_1 \wedge \varphi_2 \mid \mathbf{emp} \mid \mathbf{x} = \mathbf{y} \mid \mathbf{x} \hookrightarrow \mathbf{y} \mid \varphi_1 * \varphi_2 \mid \varphi_1 -* \varphi_2$

## Semantics

- standard for  $\wedge$  and  $\neg$  ;
- $(s, h) \models \mathbf{emp} \iff \text{dom}(h) = \emptyset$
- $(s, h) \models \mathbf{x} = \mathbf{y} \iff s(\mathbf{x}) = s(\mathbf{y})$
- $(s, h) \models \mathbf{x} \hookrightarrow \mathbf{y} \iff h(s(\mathbf{x})) = s(\mathbf{y})$

# Separating conjunction (\*)

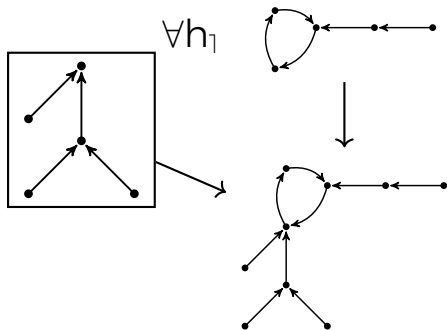
$(s, h) \models \varphi_1 * \varphi_2$  if and only if



There is a way to split the heap into two so that, together with the store, one part satisfies  $\varphi_1$  and the other satisfies  $\varphi_2$ .

# Separating implication ( $\rightarrow^*$ )

$(s, h) \models \varphi_1 \rightarrow^* \varphi_2$  if and only if



$$\text{dom}(h) \cap \text{dom}(h_1) = \emptyset$$
$$(s, h_1) \models \varphi_1$$



$$(s, h + h_1) \models \varphi_2$$

Whenever a (disjoint) heap that, together with the store, satisfies  $\varphi_1$  is added, the resulting memory state satisfies  $\varphi_2$ .

# Decision Problems

- Hoare proof-system requires to solve classical problems: satisfiability/validity/entailment

$$\frac{\varphi \Rightarrow \psi \quad \{\psi\} \text{P} \{\psi'\} \quad \psi' \Rightarrow \varphi'}{\{\varphi\} \text{P} \{\varphi'\}} \text{consequence rule}$$

- satisfiability is PSPACE-complete for  $\text{SL}(*, -*)$

**Note:** entailment and validity reduce to satisfiability for  $\text{SL}(*, -*)$ .



# Robustness properties

- Acyclicity holds for  $\varphi$  iff every model of  $\varphi$  is acyclic
- Garbage freedom holds for  $\varphi$  iff in every model of  $\varphi$ , each  $\ell \in \text{dom}(h)$  is reachable from a program variable of  $\varphi$

## C. Jansen et al., ESOP'17

Checking for robustness properties is EXPTIME-complete for Symbolic Heaps with Inductive Predicates.

- Symbolic Heaps  $\implies$  no  $*$ , no  $\wedge$  and  $\neg$  inside  $*$
- Inductive Predicates  $\sim$  Horn clauses where  $*$  replaces  $\wedge$

$$P(\vec{x}) \Leftarrow \exists \vec{z} Q_1 \overset{*}{\wedge} \dots \overset{*}{\wedge} Q_n \quad \text{fv}(Q_i) \subseteq \vec{x}, \vec{z}$$

### Our Goal

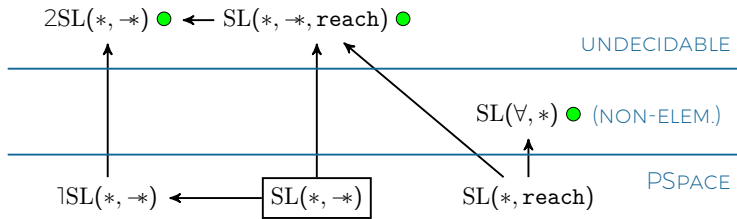
Provide similar results for **propositional** separation logic.

# Desiderata

We aim to an extension of propositional separation logic where

- satisfiability is decidable in PSPACE (as  $SL(*, -*)$ )
- robustness properties reduce to one of these problems

## Known extensions



## SL(\*, -\*) + reachability and 1 quantified variable

- $(s, h) \models \text{reach}^+(x, y) \iff h^L(s(x)) = s(y)$  for some  $L \geq 1$
- $(s, h) \models \exists u \varphi \iff$  there is  $\ell \in \text{LOC}$  s.t.  $(s[u \leftarrow \ell], h) \models \varphi$

It is only possible to quantify over the variable name  $u$ .

## Robustness properties reduce to entailment

- Acyclicity:  $\varphi \models \neg \exists u \text{reach}^+(u, u)$
- Garbage freedom:  $\varphi \models \forall u (\text{alloc}(u) \Rightarrow \bigvee_{x \in \text{fv}(\varphi)} \text{reach}(x, u))$

where  $u \notin \text{fv}(\varphi)$  and

- $\text{alloc}(x) \stackrel{\text{def}}{=} (x \leftrightarrow x) \text{ -* } \perp$
- $\text{reach}(x, y) \stackrel{\text{def}}{=} x = y \vee \text{reach}^+(x, y)$

# Restrictions

The logic  $\text{ISL}(*, \rightarrow, \text{reach}^+)$  is undecidable. We syntactically restrict the logic so that for each occurrence of  $\text{reach}^+(\mathbf{x}, \mathbf{y})$ :

**R1** it is not on the right side of its first  $\rightarrow$  ancestor (seeing the formula as a tree)

■  $\varphi \rightarrow (\psi * \text{reach}^+(\mathbf{u}, \mathbf{u}))$  violates **R1**

**R2** if  $\mathbf{x} = \mathbf{u}$  then  $\mathbf{y} = \mathbf{u}$  (syntactically)

■  $\text{reach}^+(\mathbf{u}, \mathbf{x})$  violates **R2**

**Note:** robustness properties formulae are still expressible.

# Results

- 1  $1SL_{R1}(*, -*, \mathbf{reach}^+)$ : satisfiability is NON-ELEMENTARY (more precisely, TOWER-hard)
- 2  $1SL_{R1}^{R2}(*, -*, \mathbf{reach}^+)$ : satisfiability is PSPACE-complete

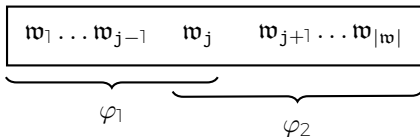
## Proof Techniques

- (1) reduce *Propositional interval temporal logic under locality principle (PITL)* to a logic captured by  $1SL_{R1}(*, -*, \mathbf{reach}^+)$
- (2) extend the *test formulae technique* used for  $SL(*, -*)$

# PITL (Moszkowski'83)

$$\varphi ::= \mathbf{pt} \mid \mathbf{a} \mid \varphi_1 \mid \varphi_2 \mid \neg \varphi \mid \varphi_1 \wedge \varphi_2$$

- interpreted on finite non-empty words over a finite alphabet  $\Sigma$
- $\mathbf{w} \models \mathbf{pt} \iff |\mathbf{w}| = 1$
- $\mathbf{w} \models \mathbf{a} \iff$  first letter of  $\mathbf{w}$  is  $\mathbf{a} \in \Sigma$  (locality principle)
- $\mathbf{w} \models \varphi_1 \mid \varphi_2 \iff \mathbf{w}[1 : j] \models \varphi_1$  and  $\mathbf{w}[j : |\mathbf{w}|] \models \varphi_2$   
for some  $j \in [1, |\mathbf{w}|]$



- Satisfiability is decidable, but NON-ELEMENTARY

# Auxiliary Logic on Trees (ALT)

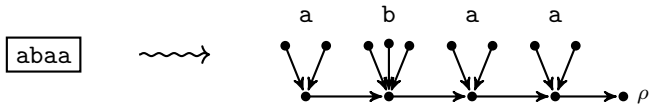
$$\varphi := \varphi_1 \wedge \varphi_2 \mid \neg \varphi \mid \varphi_1 * \varphi_2 \mid \exists \mathbf{u} \varphi \mid \mathbf{T}(\mathbf{u}) \mid \mathbf{G}(\mathbf{u})$$

- interpreted on acyclic memory states (set of rooted trees)
- one special tree, rooted in  $\rho \in \mathbf{LOC}$
- $\exists \mathbf{u} \varphi$  and  $\varphi_1 * \varphi_2$  as before
- $(s, h) \models_{\rho} \mathbf{T}(\mathbf{u})$  iff  $s(\mathbf{u}) \in \text{dom}(h)$  and it does reach  $\rho$
- $(s, h) \models_{\rho} \mathbf{G}(\mathbf{u})$  iff  $s(\mathbf{u}) \in \text{dom}(h)$  and it does not reach  $\rho$

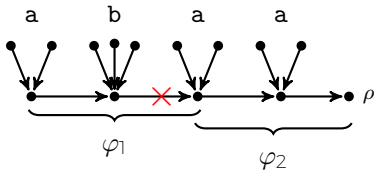
Note: ALT is captured by  $1\text{SL}_{\text{R1}}(*, \neg*, \text{reach}^+)$ .

# Reducing PITL to ALT

- Easy to encode words as acyclic memory states



- Set of models encoding words can be characterised in ALT
- However, difficult to translate  $\varphi_1 \parallel \varphi_2$ :  
cannot express properties about the trees not rooted in  $\rho$ ,  
apart from their size

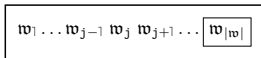


After the **cut**, left side does not reach  $\rho$  anymore.



# PITL to ALT: alternative semantics for PITL

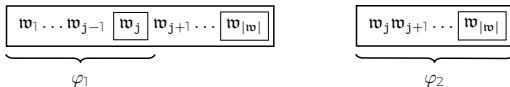
- $\boxed{a}$  marked representation of  $a \in \Sigma$



- $\varphi | \psi$  on standard semantics:



- $\varphi | \psi$  on marked semantics (can be simulated in ALT)



1 ALT and  $1SL_{R1}(*, *, \mathbf{reach}^+)$  are NON-ELEMENTARY

2 ALT is decidable in TOWER, as it is captured by  $SL(\forall, *)$

$1SL_{R1}^{R2}(*, -*, reach^+)$  is in PSPACE

~~$1SL_{R1}^{R2}(*, *, reach^+)$  is in PSPACE~~

Test Formulae “technique”

# Test Formulae example on $SL(*, -*)$

$\varphi := \neg\varphi \mid \varphi_1 \wedge \varphi_2 \mid \mathbf{emp} \mid \mathbf{x} = \mathbf{y} \mid \mathbf{x} \hookrightarrow \mathbf{y} \mid \varphi_1 * \varphi_2 \mid \varphi_1 -* \varphi_2$

- 1 Design an equivalence relation on models, based on the satisfaction of atomic predicates (test formulae), e.g.

$\mathbf{x} = \mathbf{y} \quad \mathbf{x} \hookrightarrow \mathbf{y} \quad \mathbf{alloc}(\mathbf{x}) \quad \mathbf{size} \geq \beta$

- 2 Show that any formula of our logic is equivalent to a Boolean combination of test formulae, e.g.

$(\mathbf{x} \hookrightarrow \mathbf{y}) * \neg\mathbf{emp} \iff \mathbf{x} \hookrightarrow \mathbf{y} \wedge \mathbf{size} \geq 2$

- 3 Prove small-model property for the logic of test formulae.

# 1: Designing Test Formulae

- Fix  $\alpha \in \mathbb{N}^+$ ,  $\mathbf{X} \subseteq_{\text{fin}} \text{VAR}$
- Let us define  $\text{Test}(\mathbf{X}, \alpha)$  as the finite set of predicates:  
 $\{\mathbf{x} = \mathbf{y}, \mathbf{x} \hookrightarrow \mathbf{y}, \text{alloc}(\mathbf{x}), \text{size} \geq \beta \mid \beta \in [1, \alpha], \mathbf{x}, \mathbf{y} \in \mathbf{X}\}$

Indistinguishability relation  $(s, h) \approx_{\alpha}^{\mathbf{X}} (s', h')$

for every  $\varphi \in \text{Test}(\mathbf{X}, \alpha)$ ,  $(s, h) \models \varphi$  iff  $(s', h') \models \varphi$

**Note:**  $\alpha$  is related to the number of occurrences of  $*$  and  $\rightarrow^*$  in a formula of separation logic.

## 2: \* elimination Lemma

We want to design  $\text{Test}(\mathbf{X}, \alpha)$  so that the following results hold

- For every  $\varphi \in \text{Bool}(\text{Test}(\mathbf{X}, \alpha_1))$ ,  $\psi \in \text{Bool}(\text{Test}(\mathbf{X}, \alpha_2))$   
there is  $\gamma \in \text{Bool}(\text{Test}(\mathbf{X}, \alpha_1 + \alpha_2))$  such that

$$\varphi * \psi \iff \gamma$$

- Similar elimination result for  $\neg*$ .

Lemmata holds for

$$\text{Test}(\mathbf{X}, \alpha) = \left\{ \begin{array}{l} \mathbf{x} = \mathbf{y}, \mathbf{x} \leftrightarrow \mathbf{y} \\ \text{alloc}(\mathbf{x}), \text{size} \geq \beta \end{array} \middle| \begin{array}{l} \beta \in [1, \alpha] \\ \mathbf{x}, \mathbf{y} \in \mathbf{X} \end{array} \right\}$$

### 3: Test formulae, after $*$ and $\neg*$ elimination

Hypothesis: A family of test formulae, such that

- captures the atomic predicates of  $\text{SL}(*, \neg*)$
- satisfies the  $*$  and  $\neg*$  elimination Lemmata

Thesis: for every formulae  $\varphi$  of  $\text{SL}(*, \neg*)$ ,

- $\varphi$  is equivalent to a Boolean combination of test formulae.
- If  $\alpha \geq |\varphi|$ ,  $\mathbf{x} \supseteq v(\varphi)$  and  $(s, h) \approx_{\alpha}^{\mathbf{x}} (s', h')$  then

$$(s, h) \models \varphi \text{ iff } (s', h') \models \varphi.$$

#### Small-model property derived from $\approx_{\alpha}^{\mathbf{x}}$

- Small-model property for Boolean combination of test formulae carries over to  $\text{SL}(*, \neg*)$ .
- test formulae in PSPACE  $\implies$   $\text{SL}(*, \neg*)$  is in PSPACE.

# $1SL_{R1}^{R2}(*, -*, reach^+)$ is in PSPACE

$\pi := x = y \mid x \hookrightarrow y \mid \mathbf{emp} \mid \underline{\mathcal{A} -* \mathcal{C}} \text{ (R1)}$

$\mathcal{C} := \pi \mid \mathcal{C} \wedge \mathcal{C} \mid \neg \mathcal{C} \mid \exists u \mathcal{C} \mid \mathcal{C} * \mathcal{C}$

$\mathcal{A} := \pi \mid \underline{reach^+(v_1, v_2)} \mid \mathcal{A} \wedge \mathcal{A} \mid \neg \mathcal{A} \mid \exists u \mathcal{A} \mid \mathcal{A} * \mathcal{A}$

where (R2) if  $v_1 = u$  then  $v_2 = u$

## Not so easy...

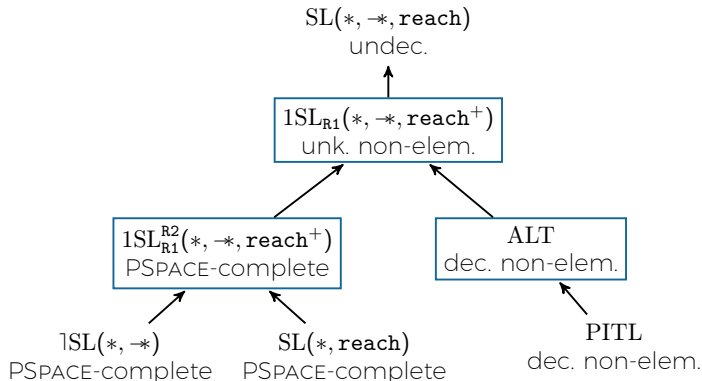
- Asymmetric  $\mathcal{A} -* \mathcal{C}$ .
  - two sets of test formulae: two  $*/\exists$  elimination Lemmata
  - $-*$  elimination Lemma that glues the two set of test formulae
- instead of “**size**  $\geq \beta$  s.t.  $\beta \in [1, \alpha]$ ”, the  $\beta$ s of new test formulae are bounded by functions on  $\alpha$ , e.g.

$$\#loop_x(\beta) \geq \gamma \quad \gamma \in [1, \frac{1}{2}\alpha(\alpha + 3) - 1]$$

bounds are found by solving a set of recurrence equations!

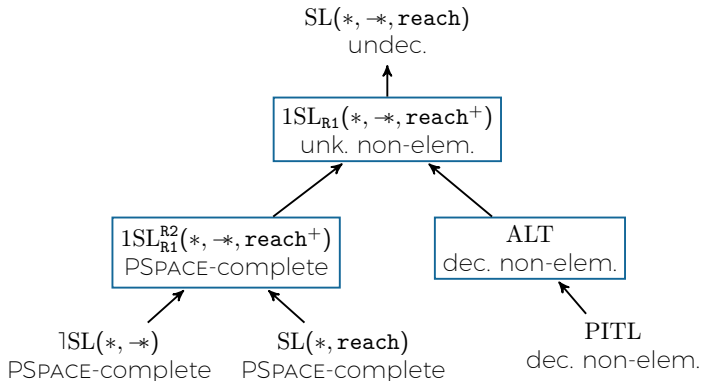


# Recap



- $1SL_{R1}^{R2}(*, -*, \text{reach}^+)$  strictly generalise other PSPACE-compl. extensions of propositional separation logic
- Can be used to check for robustness properties.

# Recap



- ALT seems to be an interesting tool for reductions, as it is a fragment or it is easily captured by many logics in TOWER e.g.  $QCTL(U)$ ,  $MSL(\diamond, \langle U \rangle, *)$ ,  $2SL(*)$