

Formal Methods for the Verification of Distributed Algorithms

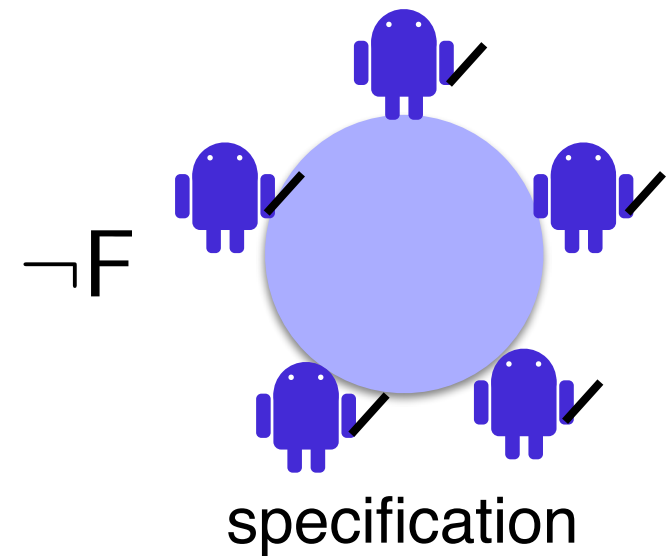
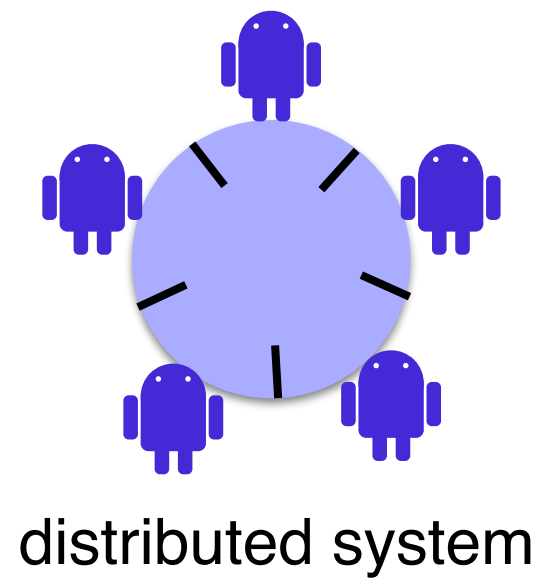
Paul Gastin

Laboratoire Spécification et Vérification
ENS Cachan, CNRS & Inria

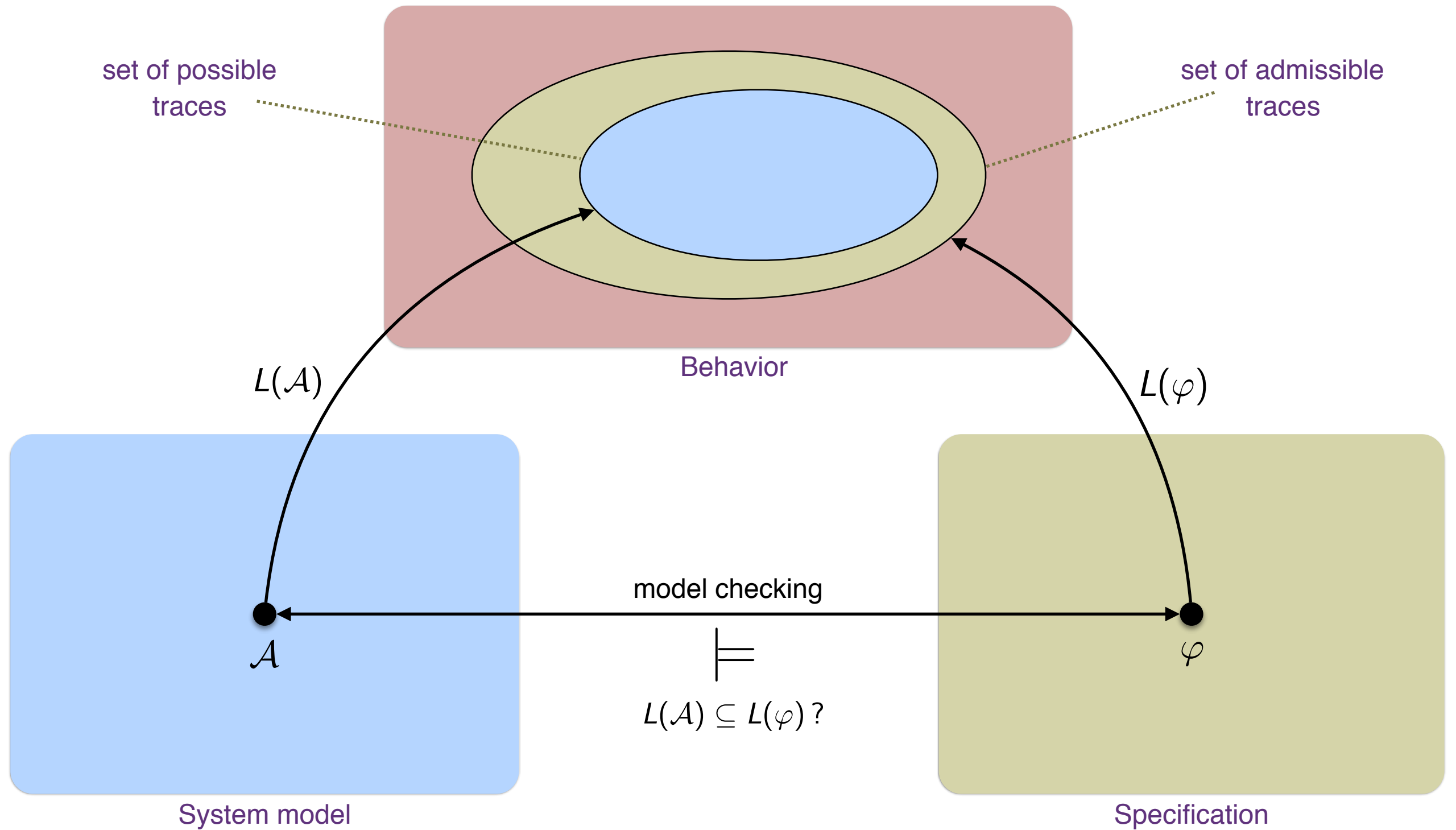
Joint work with C. Aiswarya & Benedikt Bollig

Infinity
December 15, 2015

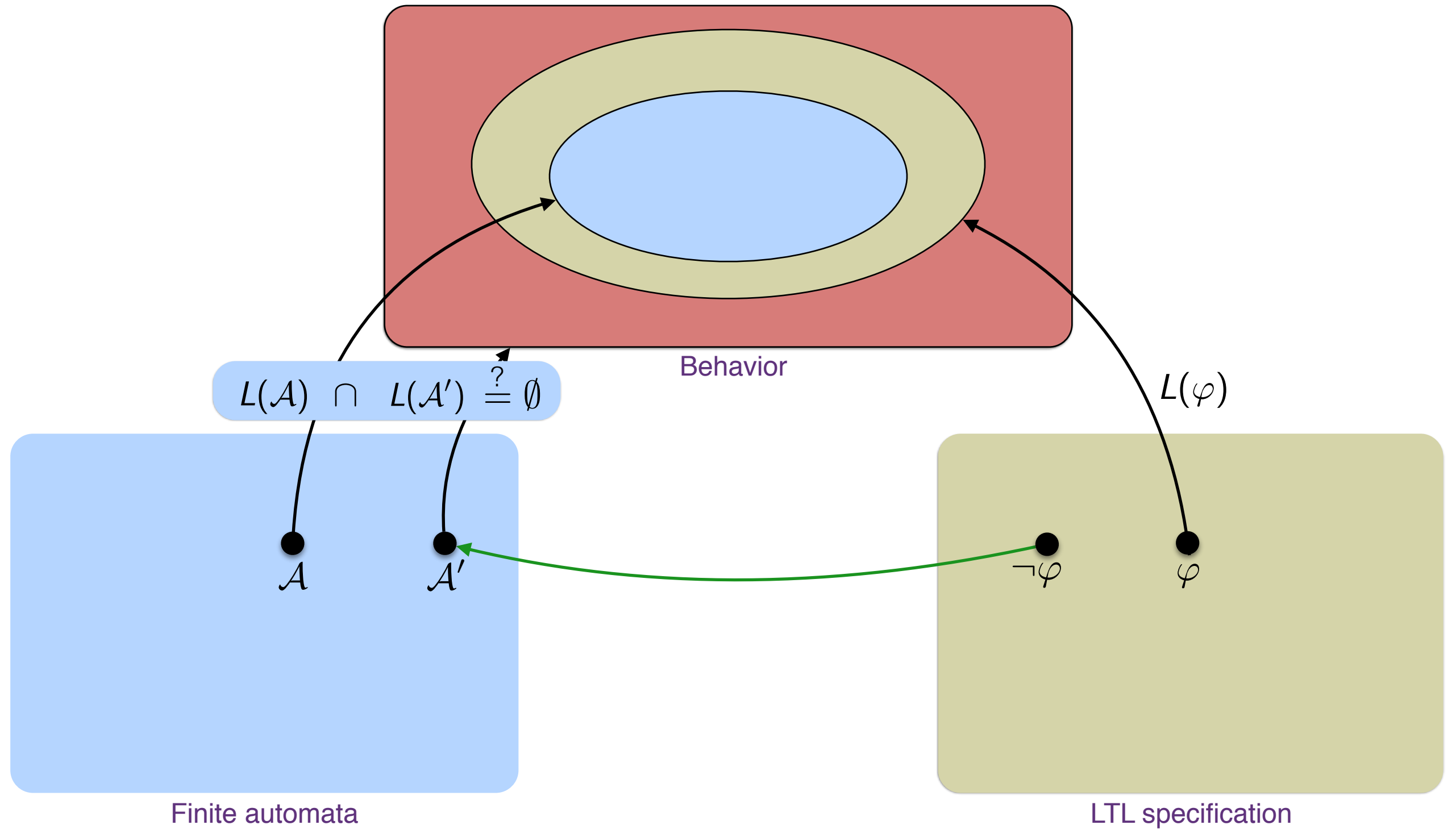
Landscape & Objectives



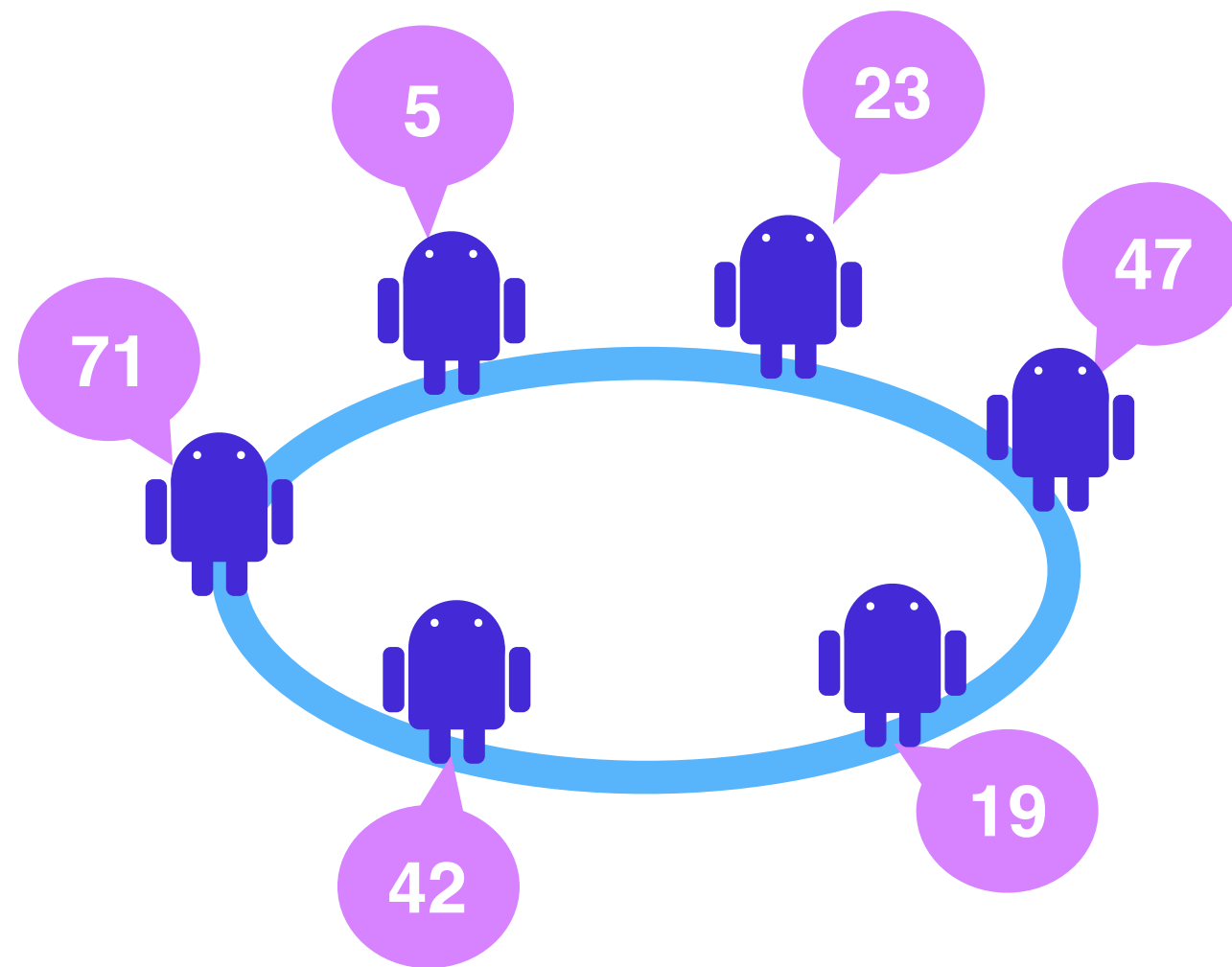
Landscape & Objectives



Landscape & Objectives



Models of Distributed Systems



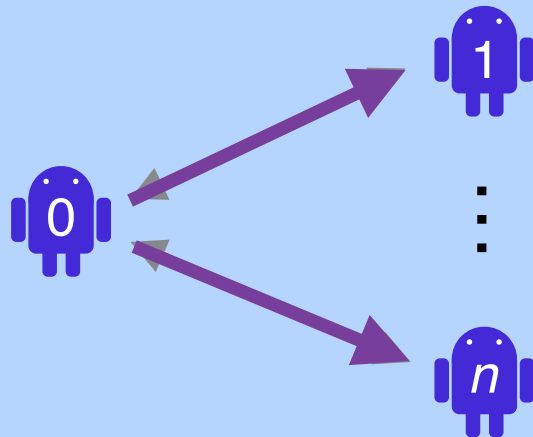
Landscape & Objectives

Topology

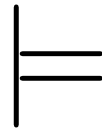
- tree, ring, star, ...

Number of processes

- fixed & static
- non-fixed & unbounded
static (parameterized)



System model



for all n

φ

Specification

Landscape & Objectives

Topology

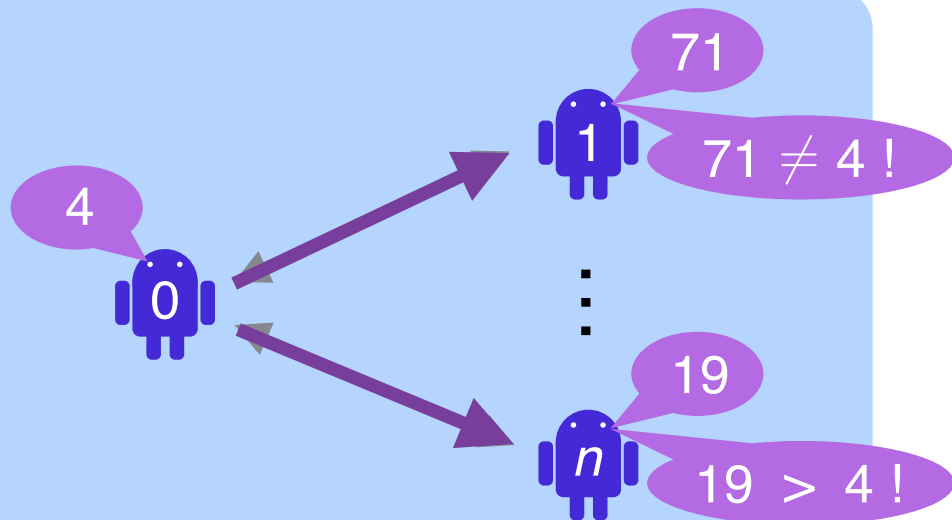
- tree, ring, star, ...

Number of processes

- fixed & static
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 - dynamic

Identification

- (partly) indistinguishable
- unique process identifiers (pids)
 - test for equality
 - test for linear order



System model

\models

φ

Specification

Landscape & Objectives

Topology

- tree, ring, star, ...

Number of processes

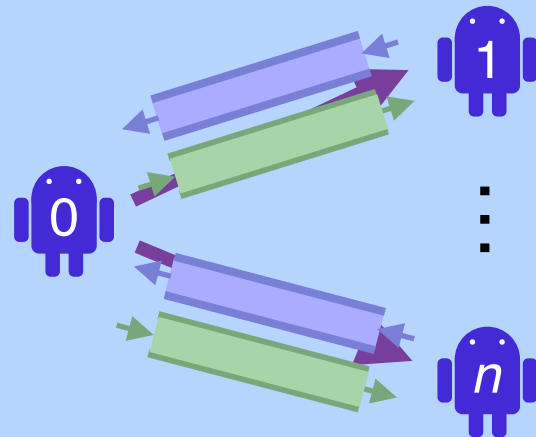
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Identification

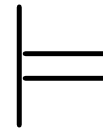
- (partly) indistinguishable
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Communication

- broadcast
- shared variable
- point-to-point
 - rendez-vous
 - FIFO queues



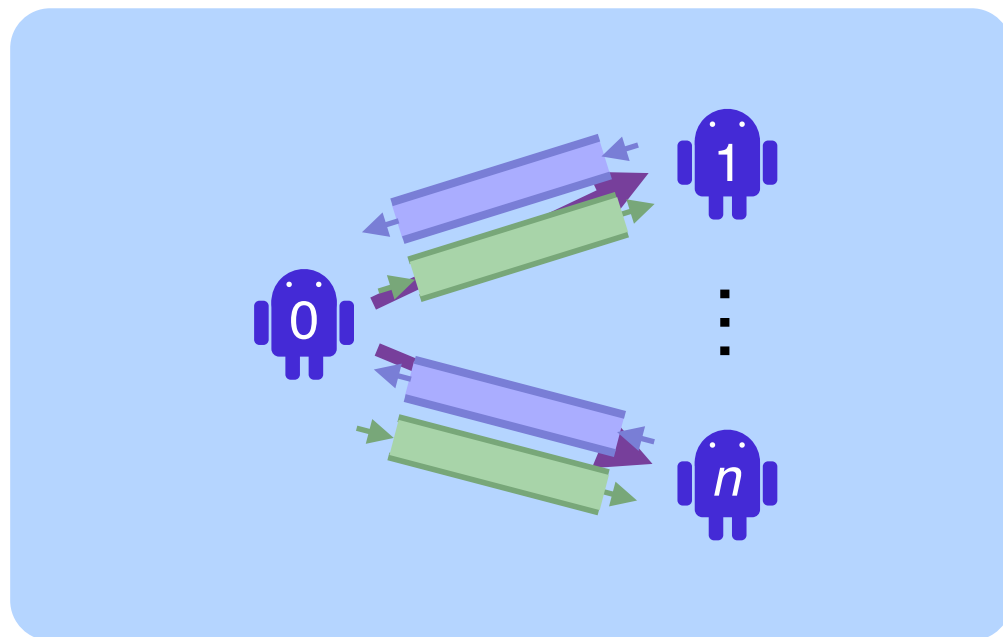
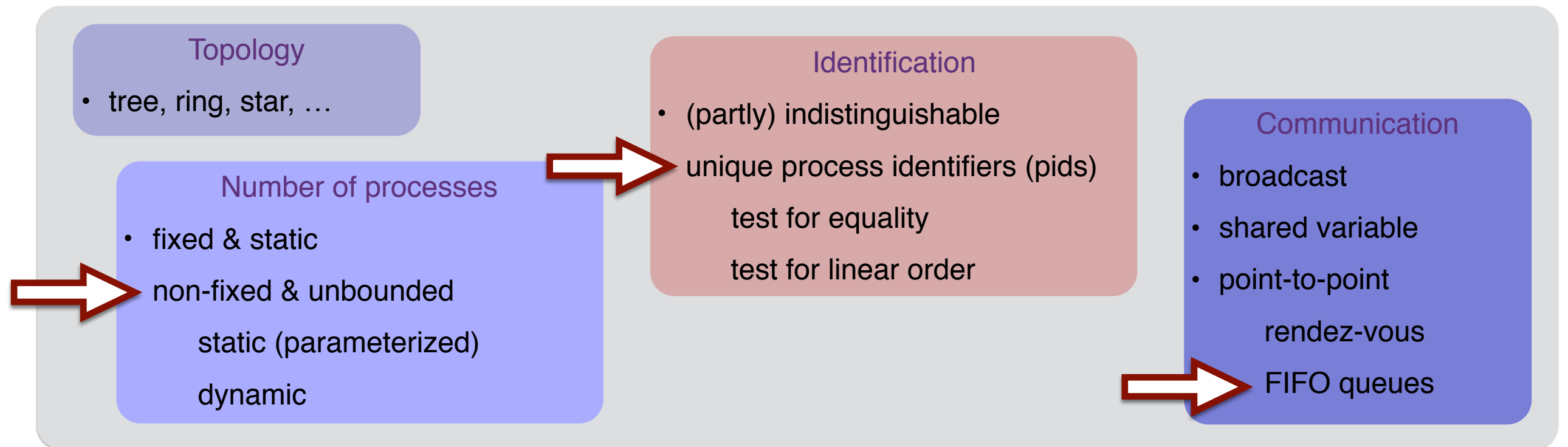
System model



φ

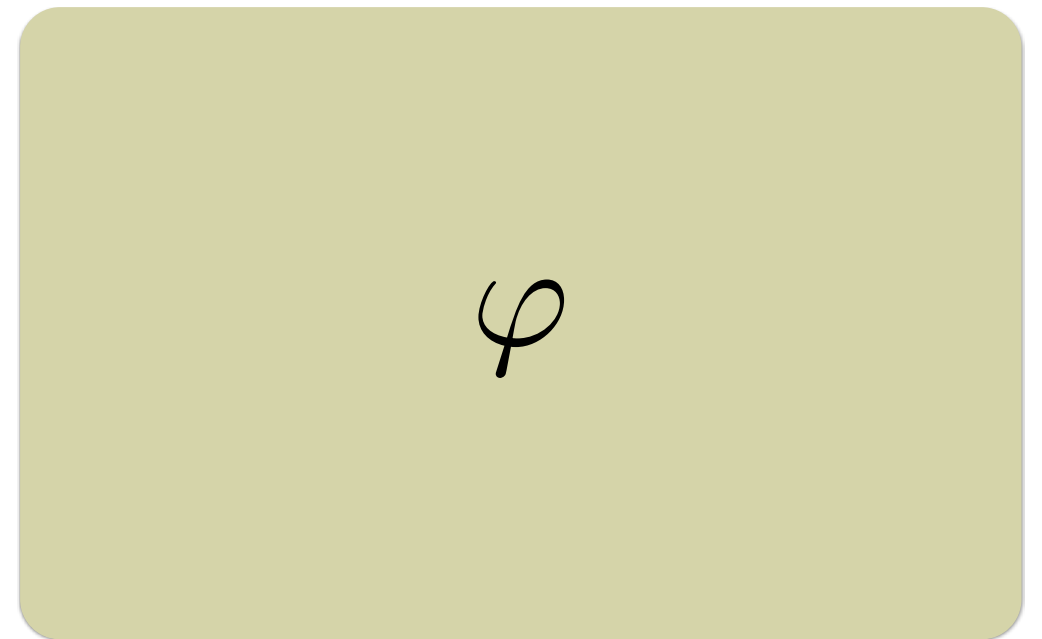
Specification

Landscape & Objectives



System model

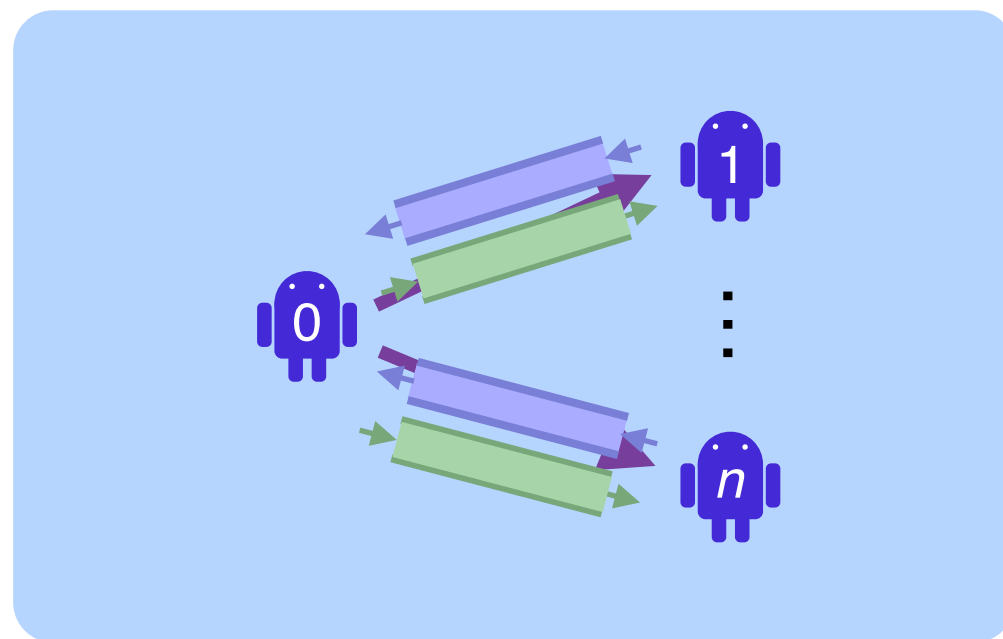
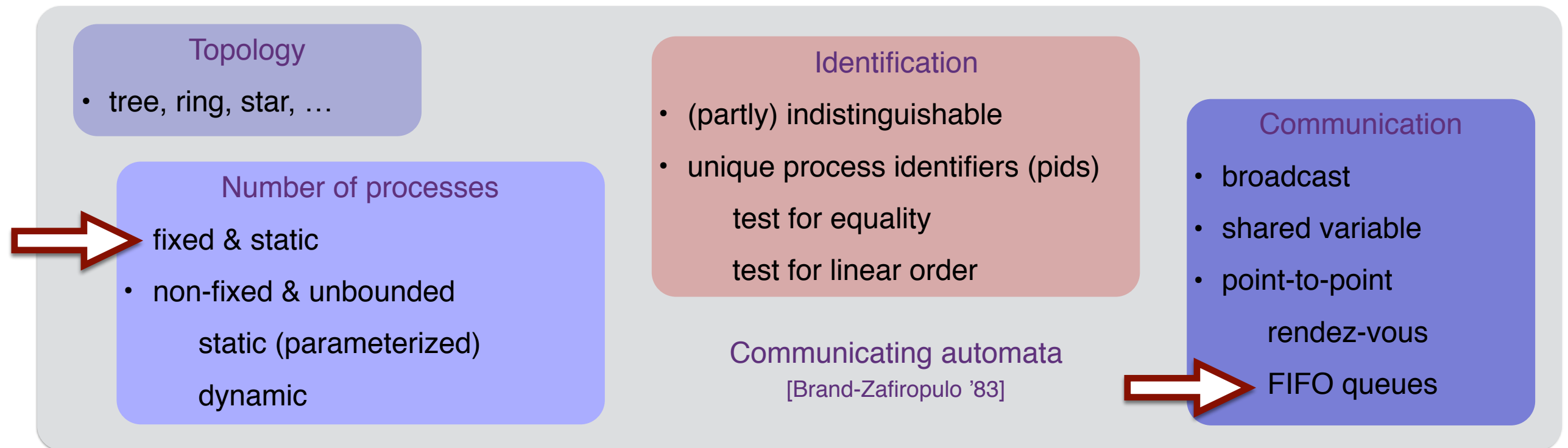
\models



Specification

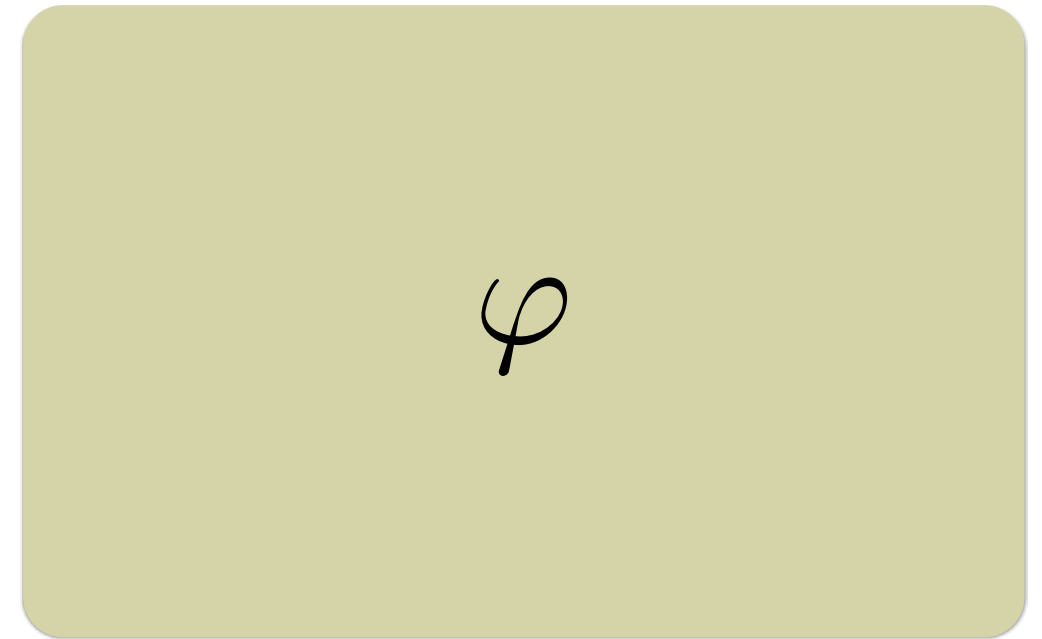
Several sources of infinity / unboundedness

Landscape & Objectives



System model

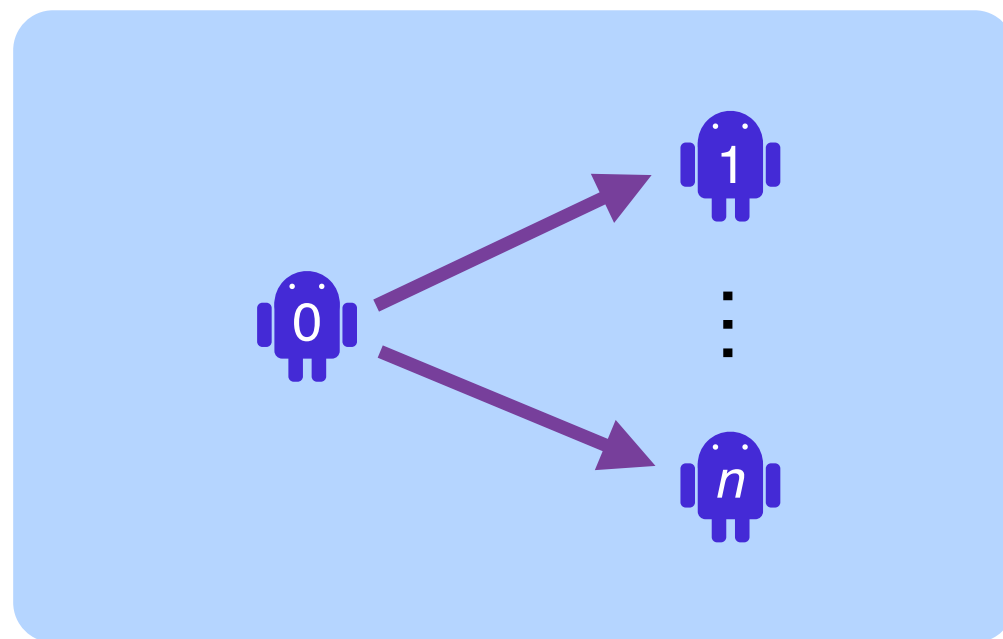
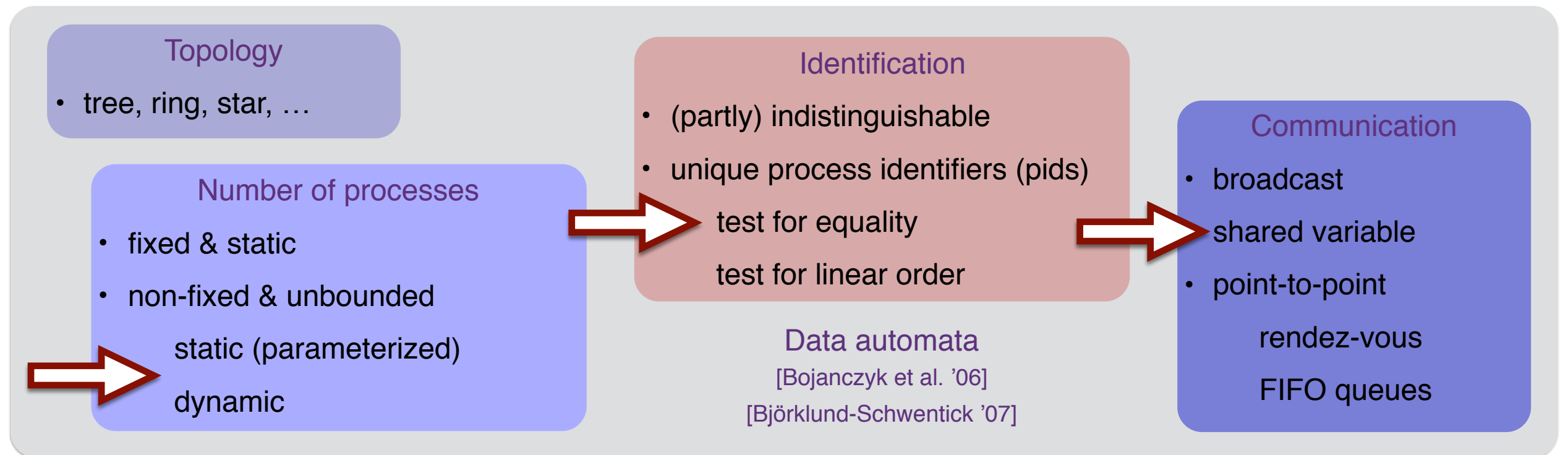
\models



Specification

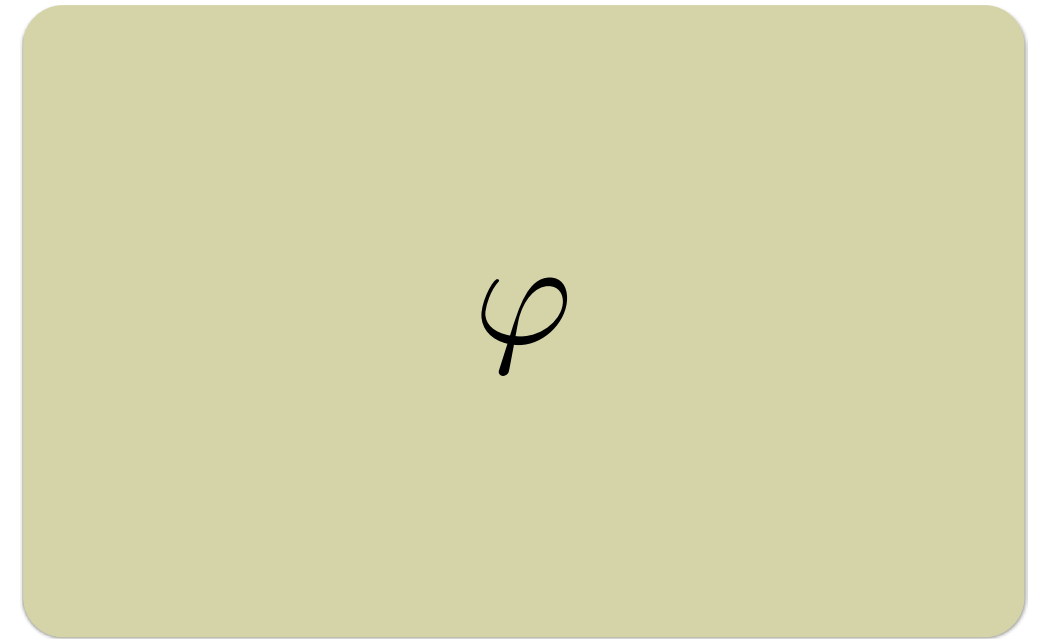
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Landscape & Objectives



System model

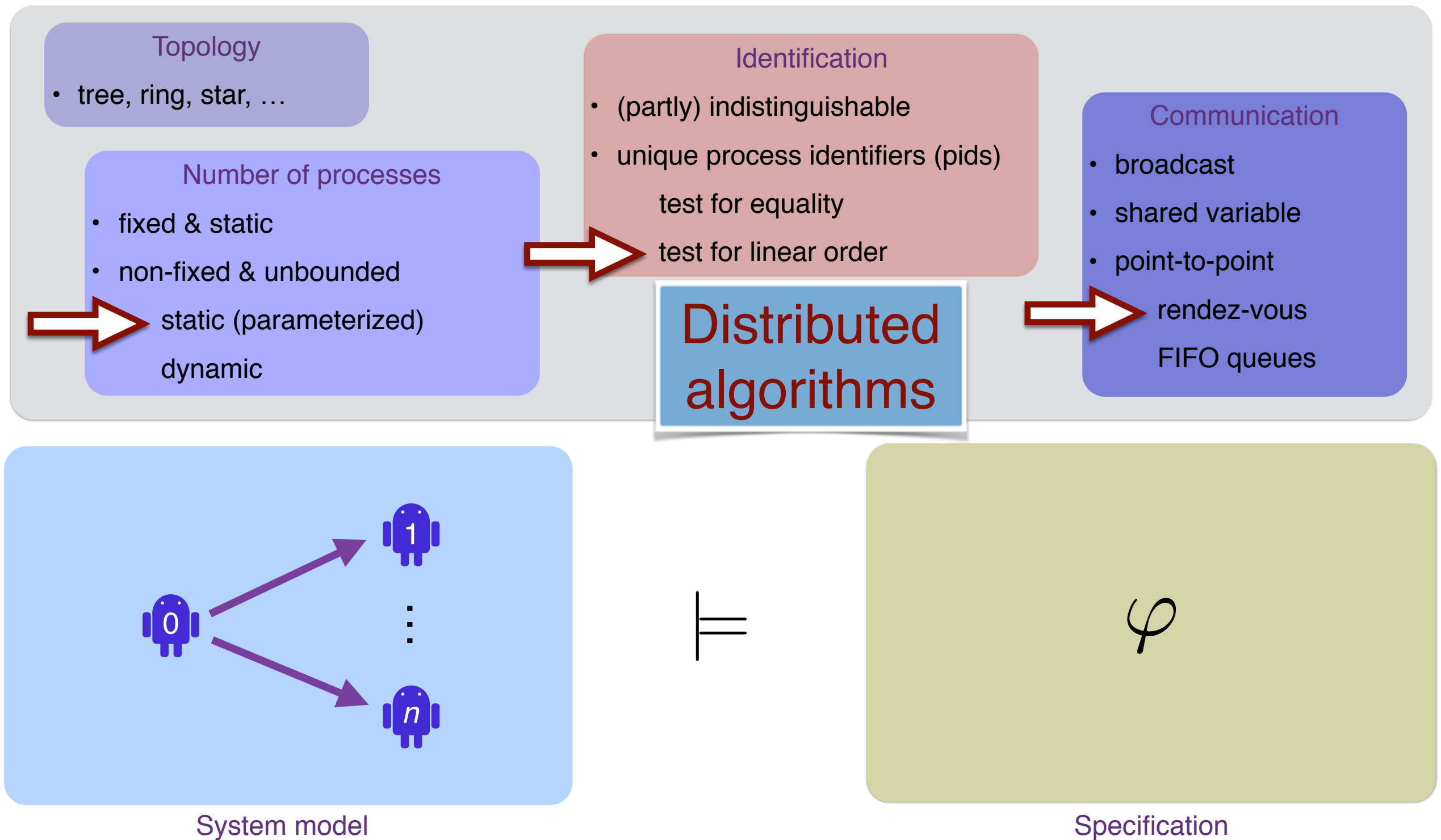
\models



Specification

Several sources of infinity / unboundedness

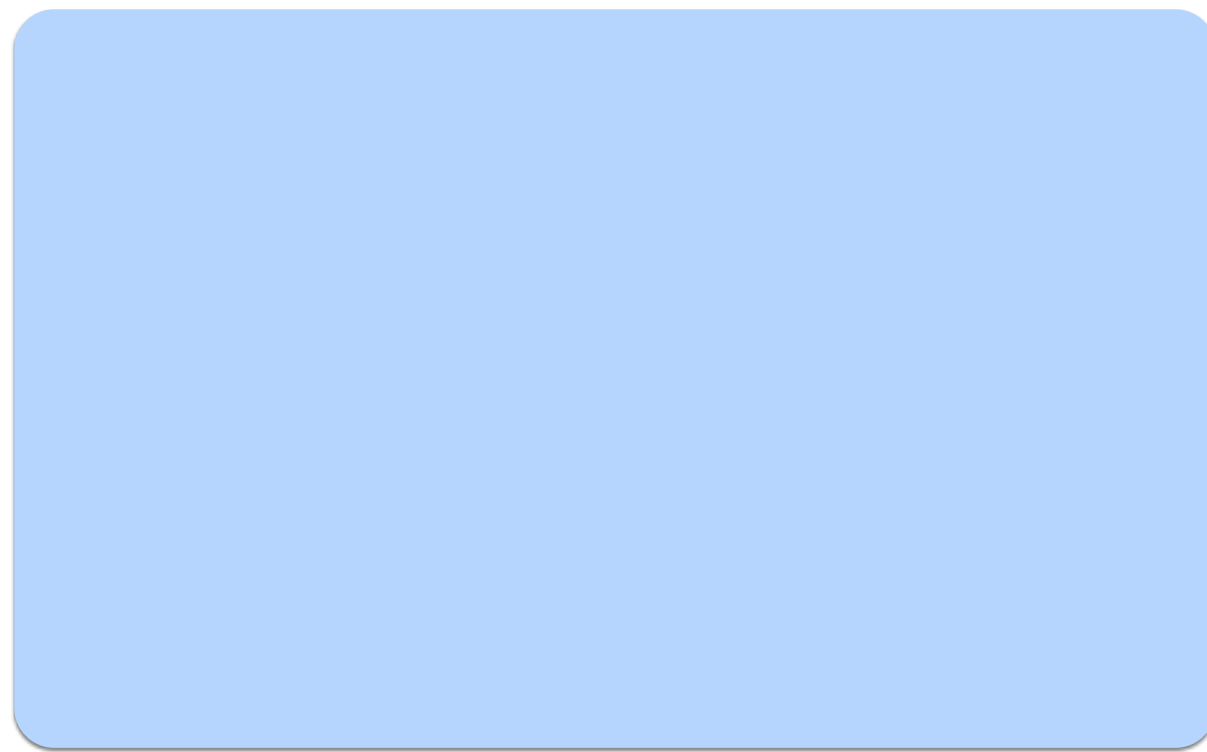
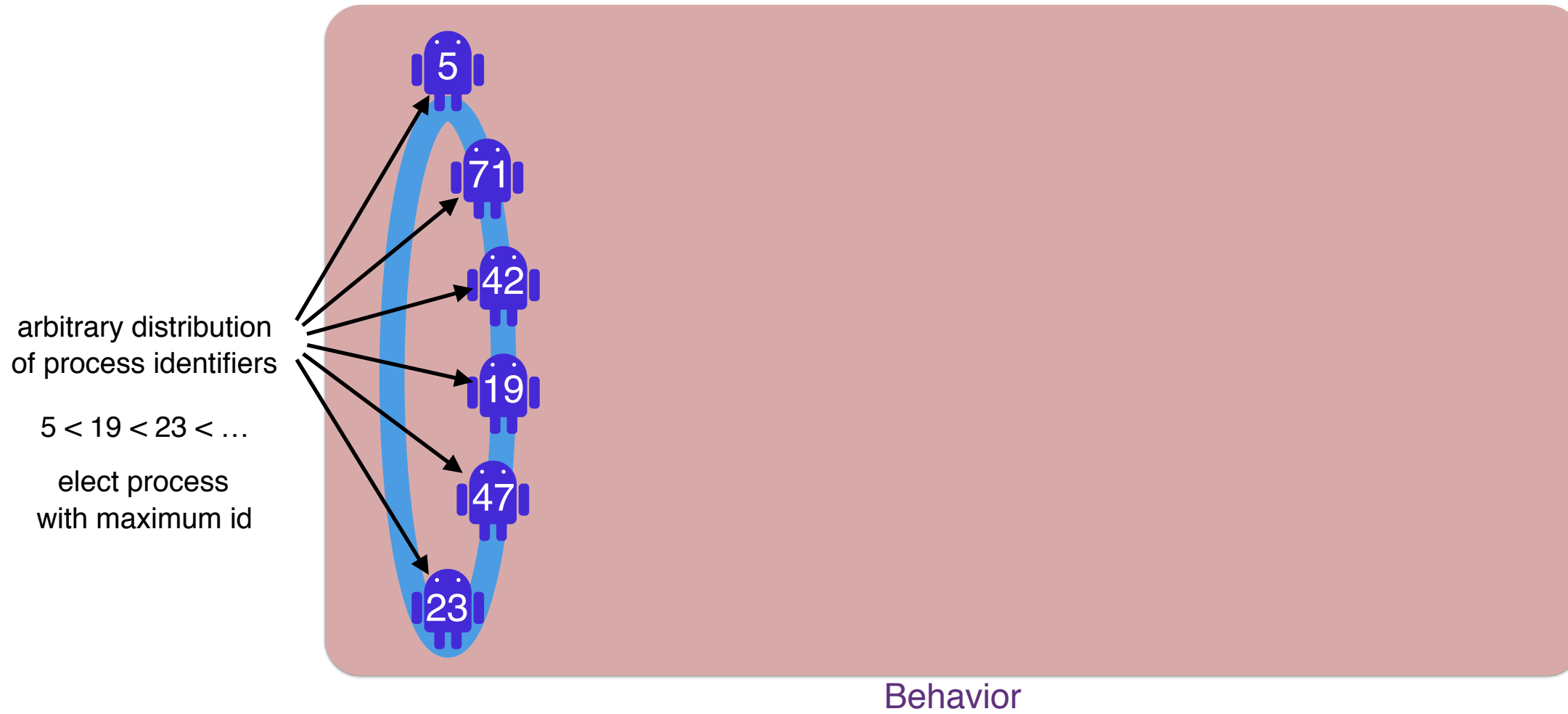
Landscape & Objectives



Several sources of infinity / unboundedness

Distributed algorithms

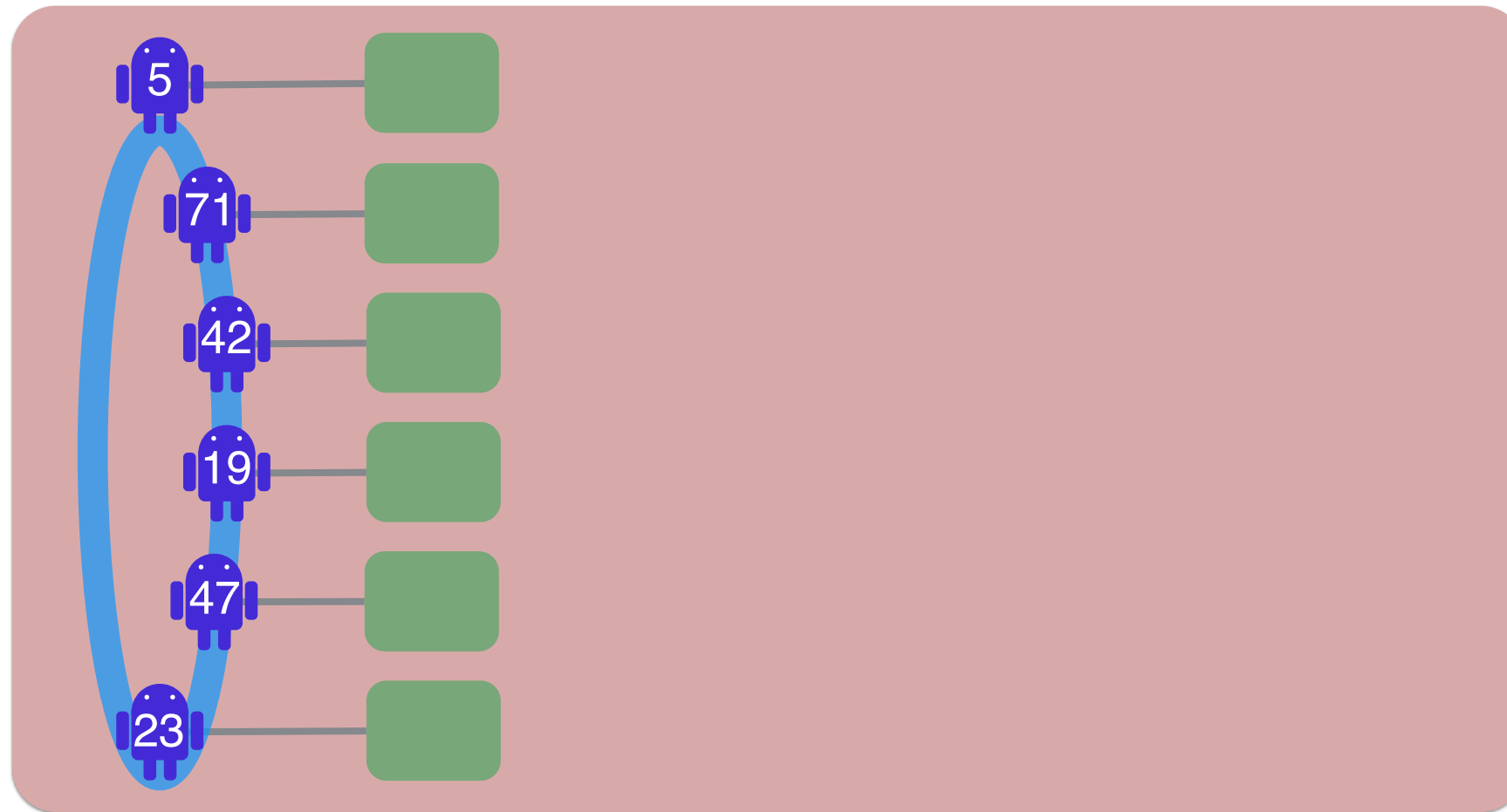
Leader election [Franklin '82]



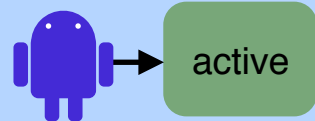
Distributed algorithm

Distributed algorithms

Leader election [Franklin '82]



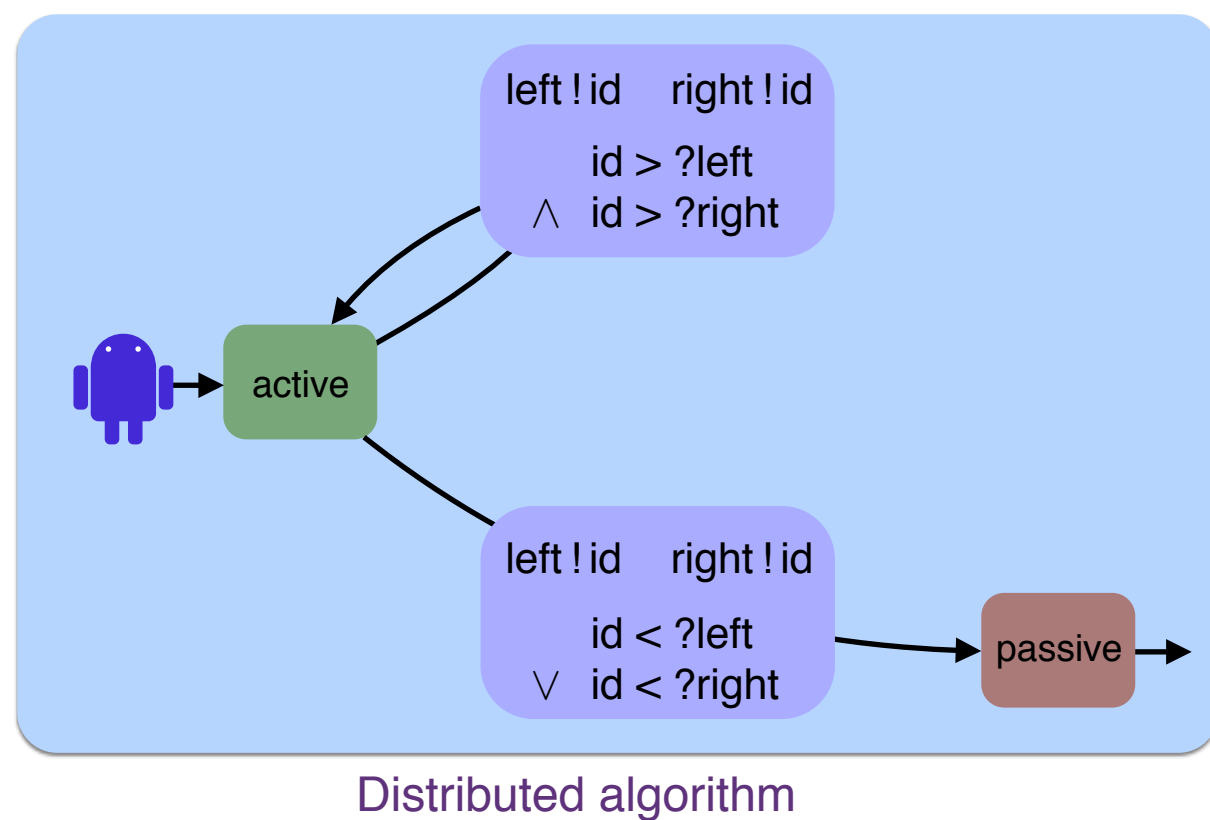
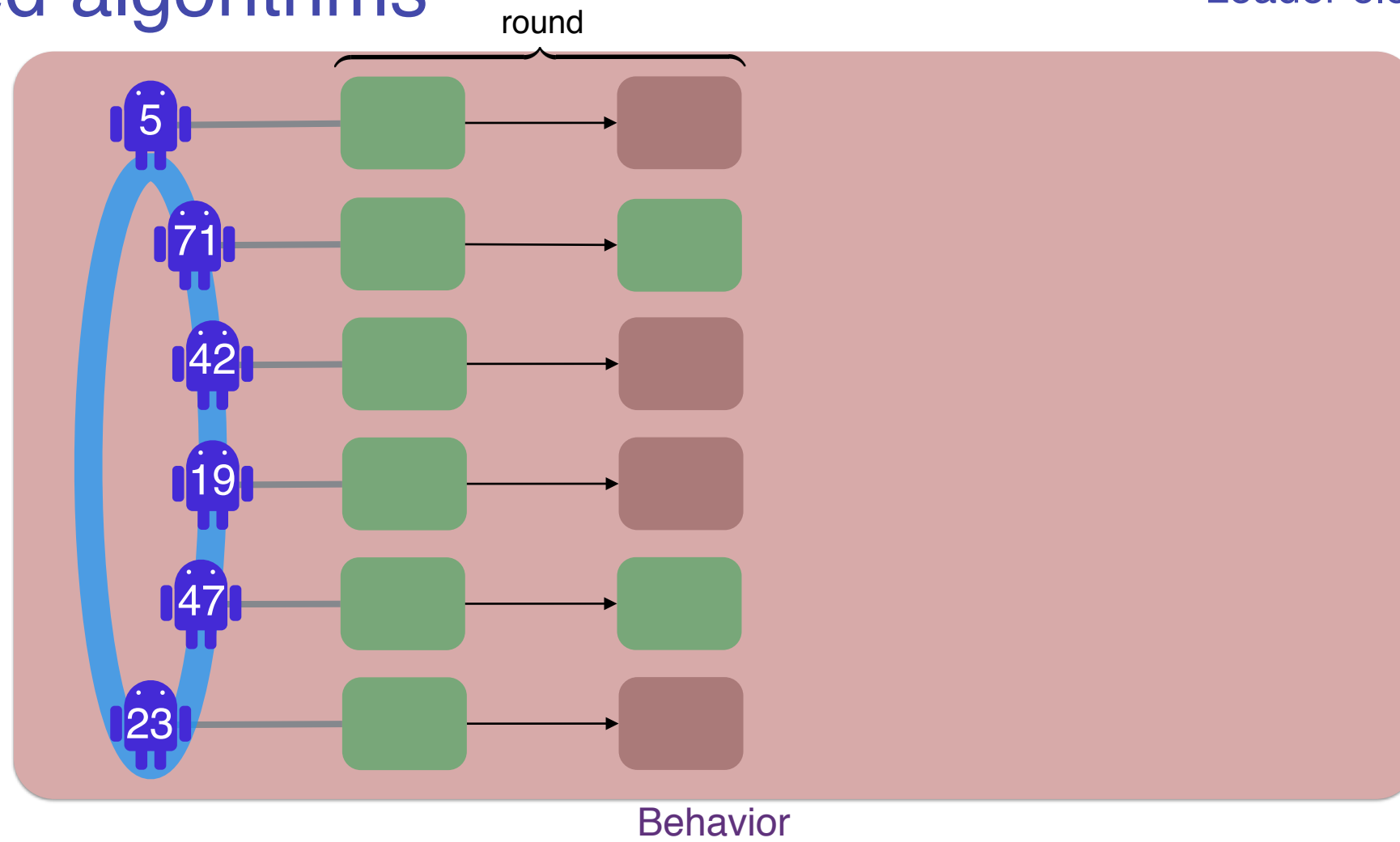
Behavior



Distributed algorithm

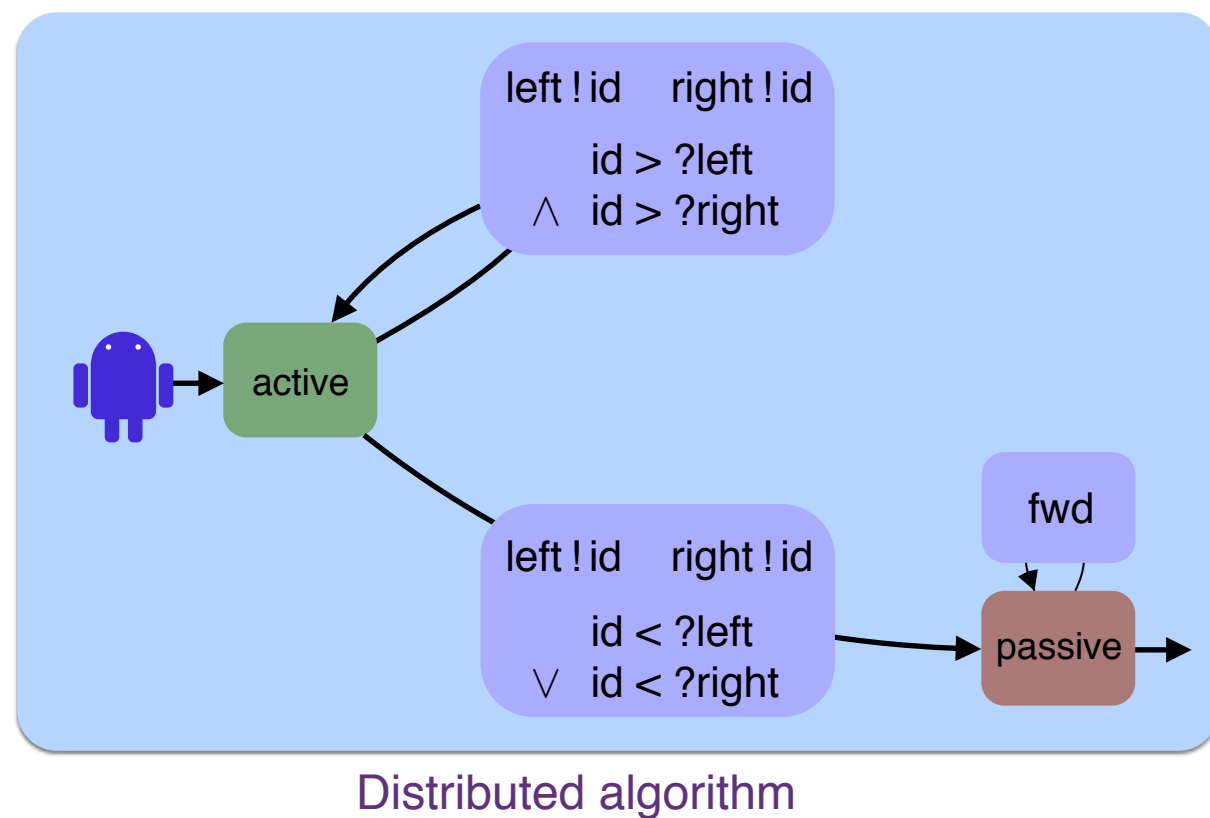
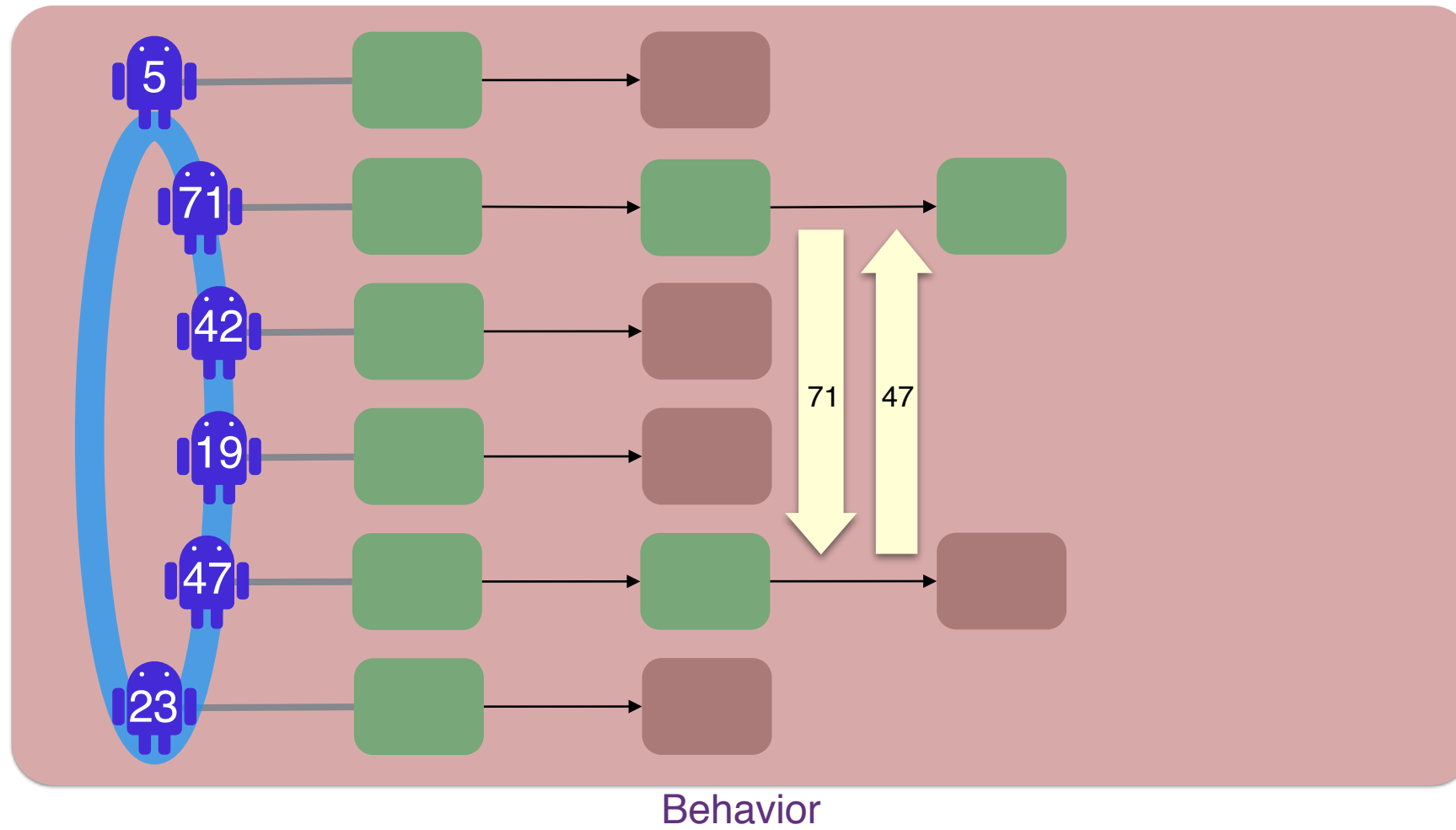
Distributed algorithms

Leader election [Franklin '82]



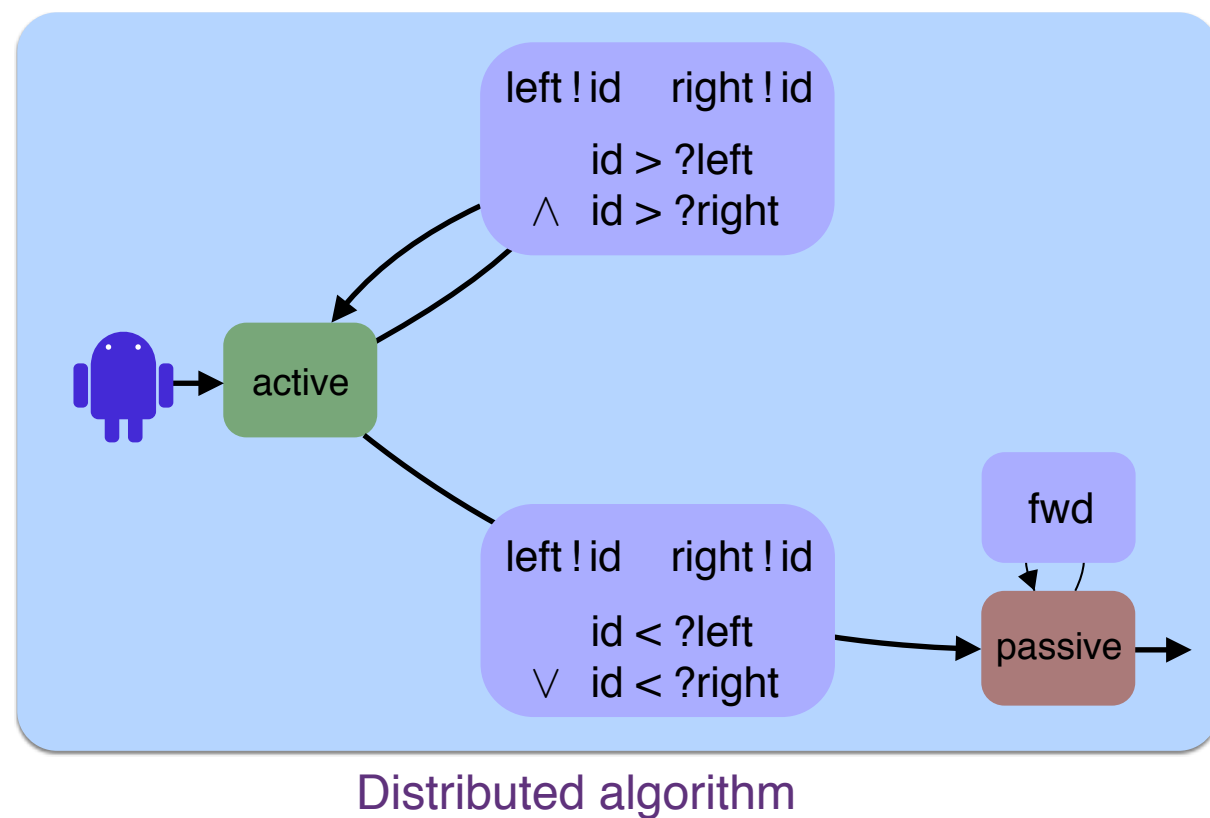
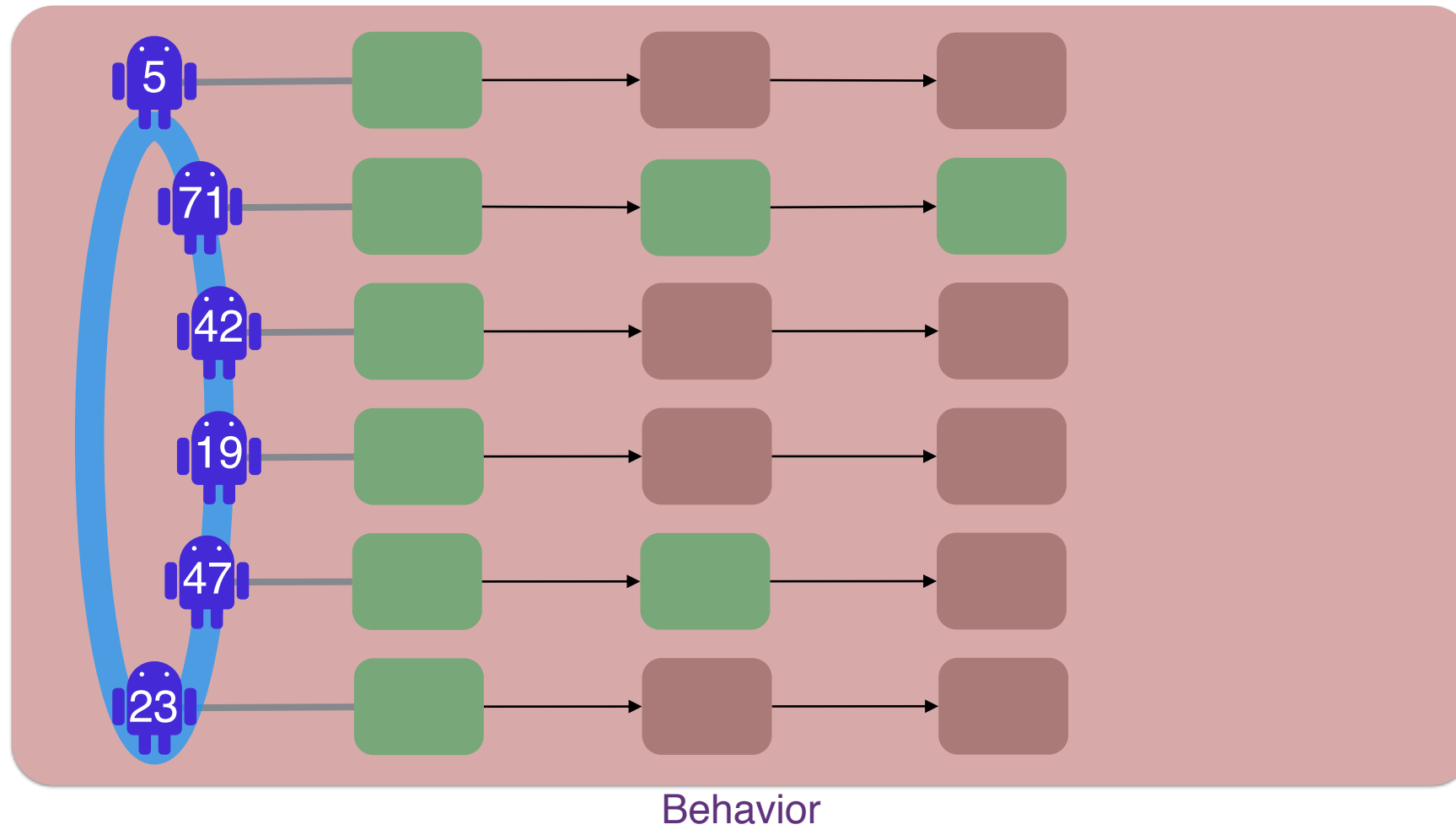
Distributed algorithms

Leader election [Franklin '82]



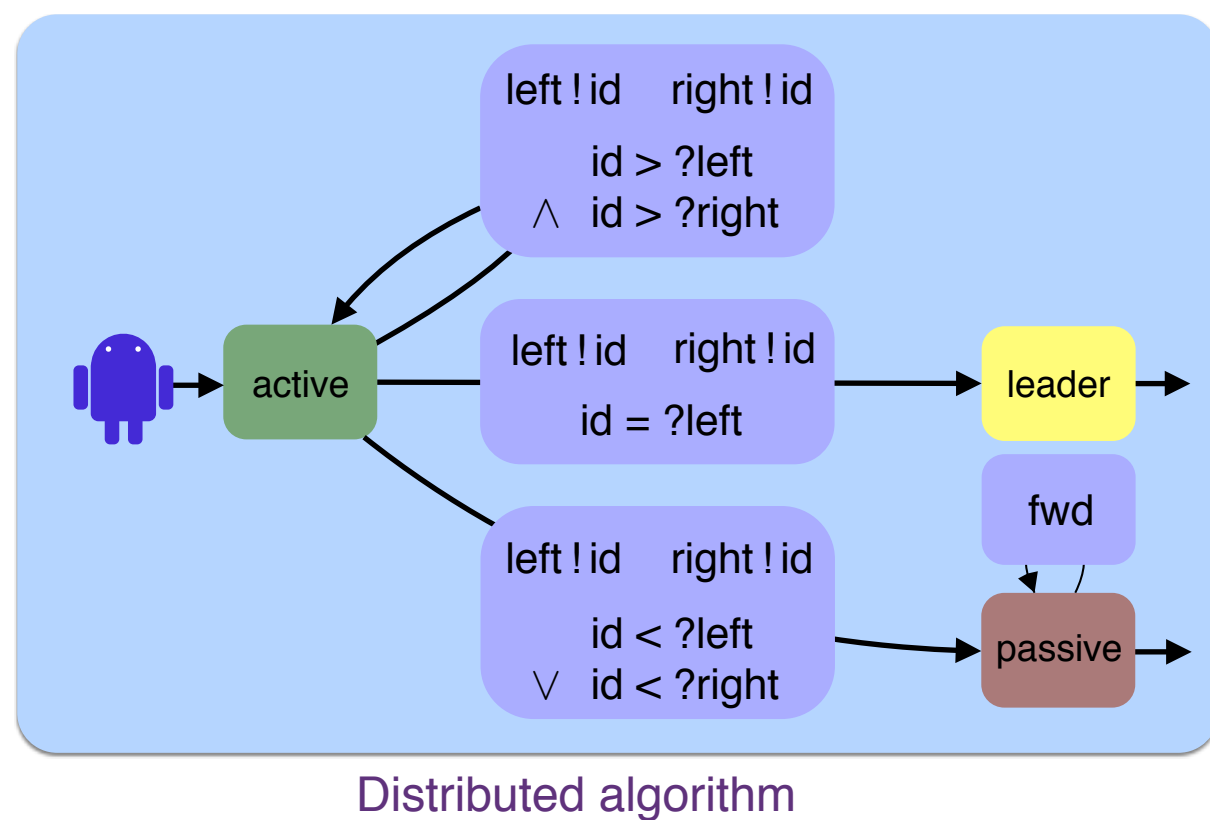
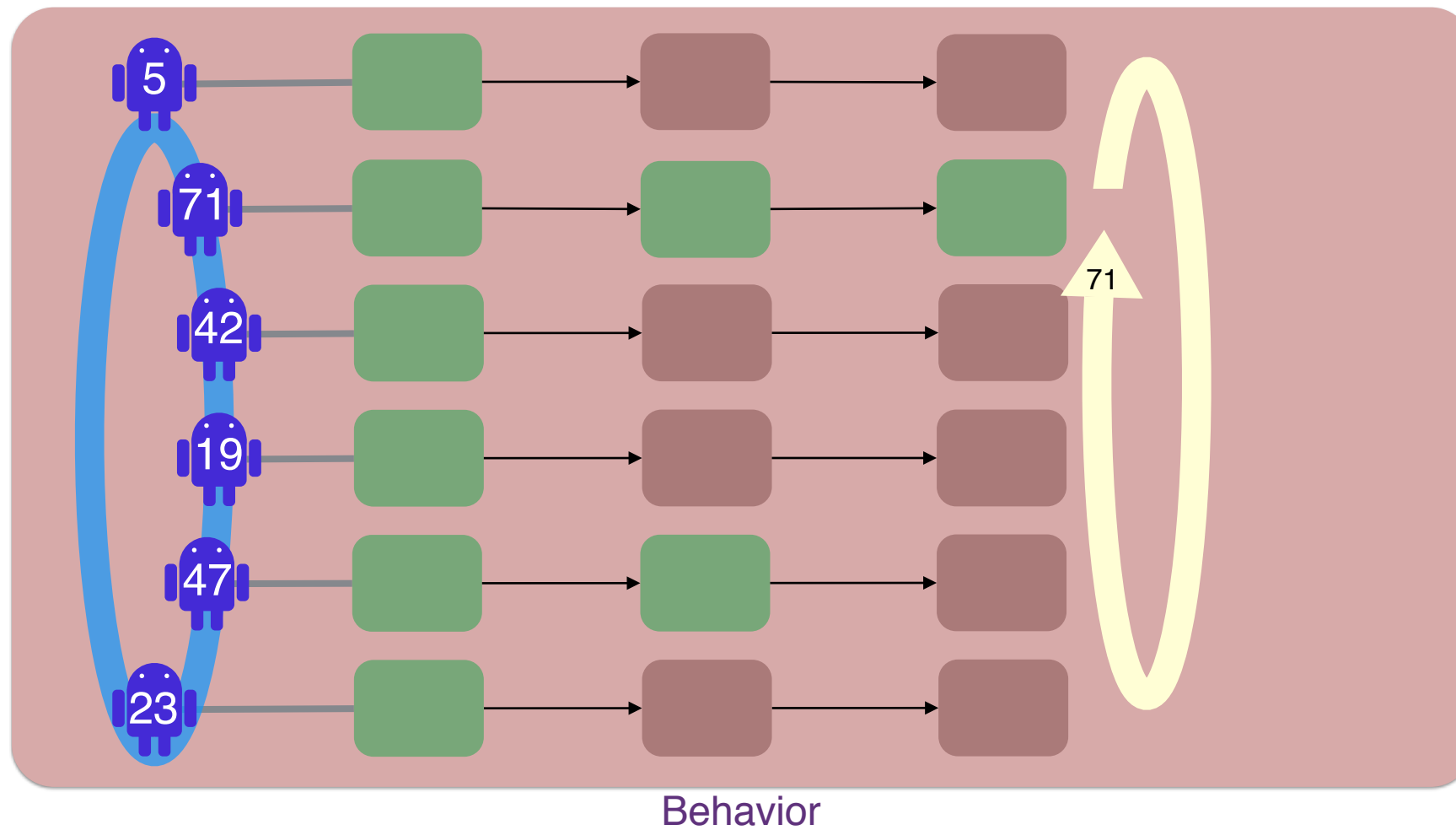
Distributed algorithms

Leader election [Franklin '82]



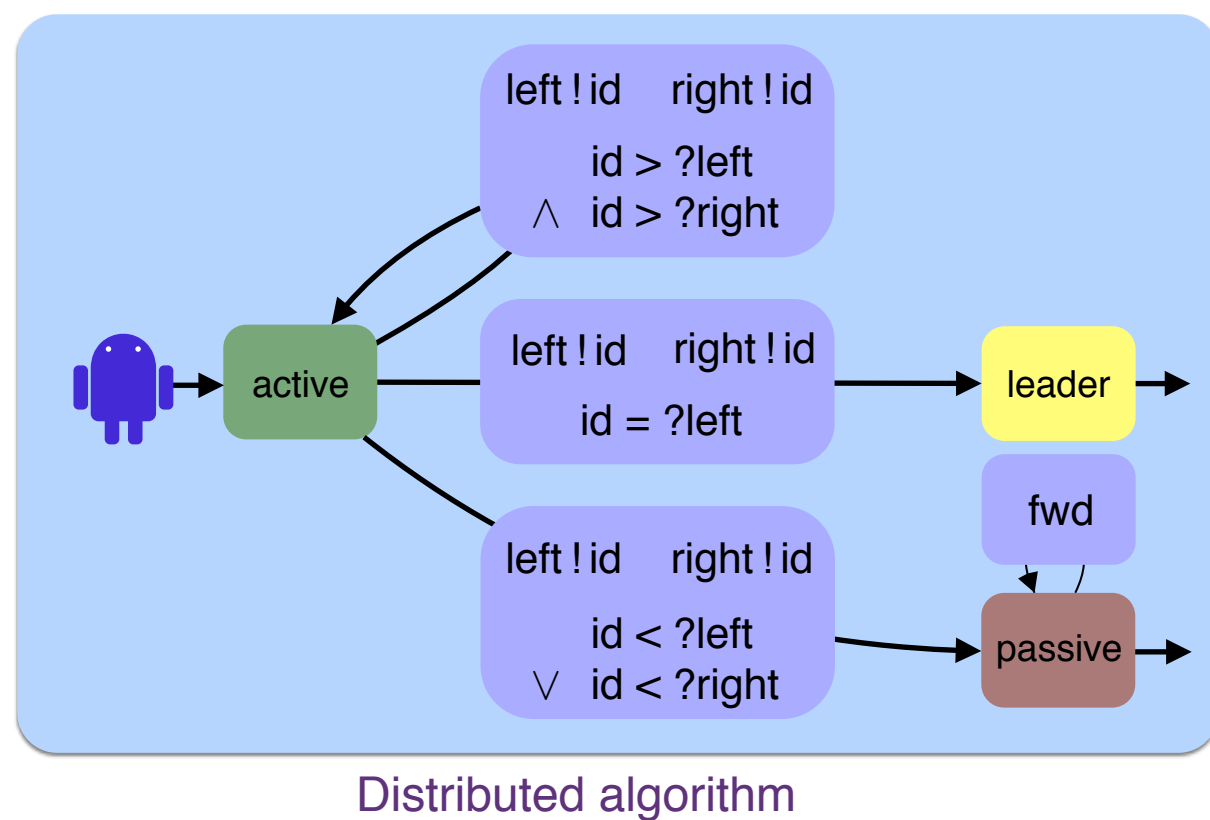
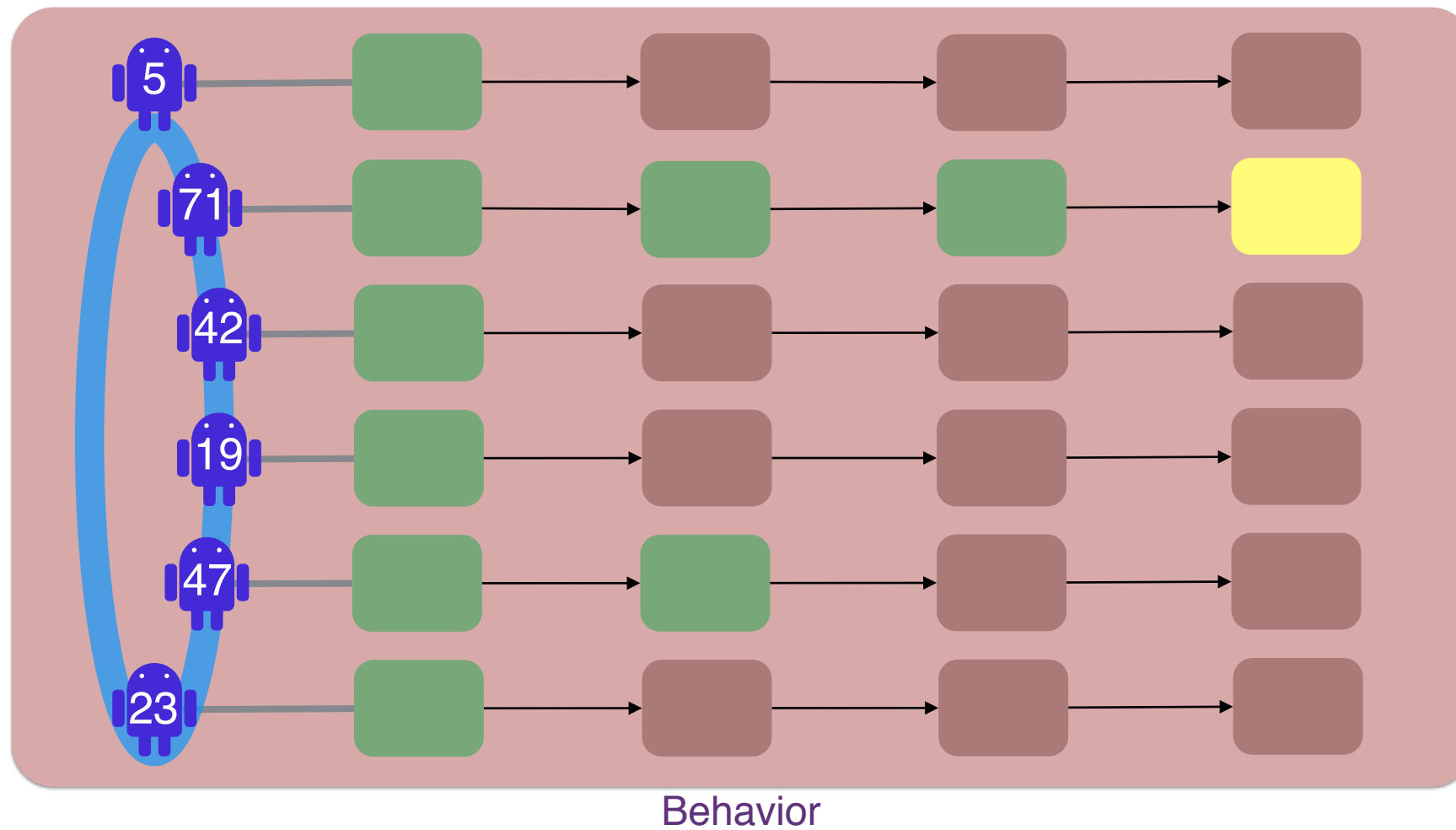
Distributed algorithms

Leader election [Franklin '82]



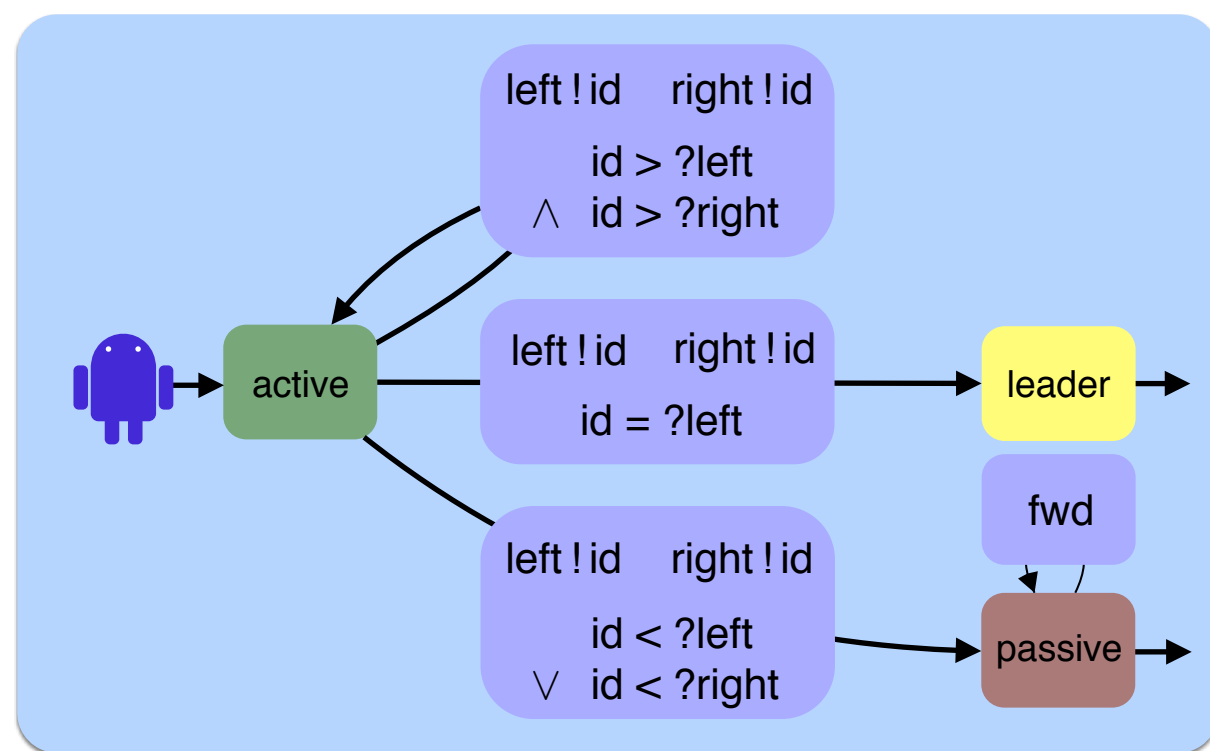
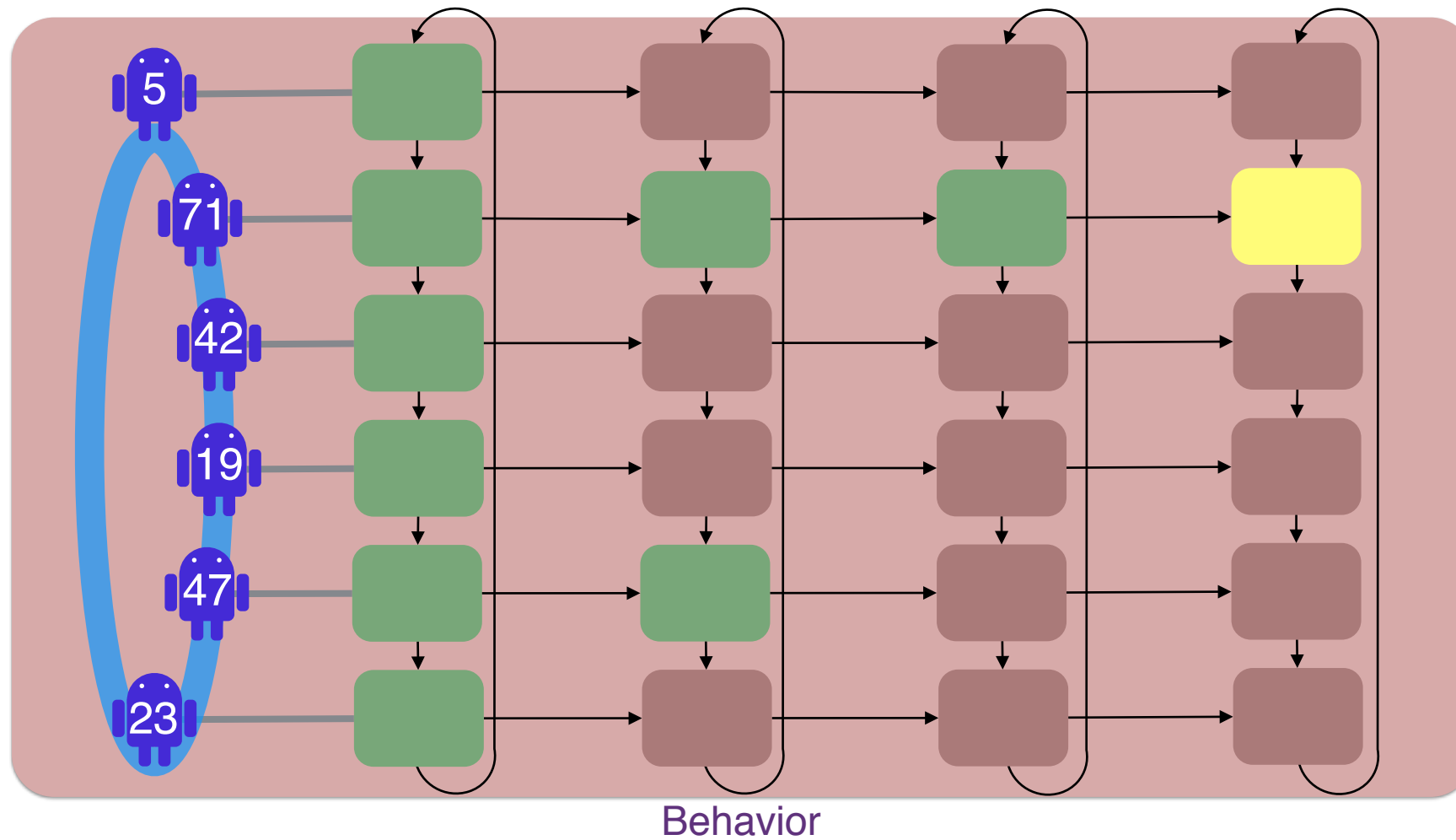
Distributed algorithms

Leader election [Franklin '82]



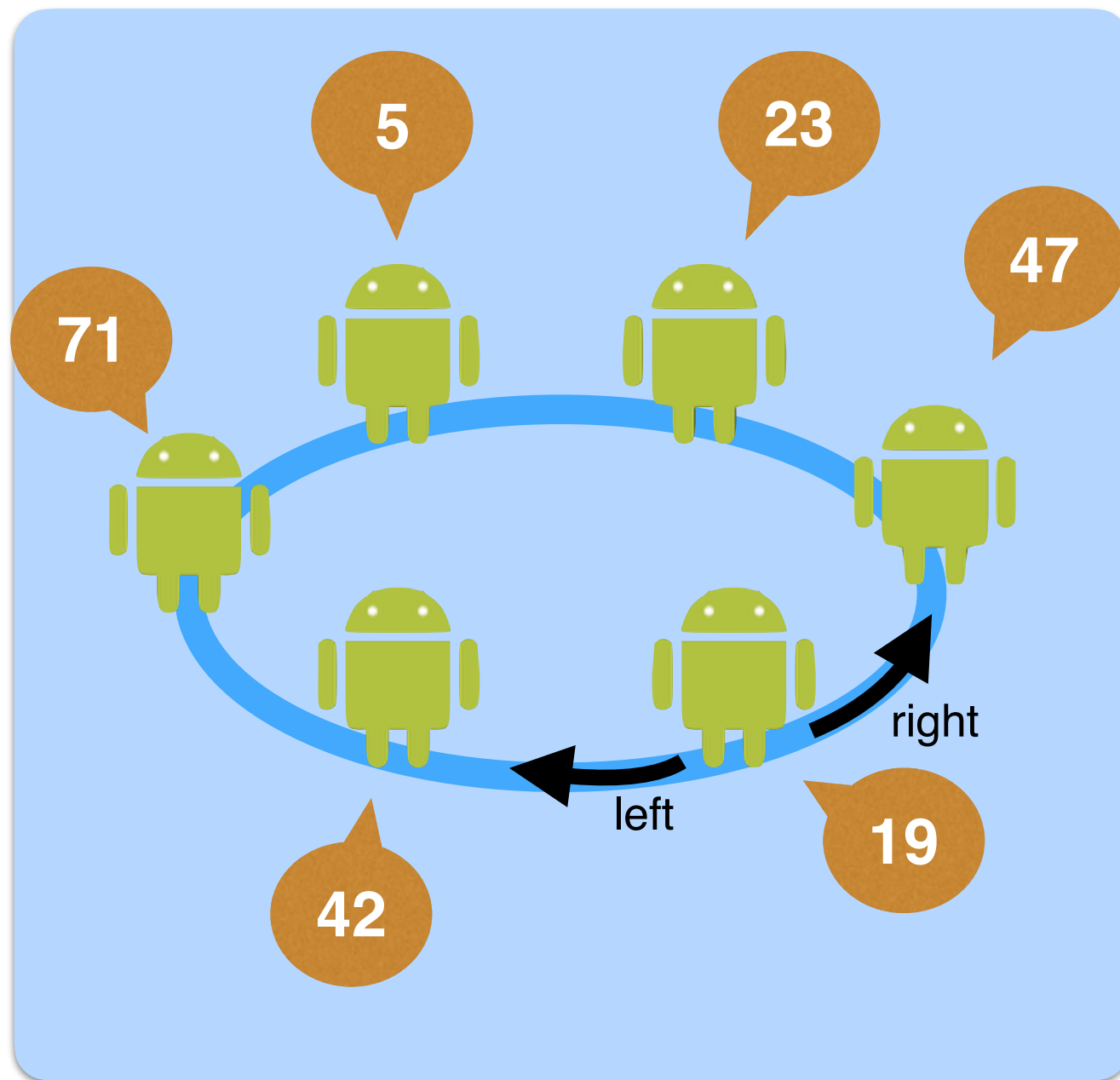
Distributed algorithms

Leader election [Franklin '82]



Distributed algorithm

Distributed algorithms



- Identical finite-state processes
- Number of processes is unknown and unbounded
- Processes have unique pids (integers — unbounded data)

A formal model for distributed algorithms

An automata-like way of writing DA

Every process  can be described by:

- Set of states
- Initial state
- Set of registers
 - stores pid
- Set of transitions
 - send pids to neighbours
 - receive pids from neighbours, and store in registers
 - compare registers
 - update registers

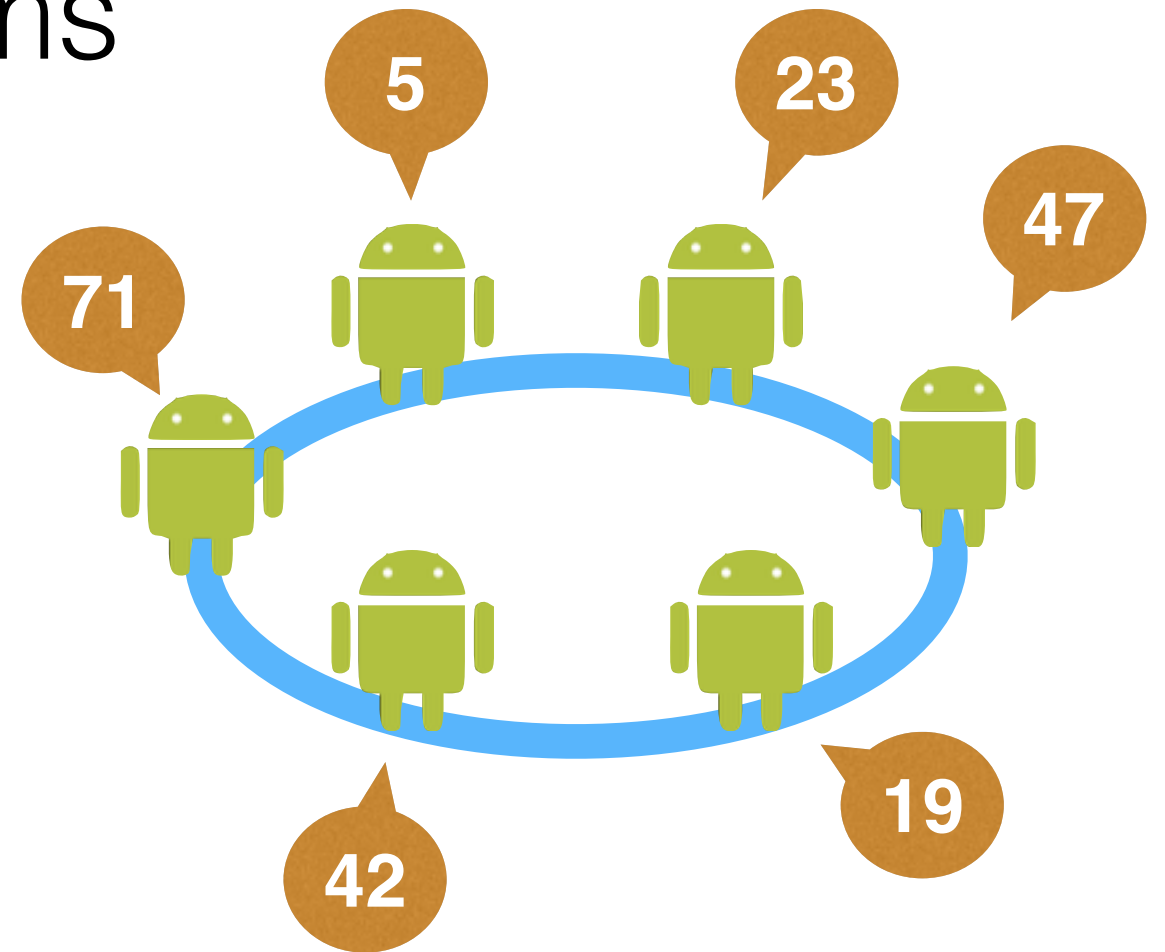
Leader Election Algorithms

Franklin82

states: *active, passive*
found

initial state: *active*

registers: *id, r, r₁, r₂*



$t_1 = \langle \text{active: left!id ; right!id ; left?r}_1 \text{ ; right?r}_2 \text{ ; } r_1 < id \text{ ; } r_2 < id \text{ ; goto active} \rangle$

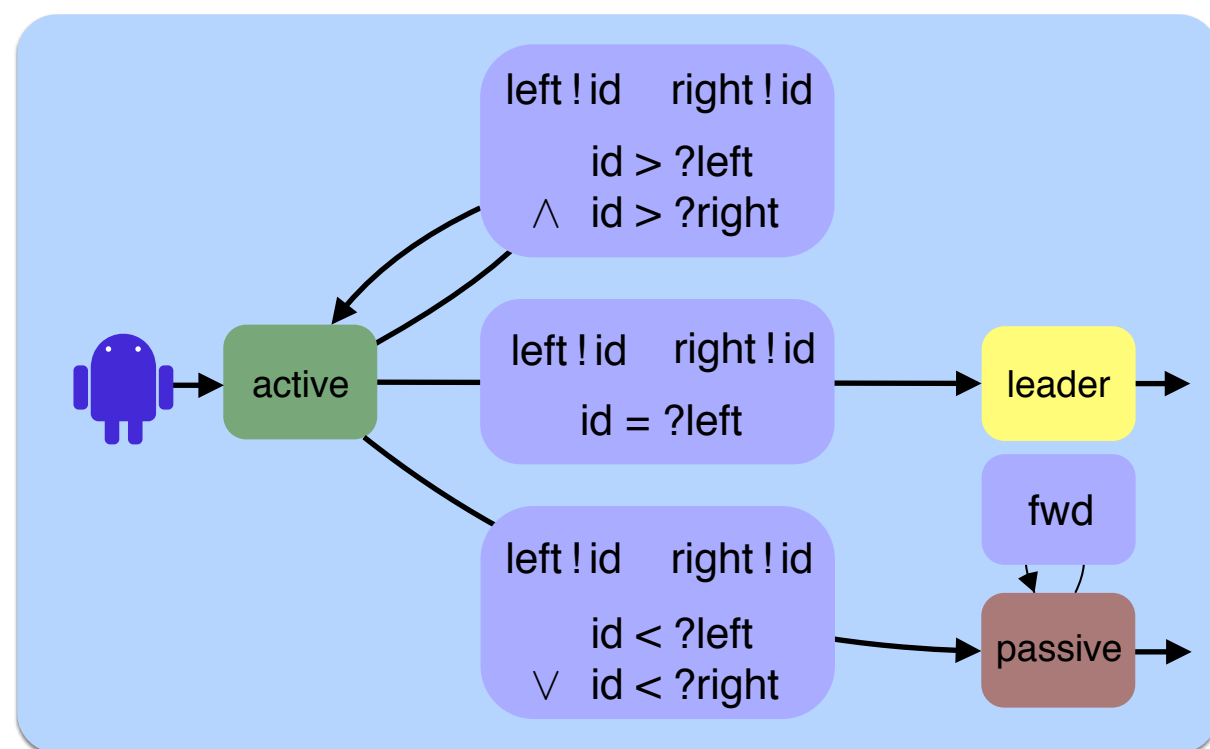
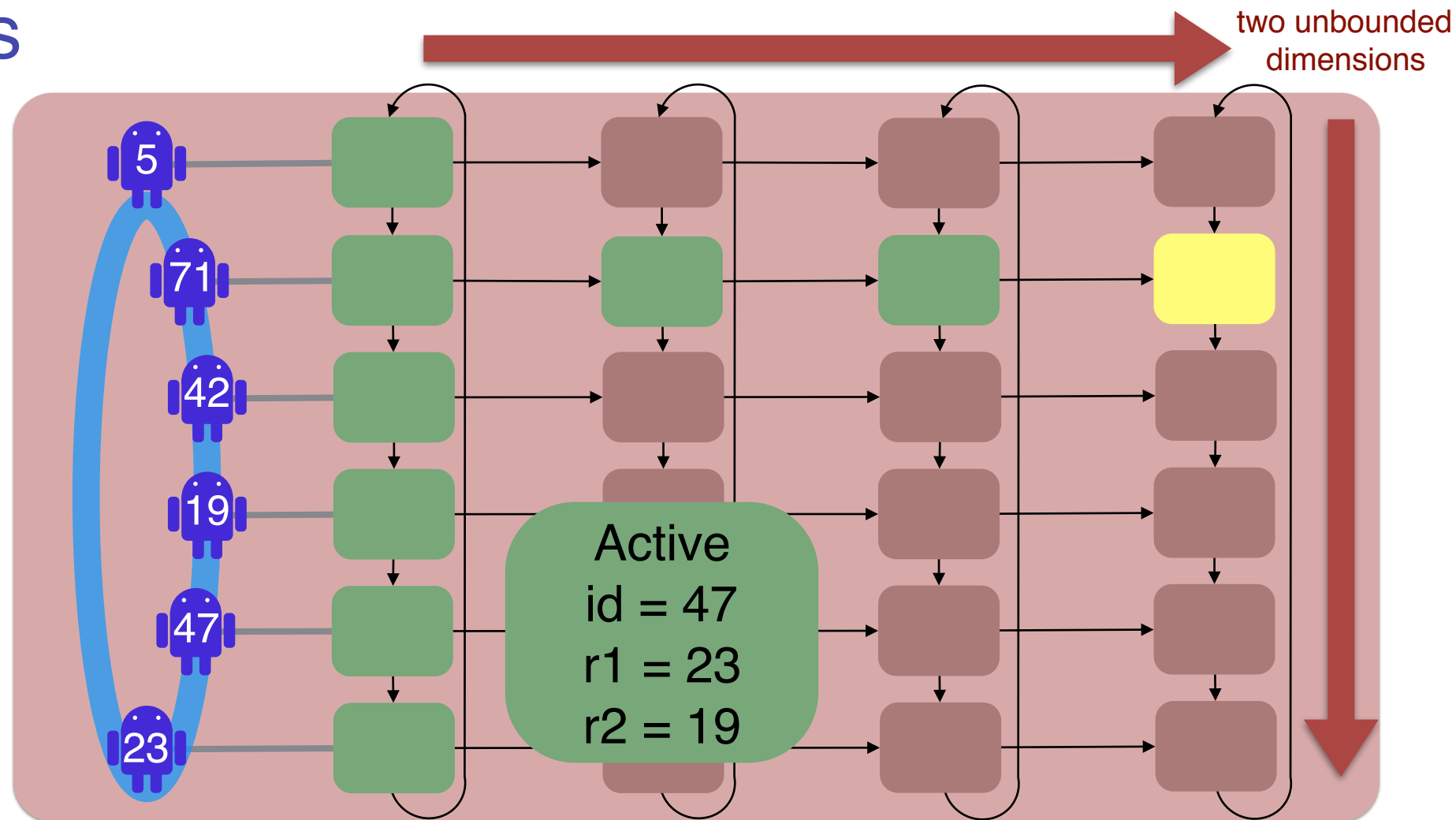
$t_2 = \langle \text{active: } \underline{\hspace{10em}} \text{ ; } id < r_1 \text{ ; goto passive} \rangle$

$t_3 = \langle \text{active: } \underline{\hspace{10em}} \text{ ; } id < r_2 \text{ ; goto passive} \rangle$

$t_4 = \langle \text{active: } \underline{\hspace{10em}} \text{ ; } id = r_1 \text{ ; } r := id \text{ ; goto found} \rangle$

$t_5 = \langle \text{passive: fwd ; left?r ; goto passive} \rangle$

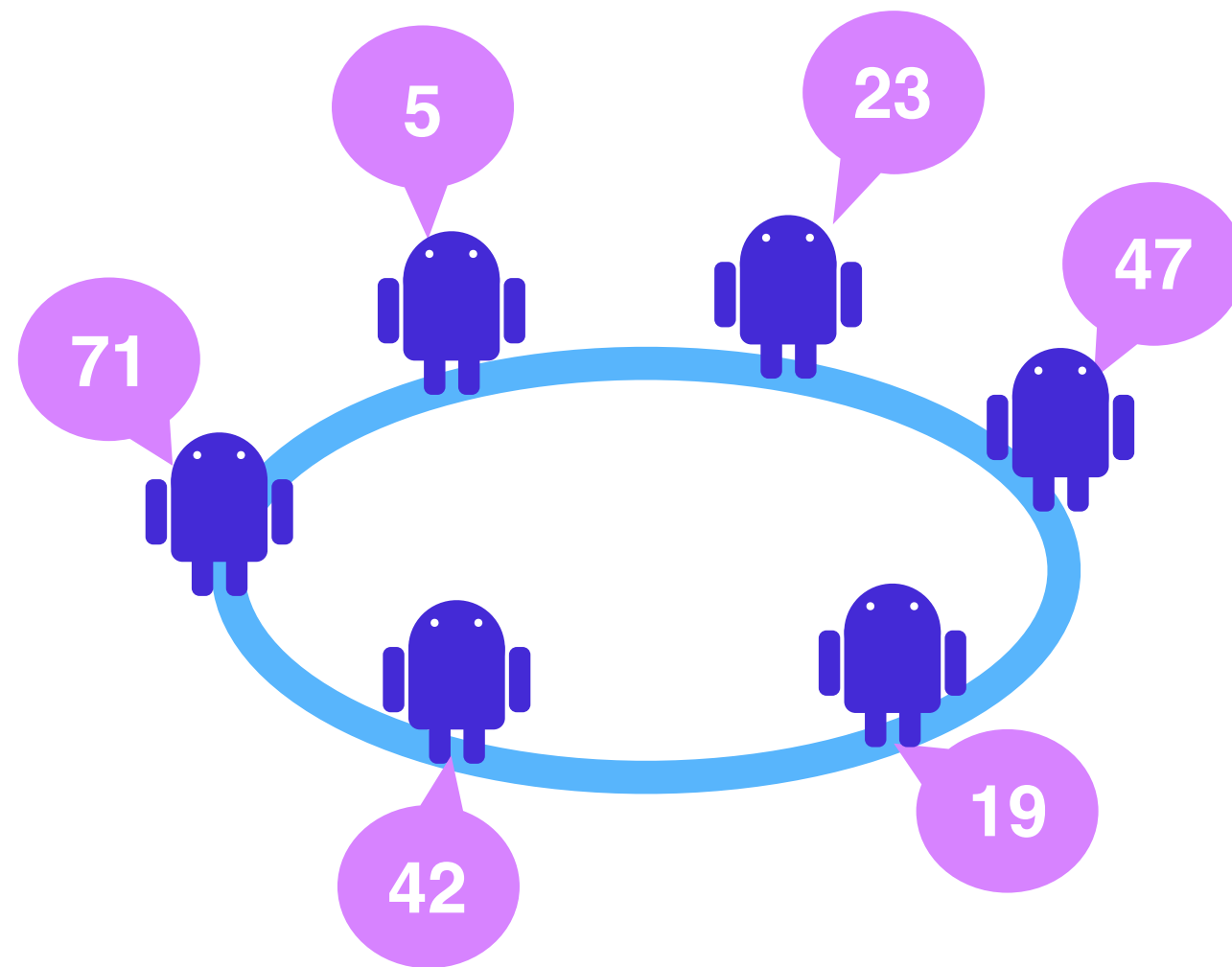
Behaviors



Distributed algorithm

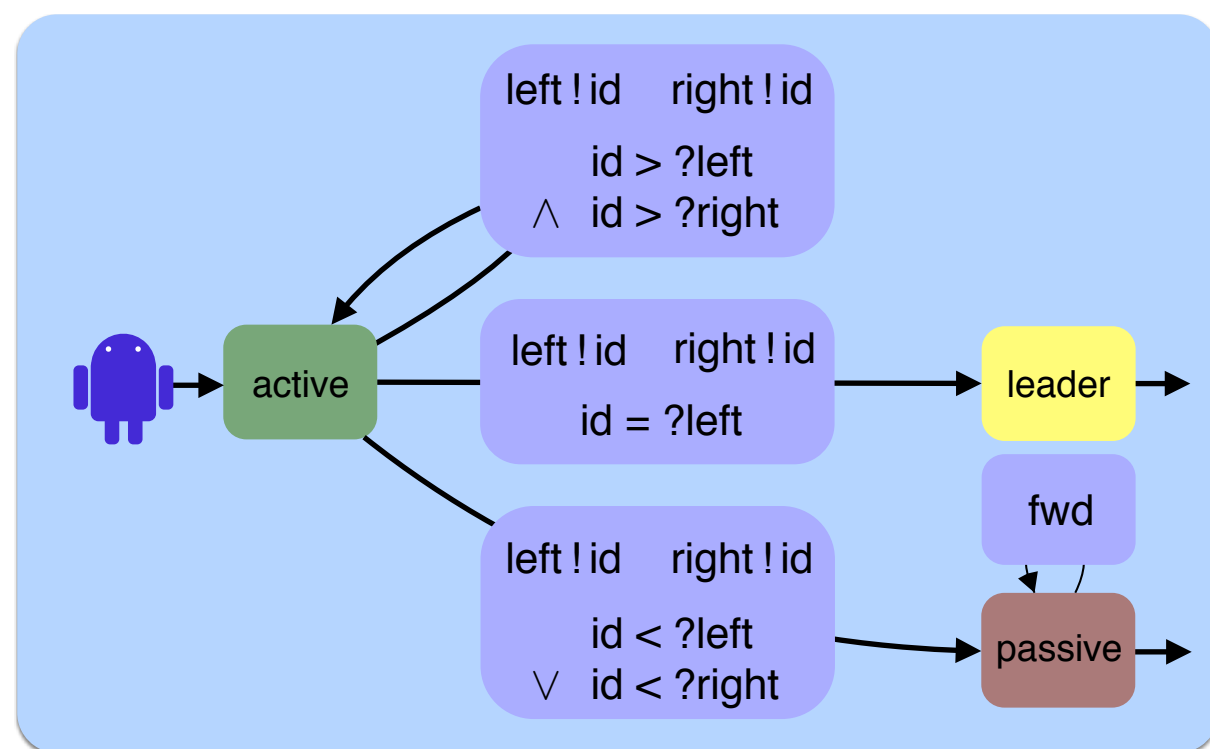
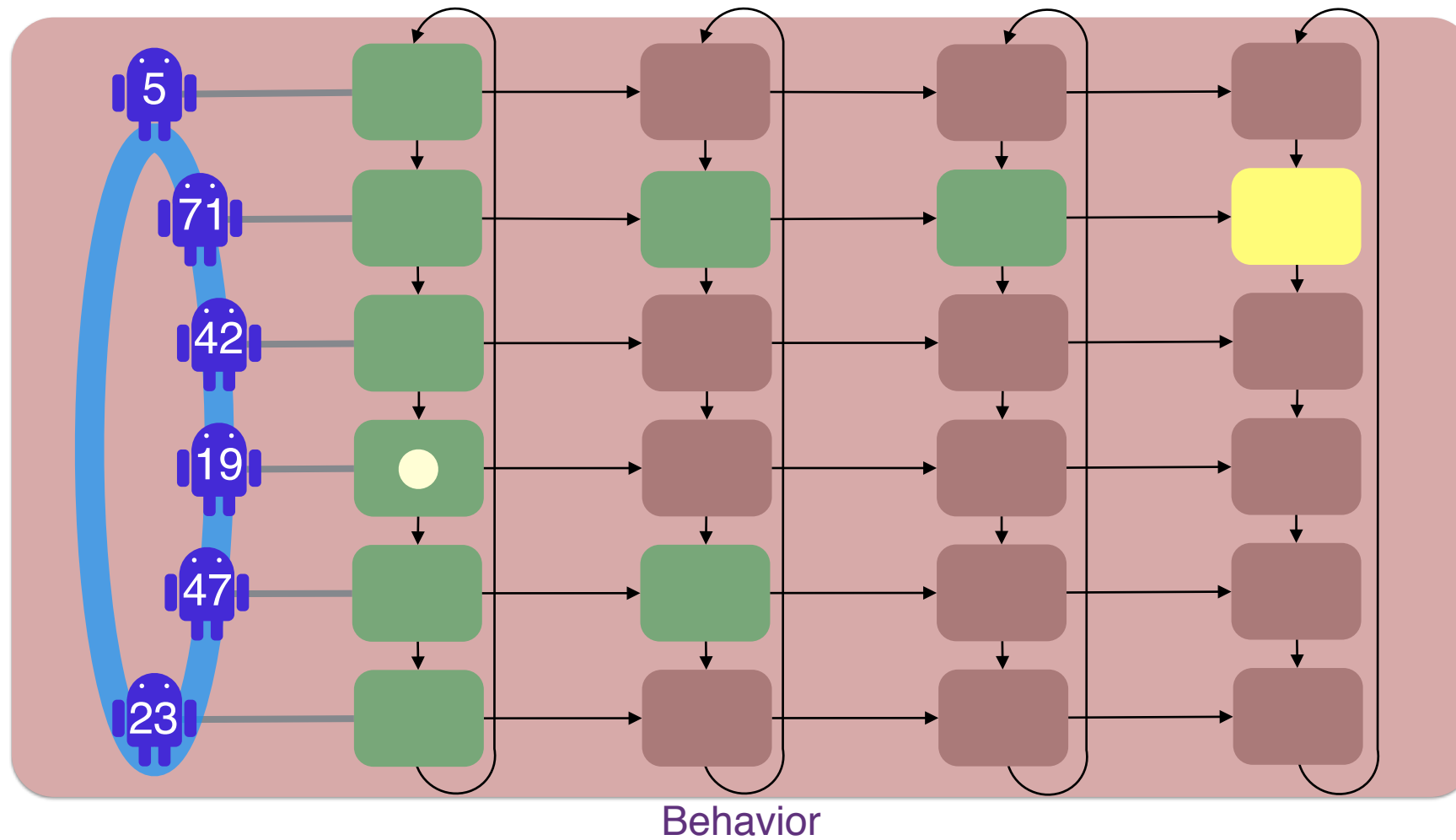
Cylinders
Arbitrary length and width
Labelled with data
from an infinite domain

Specification language

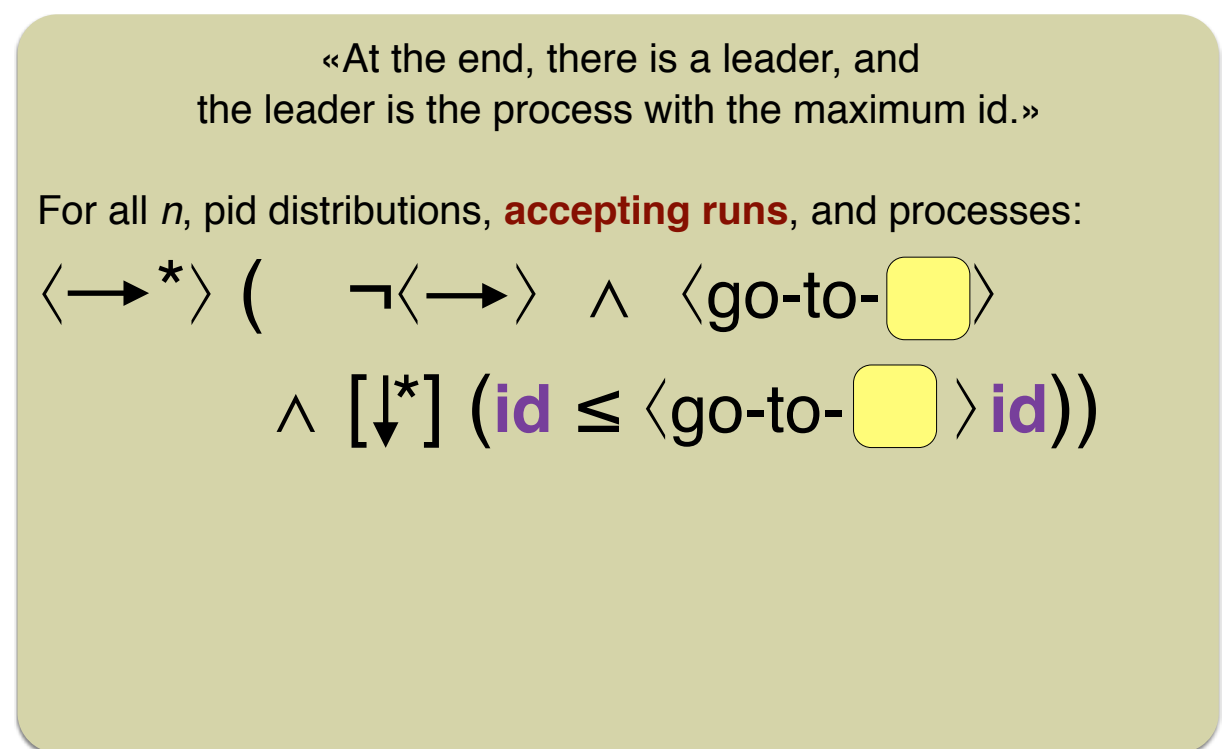


Distributed algorithms

Leader election [Franklin '82]



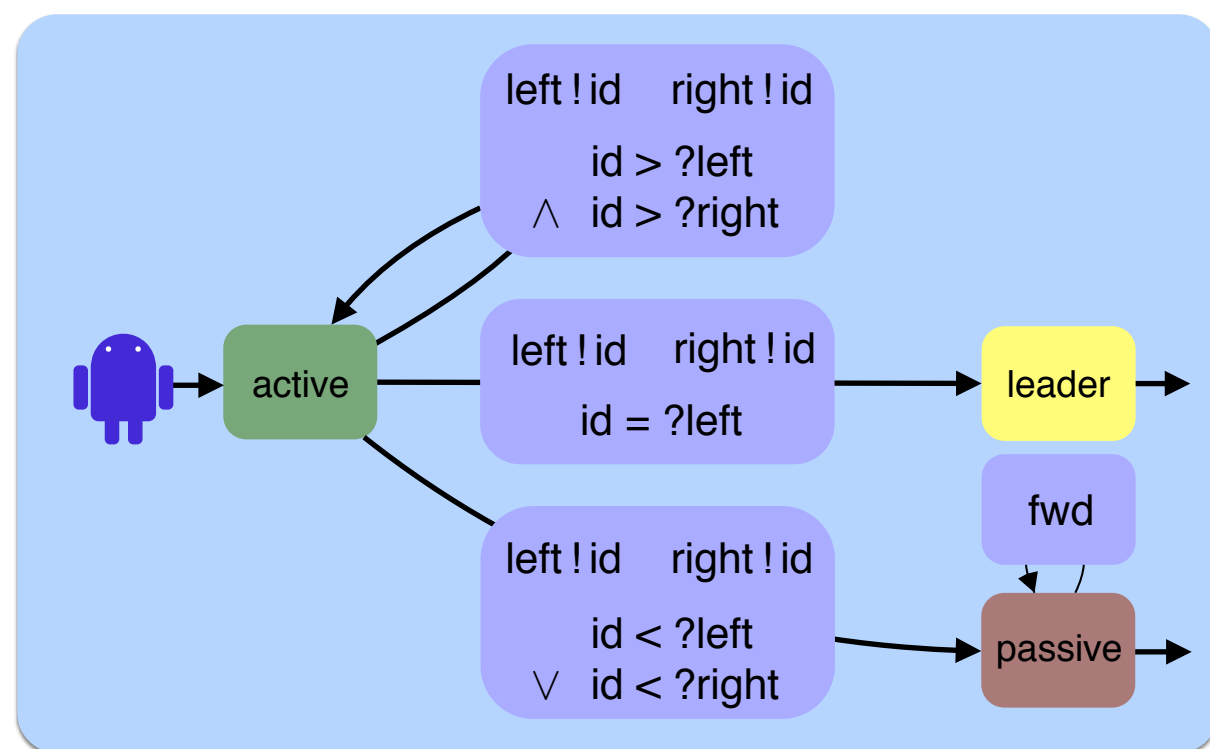
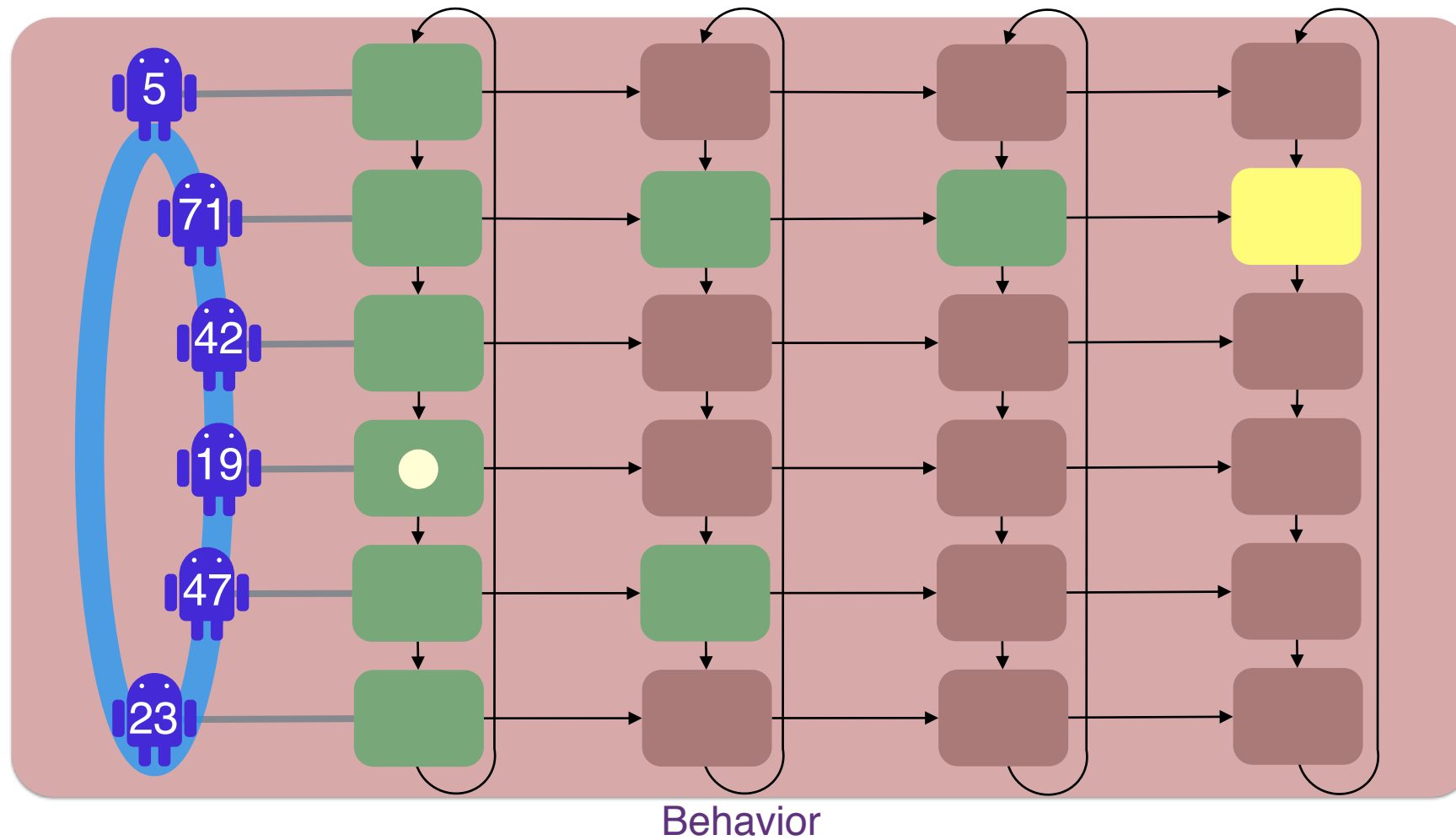
Distributed algorithm



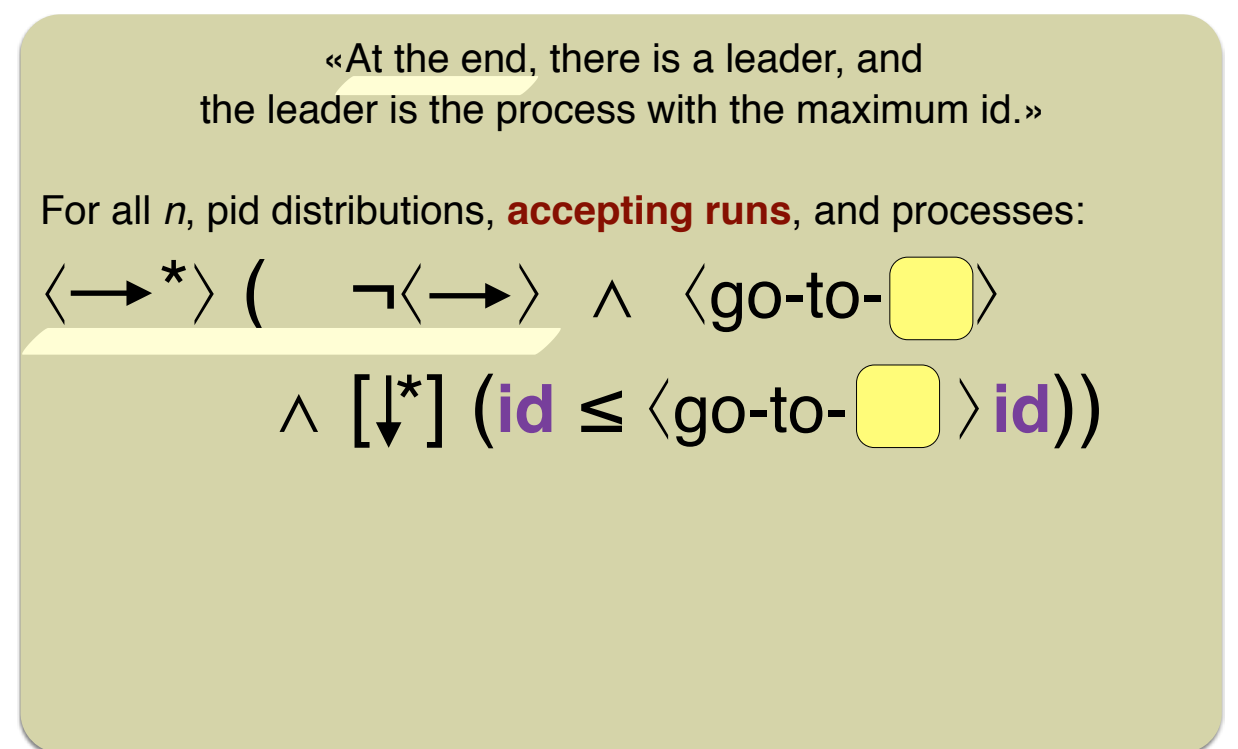
Data Propositional Dynamic Logic
[Bojanczyk et al. '09; Figueira-Segoufin '11]

Distributed algorithms

Leader election [Franklin '82]



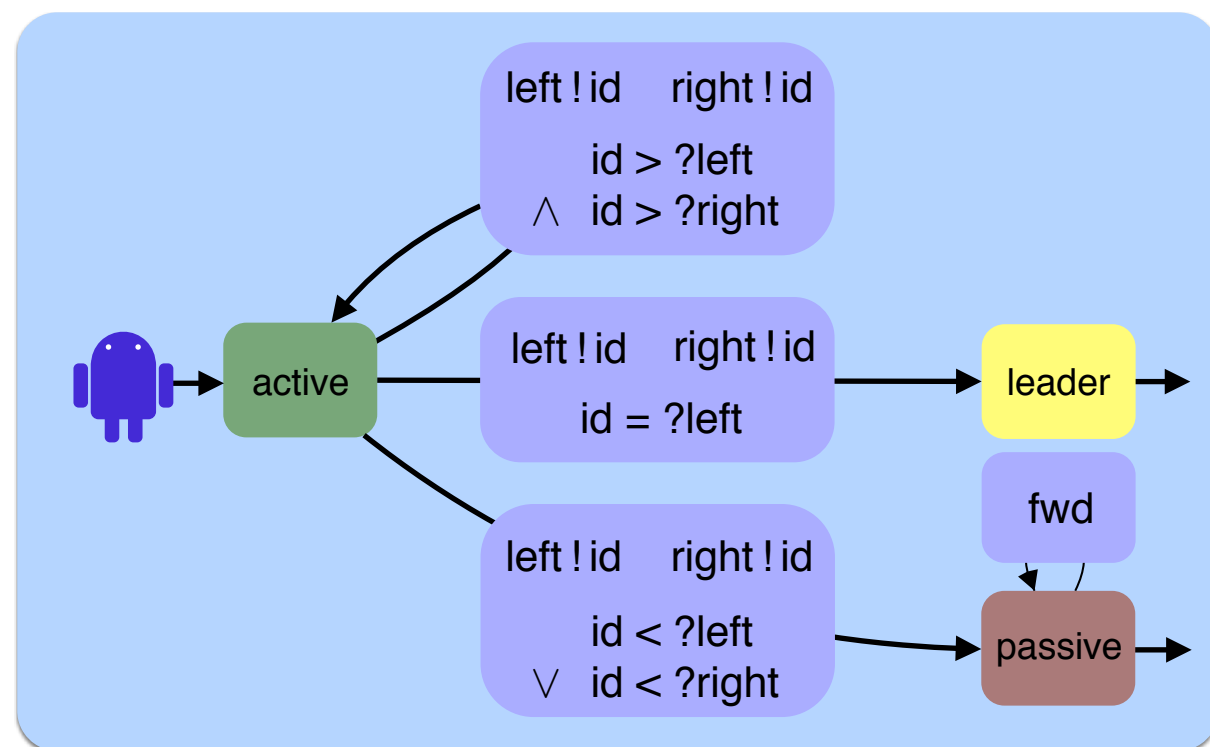
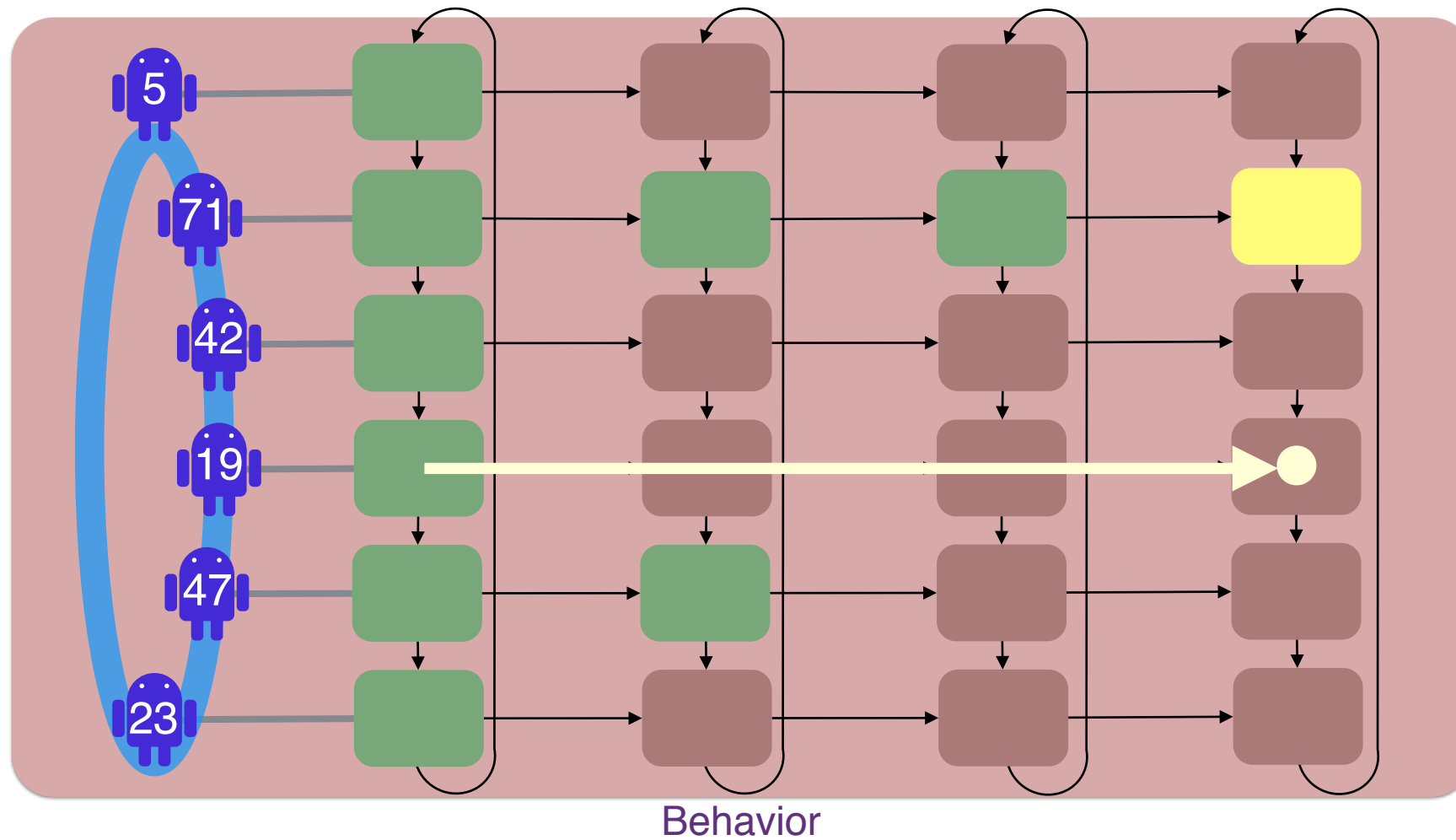
Distributed algorithm



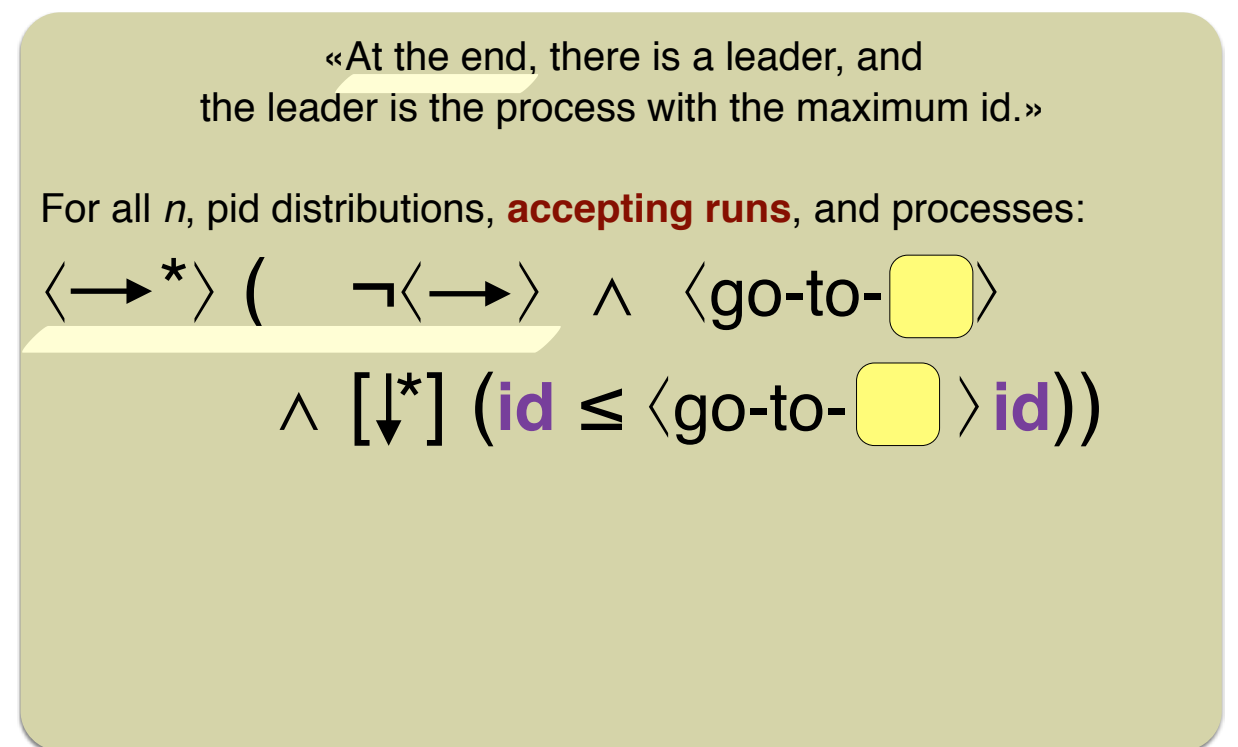
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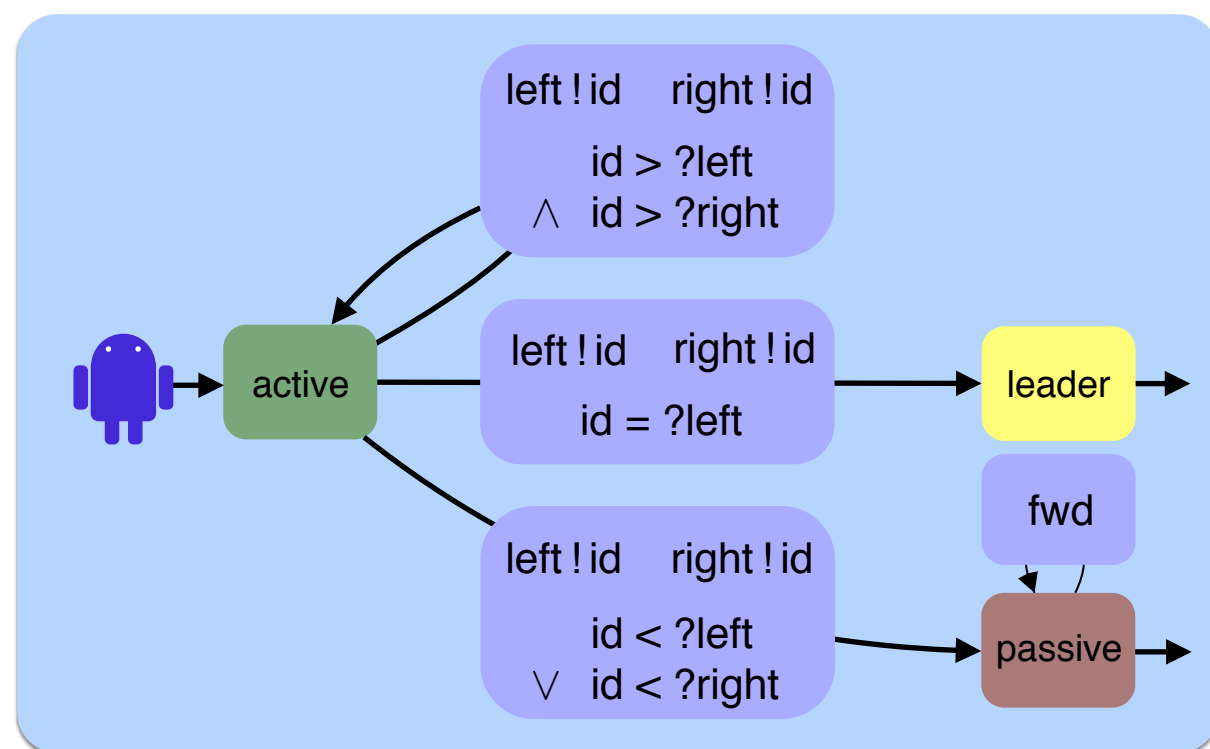
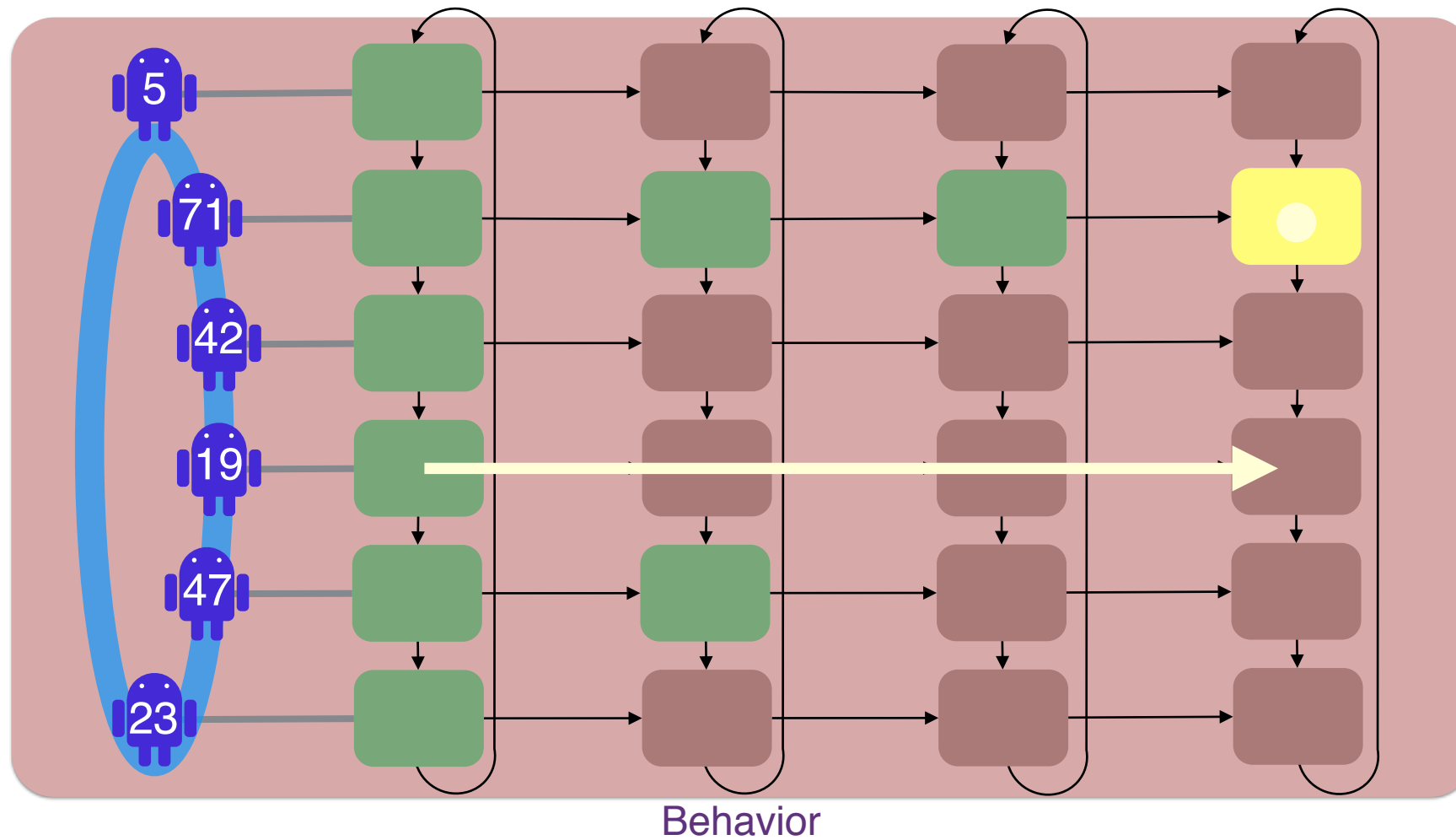
Distributed algorithm



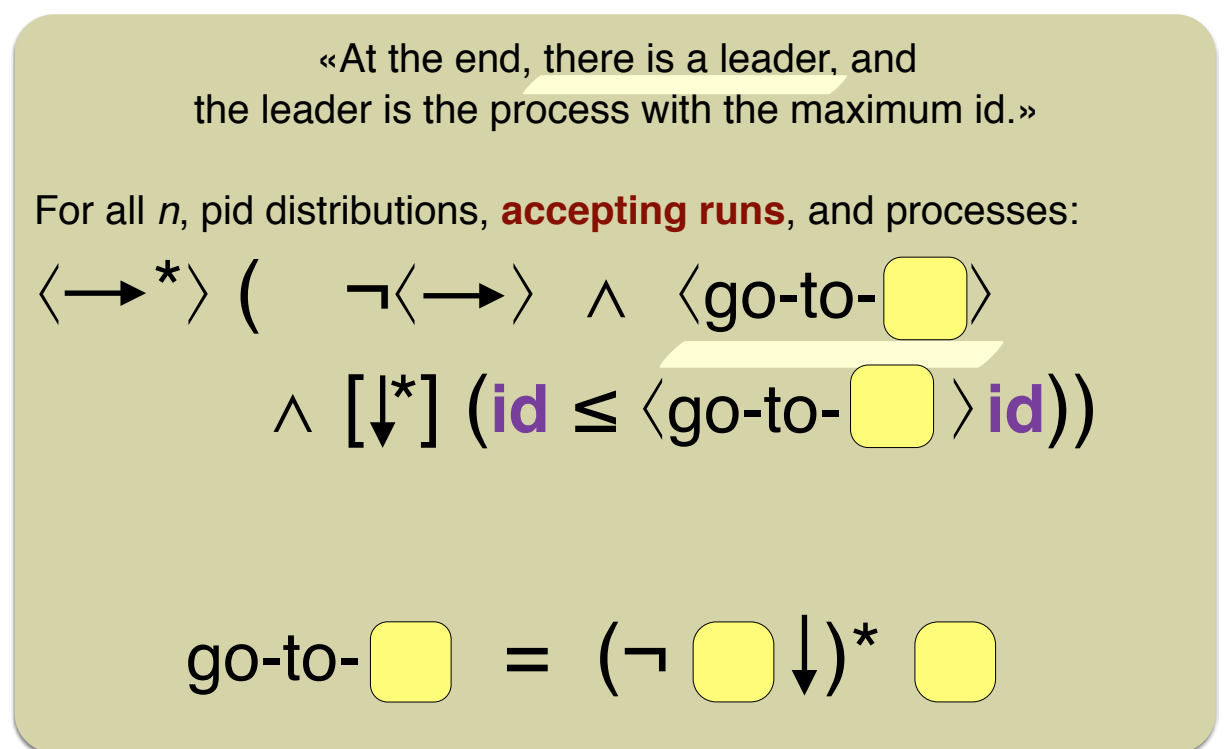
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Leader election [Franklin '82]



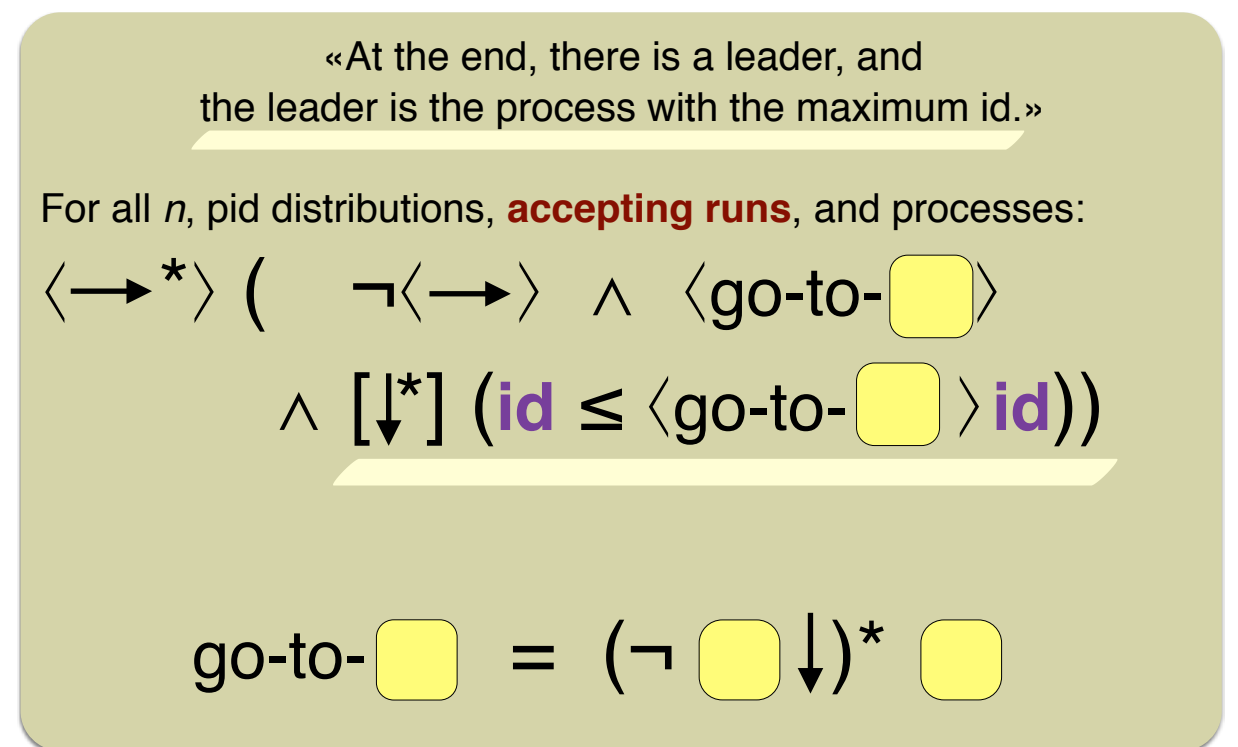
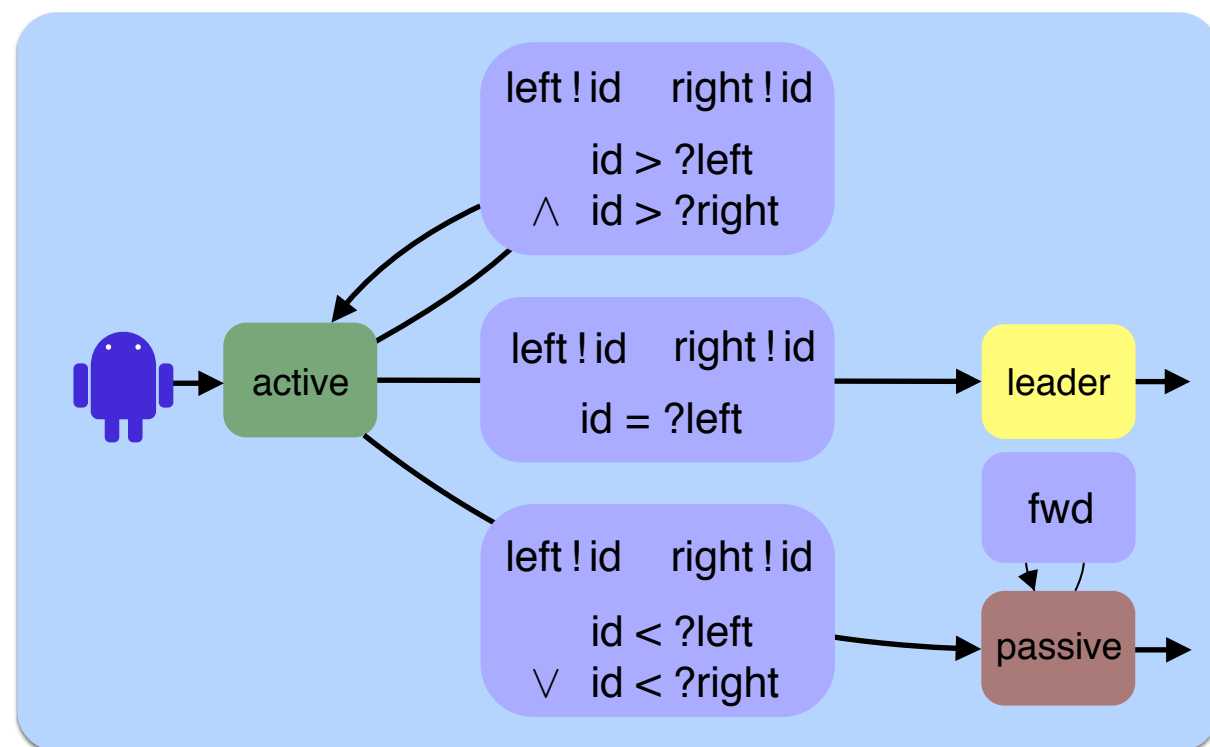
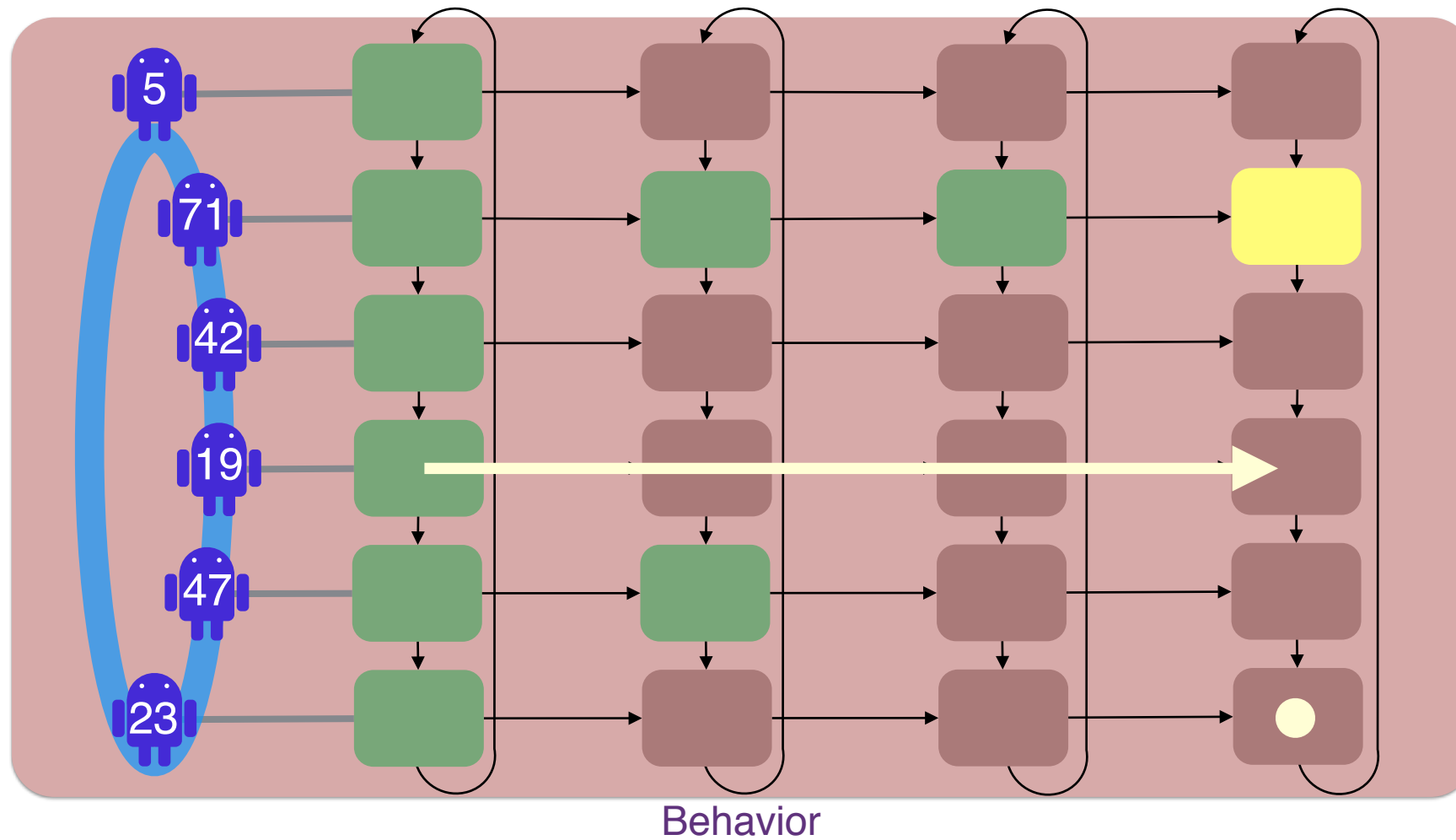
Distributed algorithm



Data Propositional Dynamic Logic
[Bojanczyk et al. '09; Figueira-Segoufin '11]

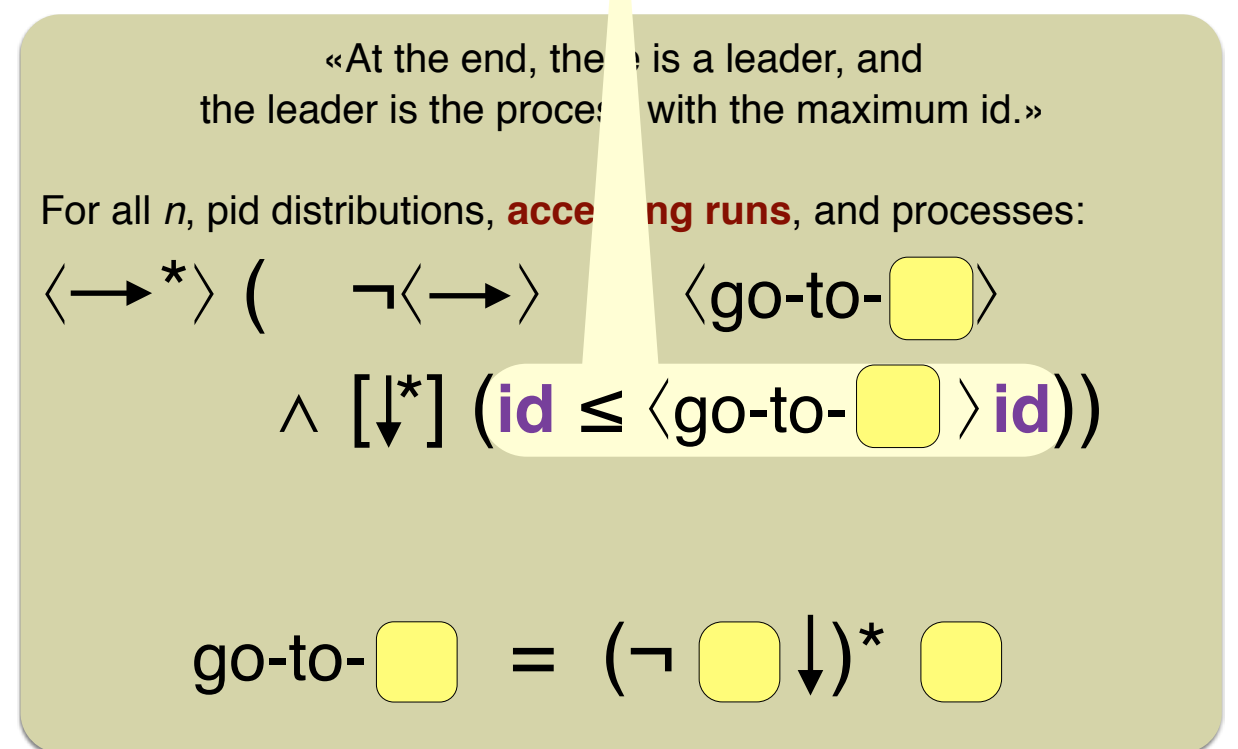
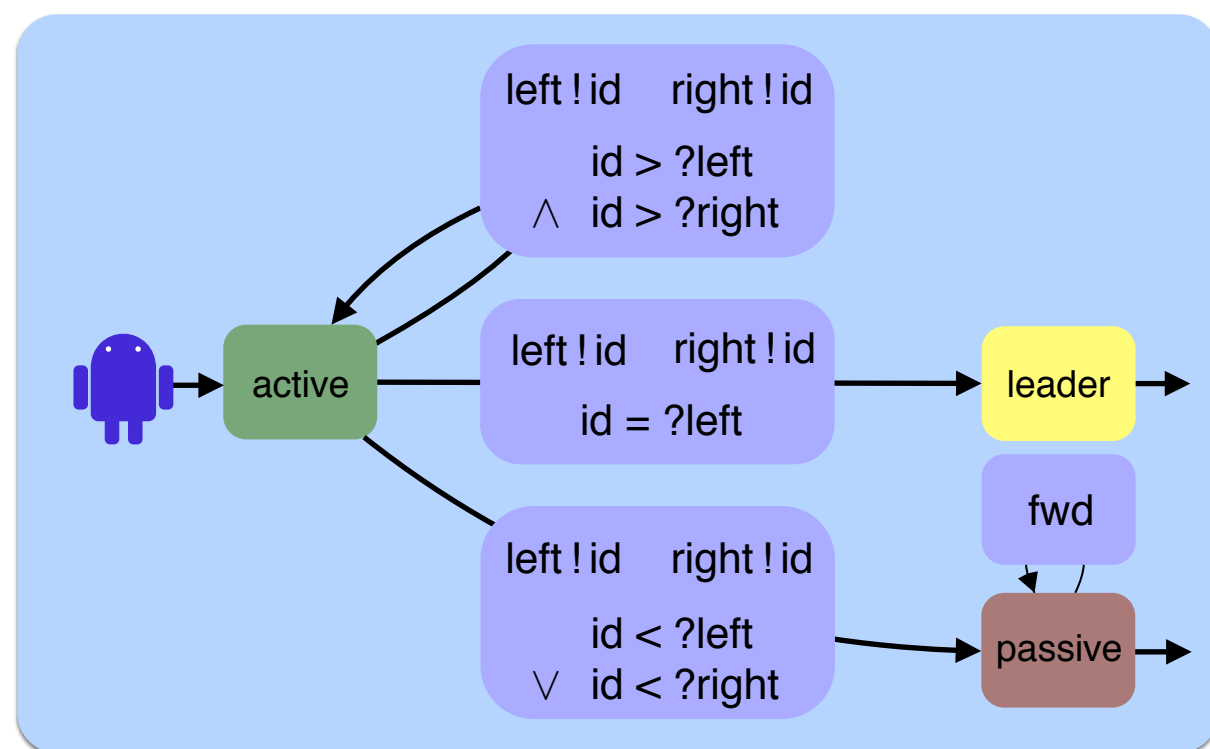
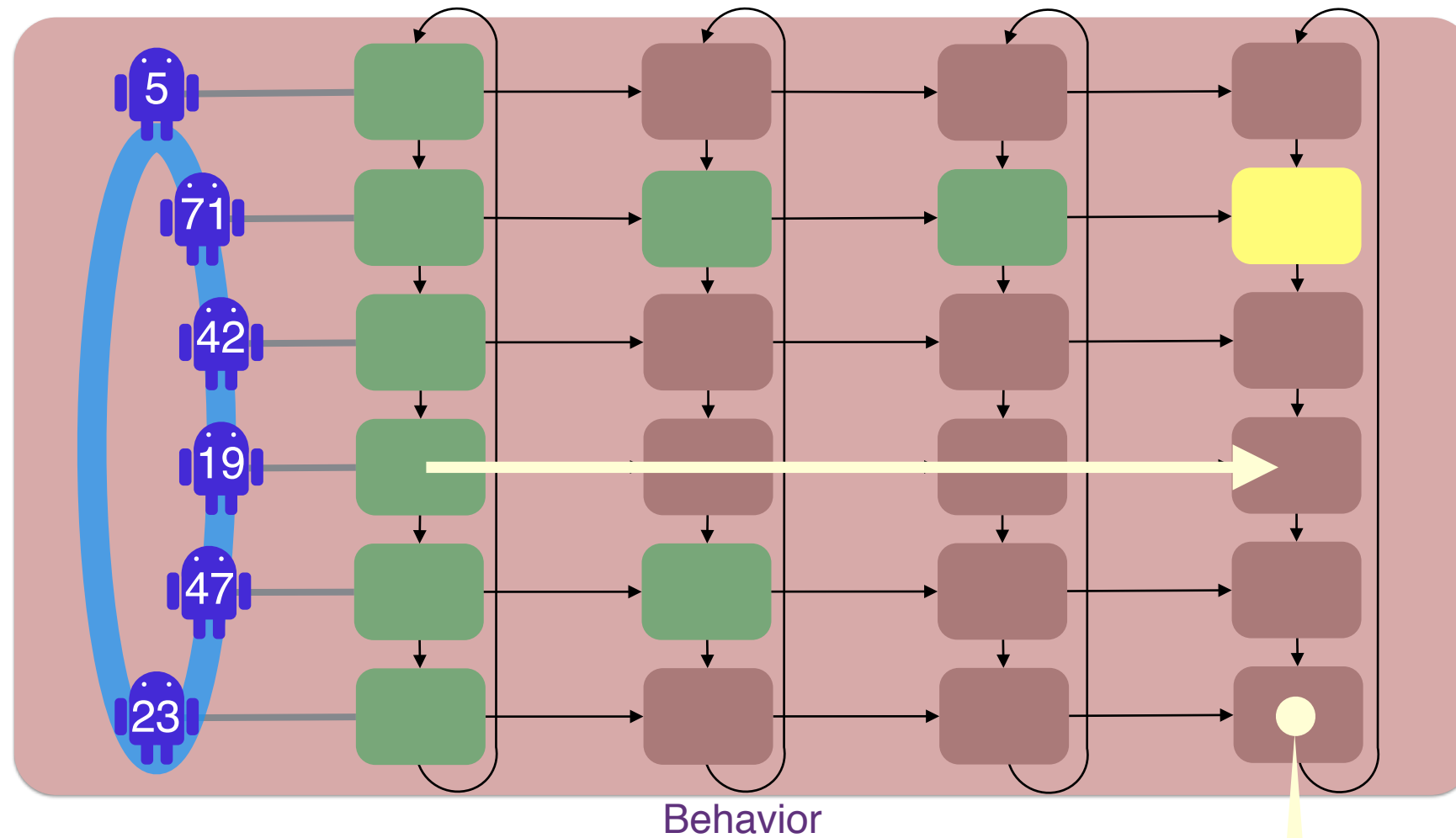
Distributed algorithms

Leader election [Franklin '82]



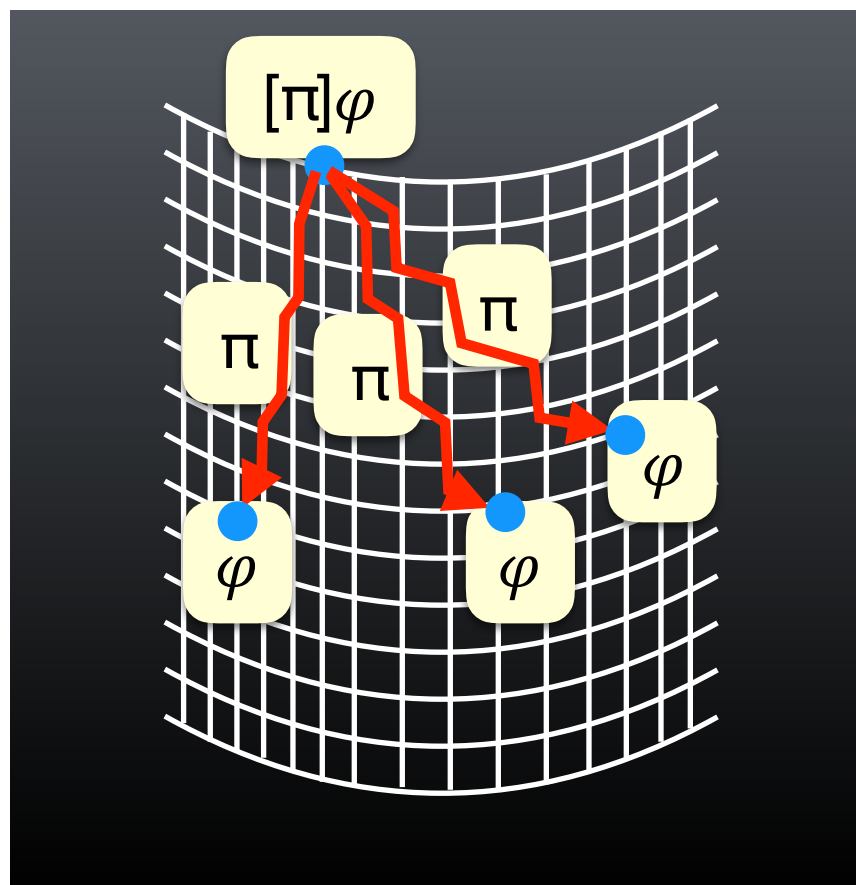
Distributed algorithms

Leader election [Franklin '82]



Specifications

Data PDL



$$\varphi, \varphi' ::= \mathbf{m} \mid s \mid \neg\varphi \mid \varphi \wedge \varphi' \mid \varphi \Rightarrow \varphi' \mid [\pi]\varphi \mid \langle\pi\rangle r \bowtie \langle\pi'\rangle r'$$

$$\pi, \pi' ::= \{\varphi\}^? \mid d \mid \pi + \pi' \mid \pi \cdot \pi' \mid \pi^*$$

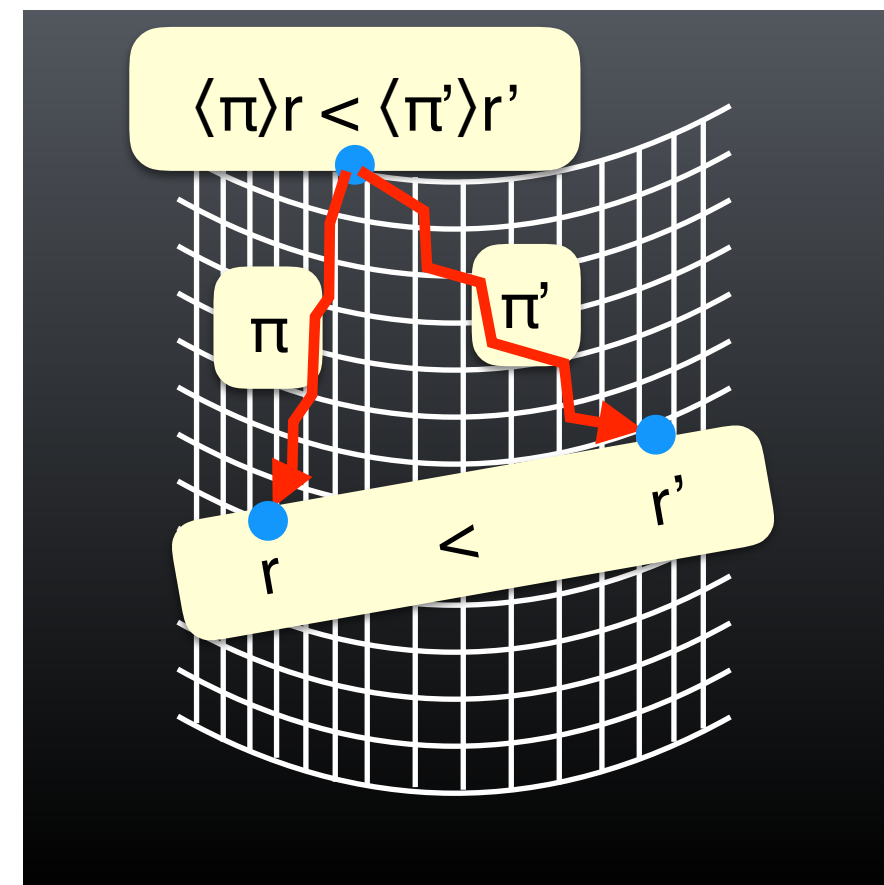
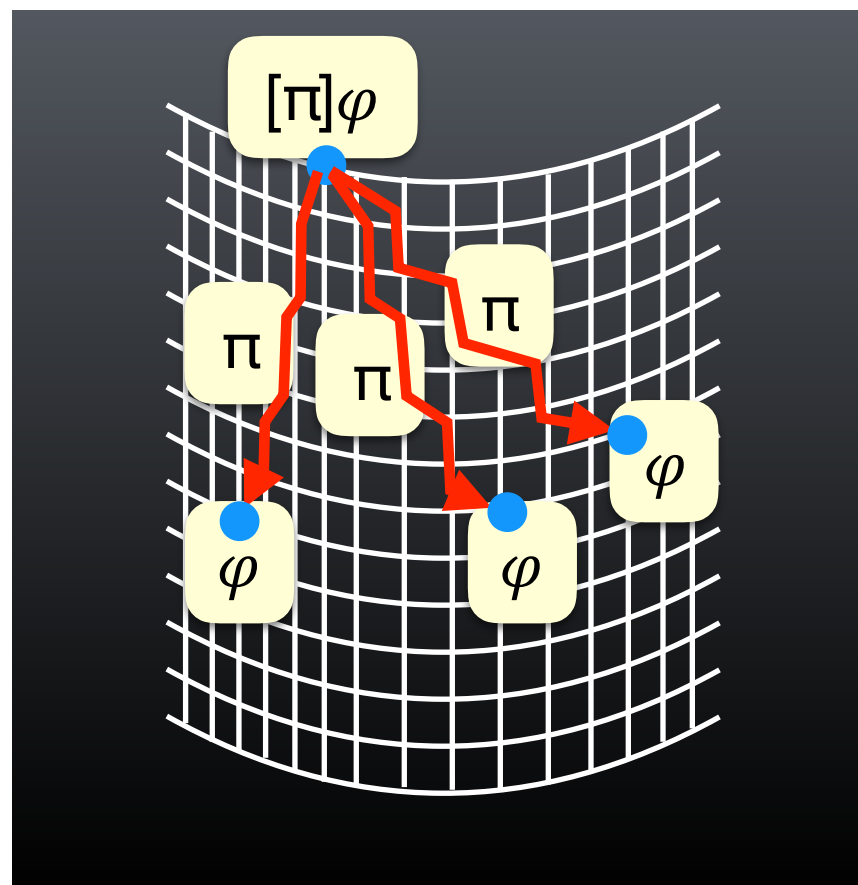
$$s \in S, r, r' \in Reg, \bowtie \in \{=, \neq, <, \leq\}, \text{ and } d \in \{\epsilon, \leftarrow, \rightarrow, \uparrow, \downarrow\}.$$

M. Bojanczyk, A. Muscholl, T. Schwentick, and L. Segoufin. Two-variable logic on data trees and XML reasoning. *J. ACM*, 56(3), 2009.

D. Figueira and L. Segoufin. Bottom-up automata on data trees and vertical XPath. In STACS'11, volume 9 of LIPIcs, pages 93–104, 2011.

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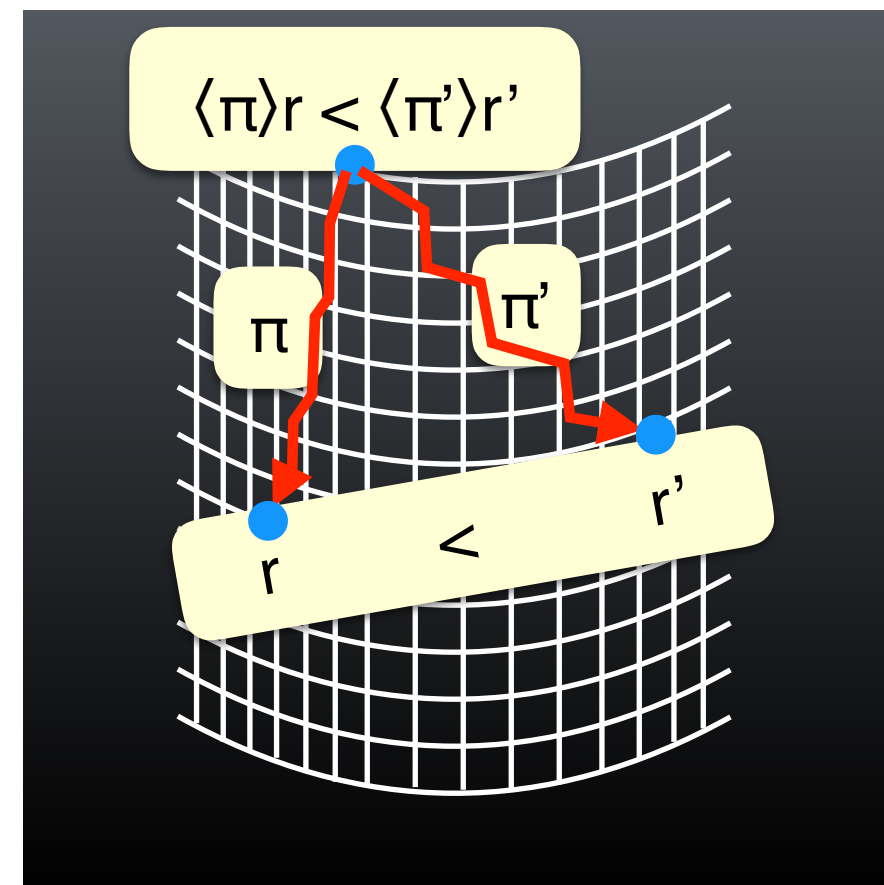
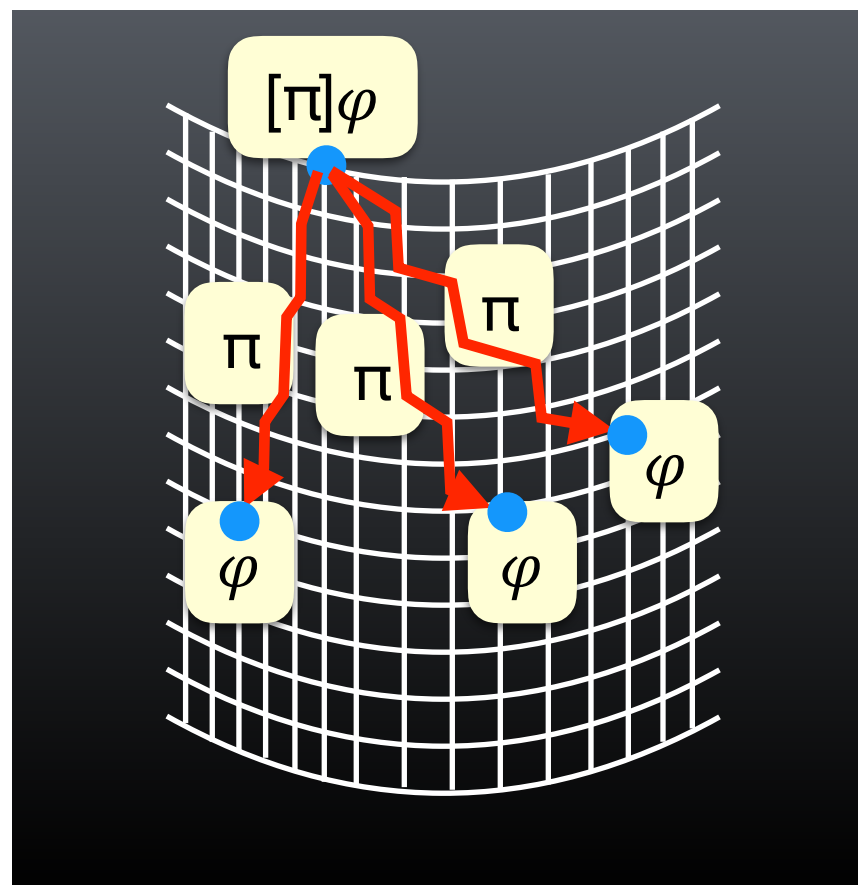
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Specifications

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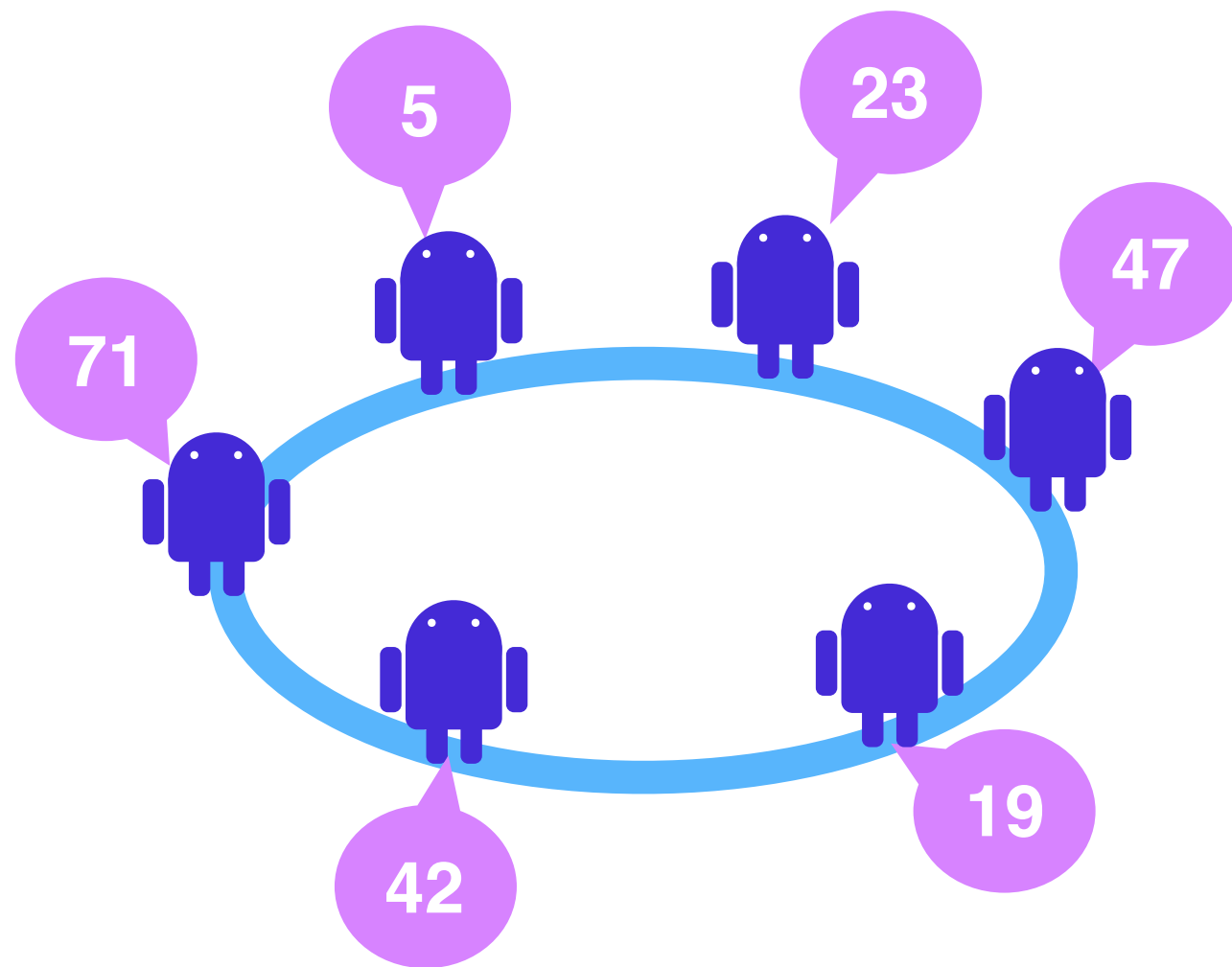
For rings of all sizes, all pid distributions,
all accepting runs, and all starting process (m)

$$\varphi, \varphi' ::= \mathbf{m} \mid s \mid \neg\varphi \mid \varphi \wedge \varphi' \mid \varphi \Rightarrow \varphi' \mid [\pi]\varphi \mid \langle\pi\rangle r \bowtie \langle\pi'\rangle r'$$

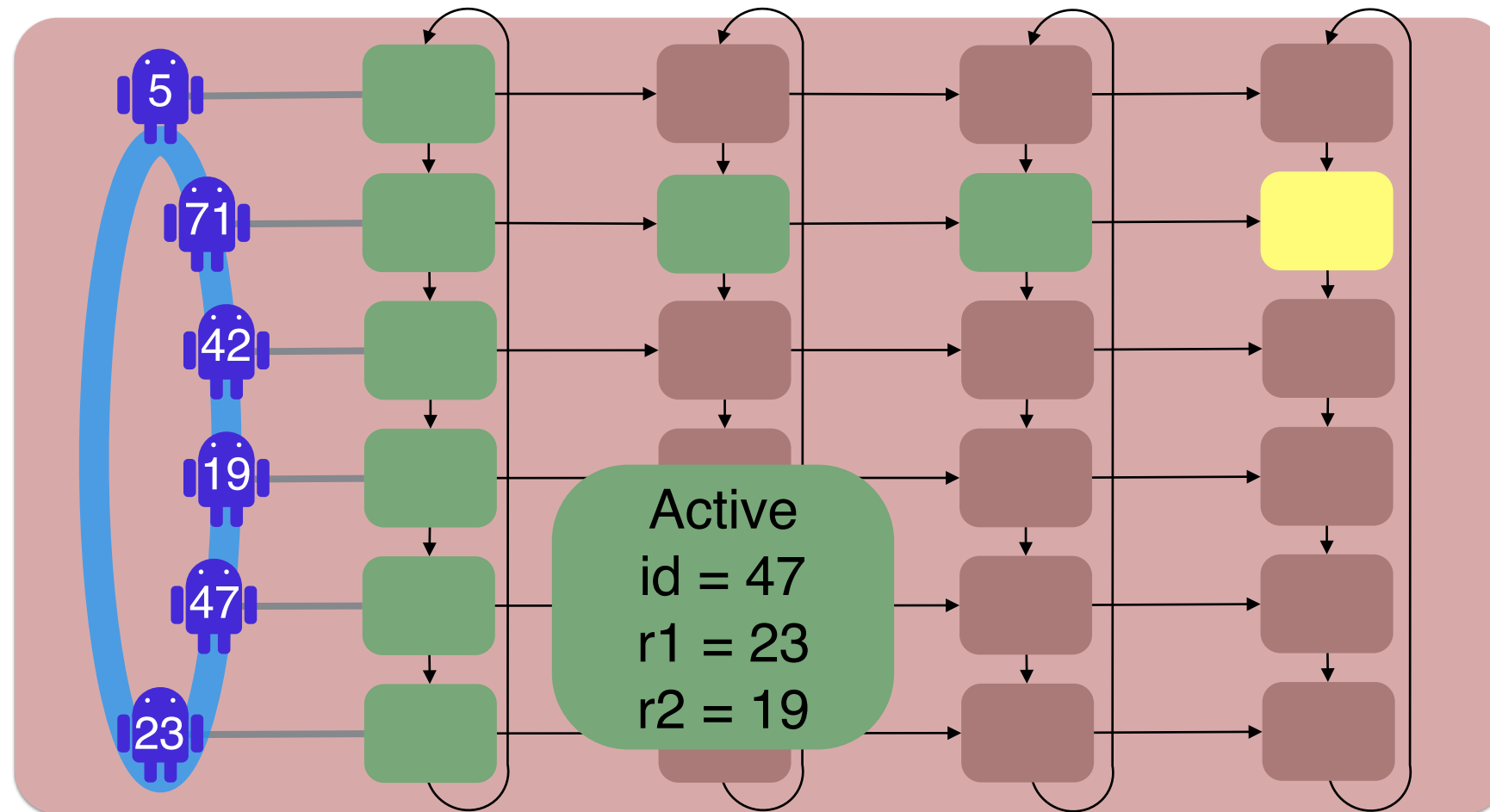
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Model Checking



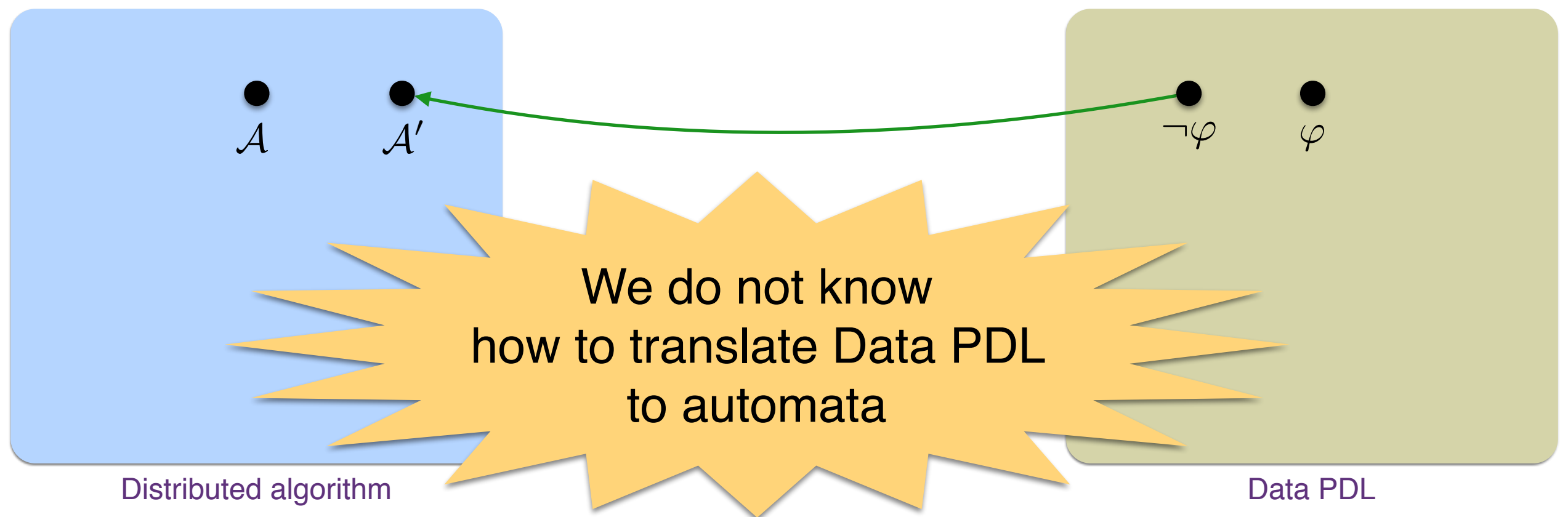
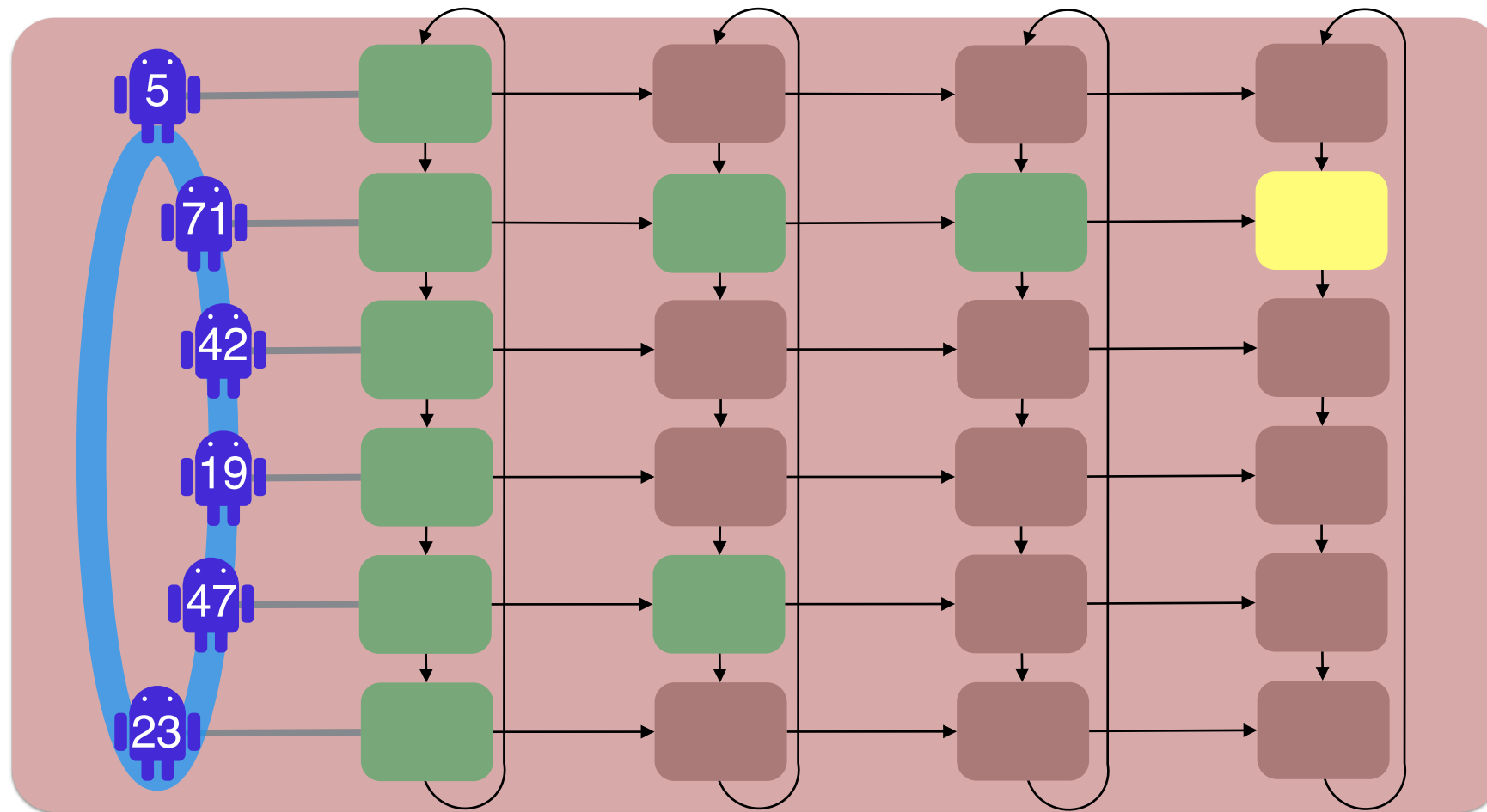
Model Checking Distributed algorithms



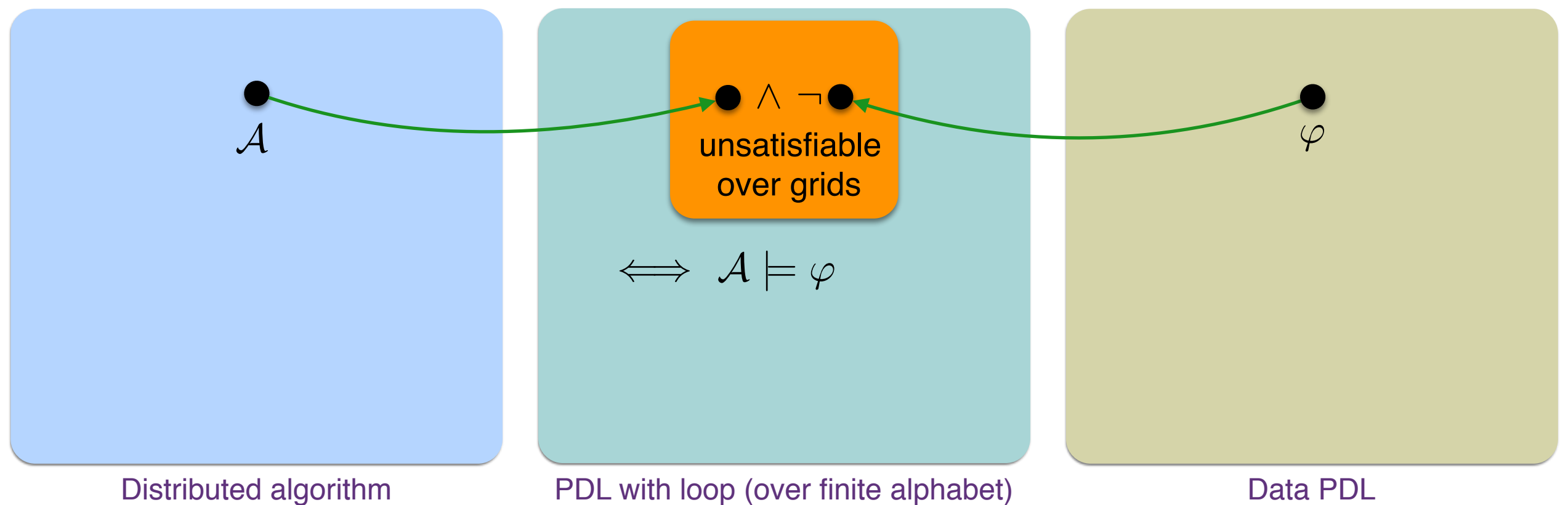
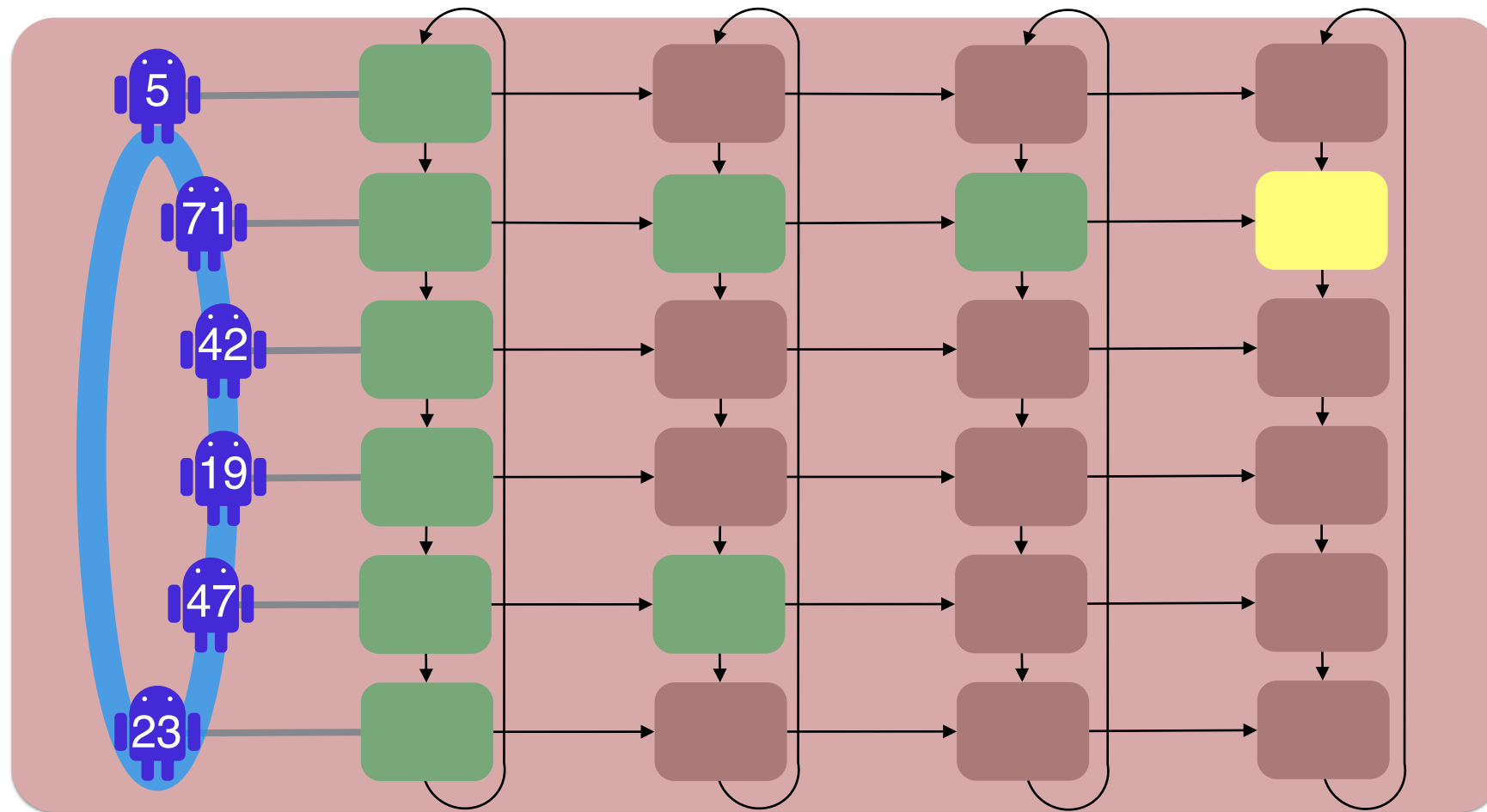
UNDECIDABLE

Cylinders of arbitrary width and length
Data from an infinite domain
Register automata with data comparisons
Data PDL with data comparisons

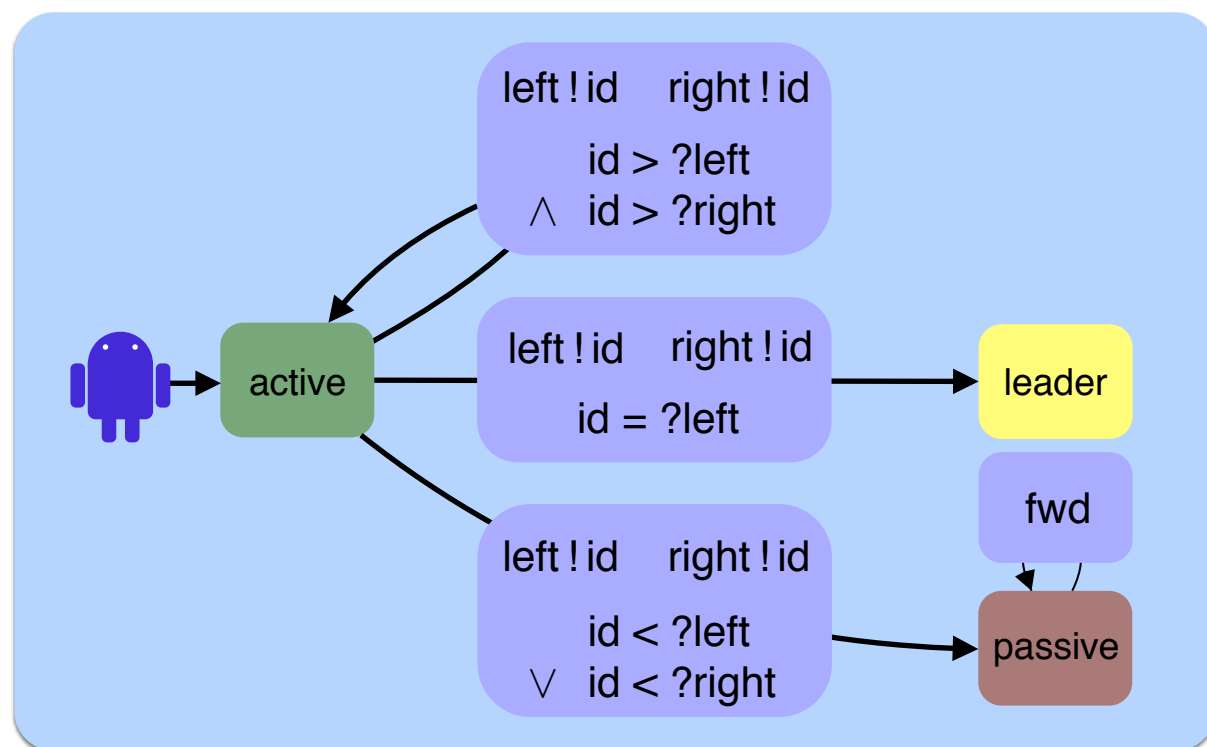
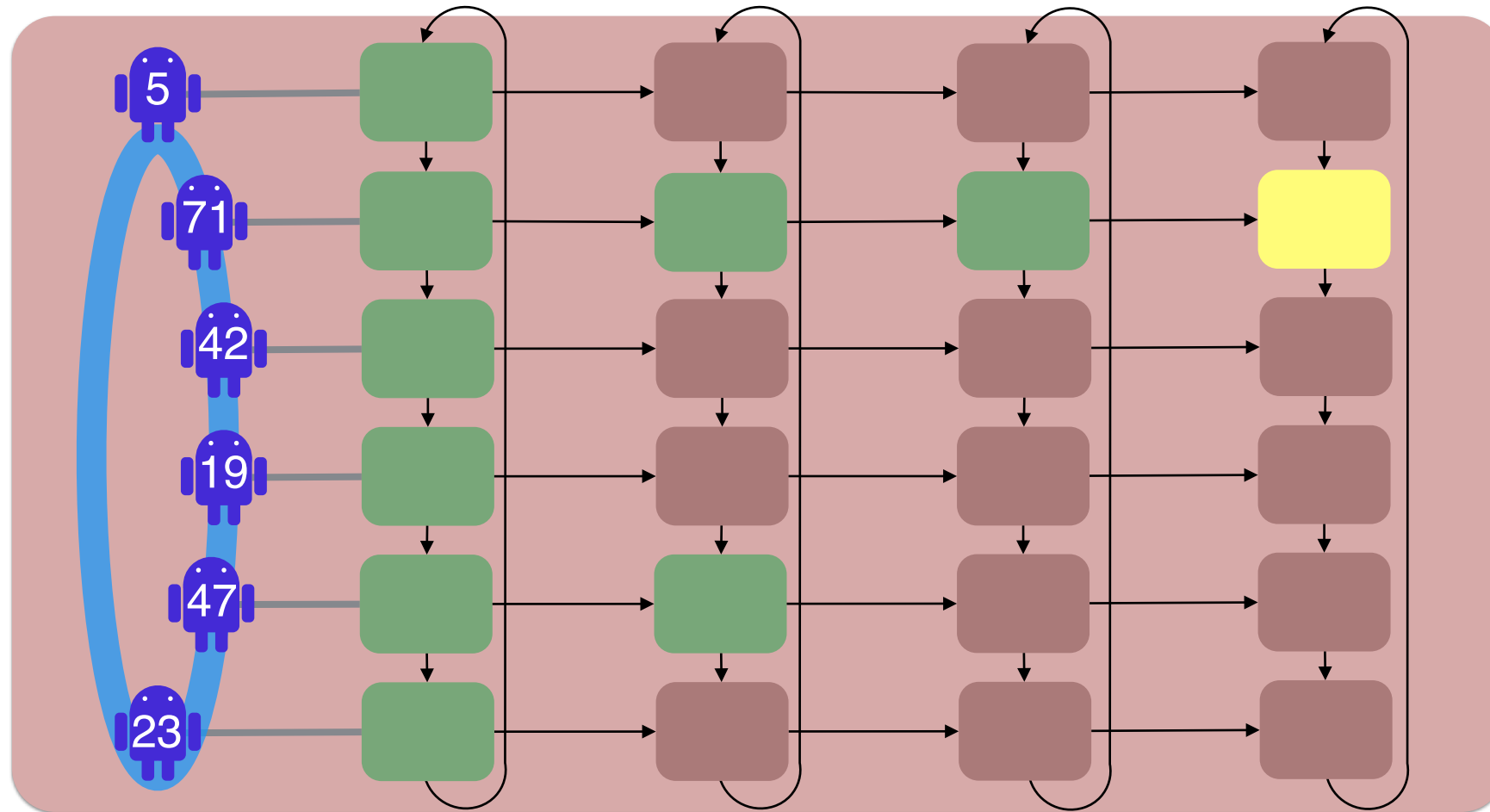
Reduction to automata?



Data abstraction

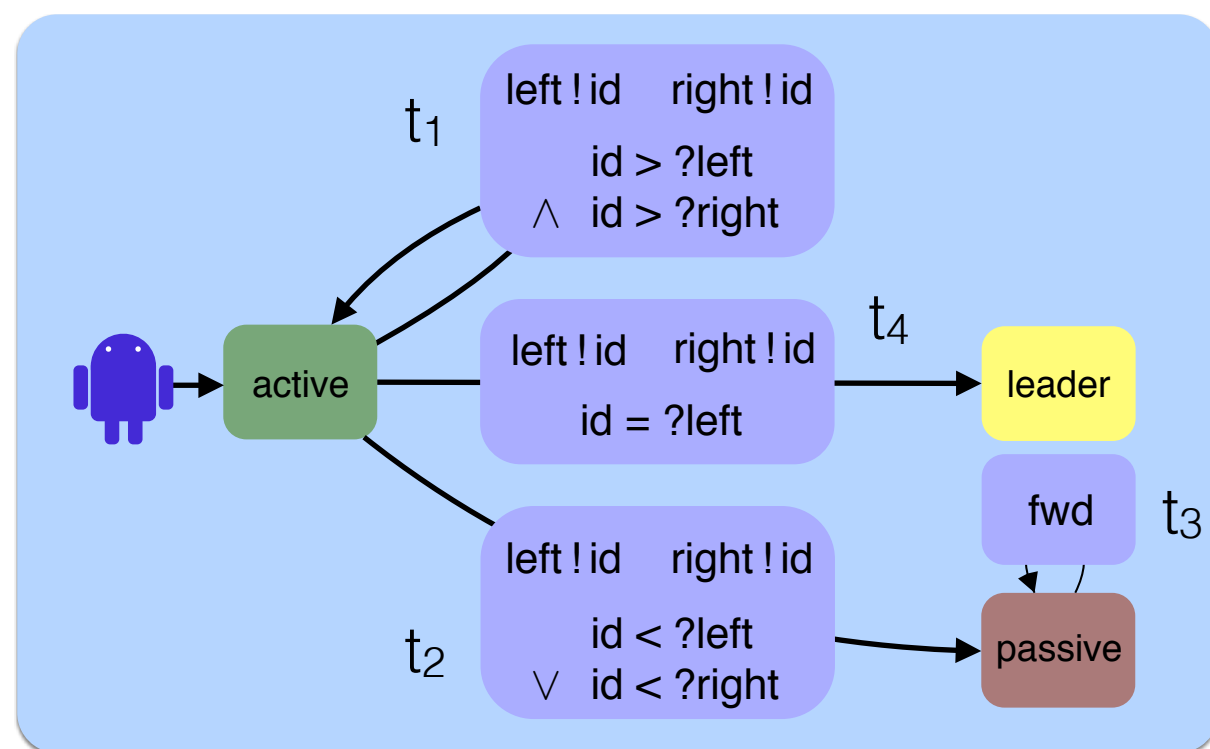
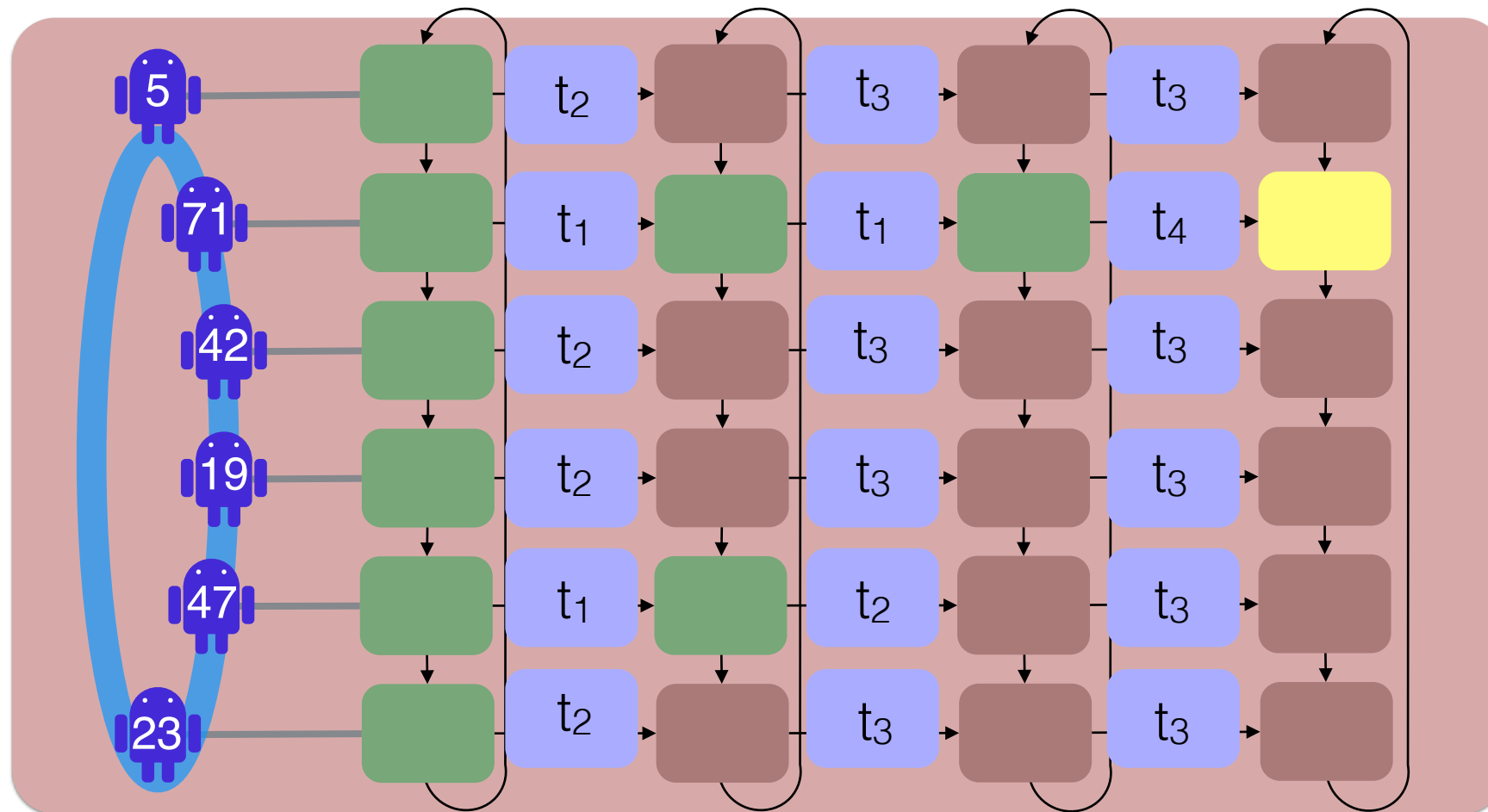


Data abstraction: symbolic runs + tracking data



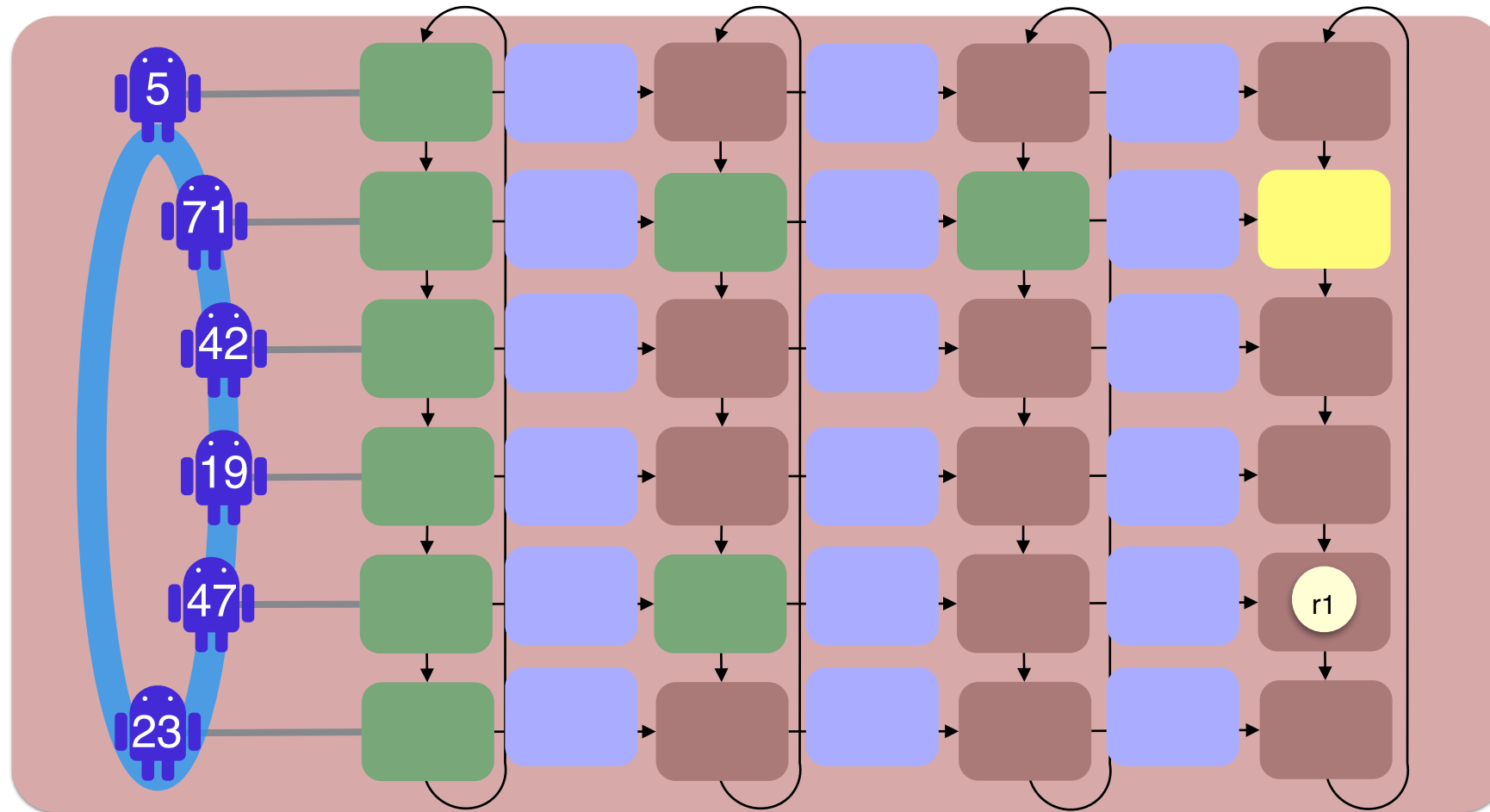
Distributed algorithm

Data abstraction: symbolic runs + tracking data



Distributed algorithm

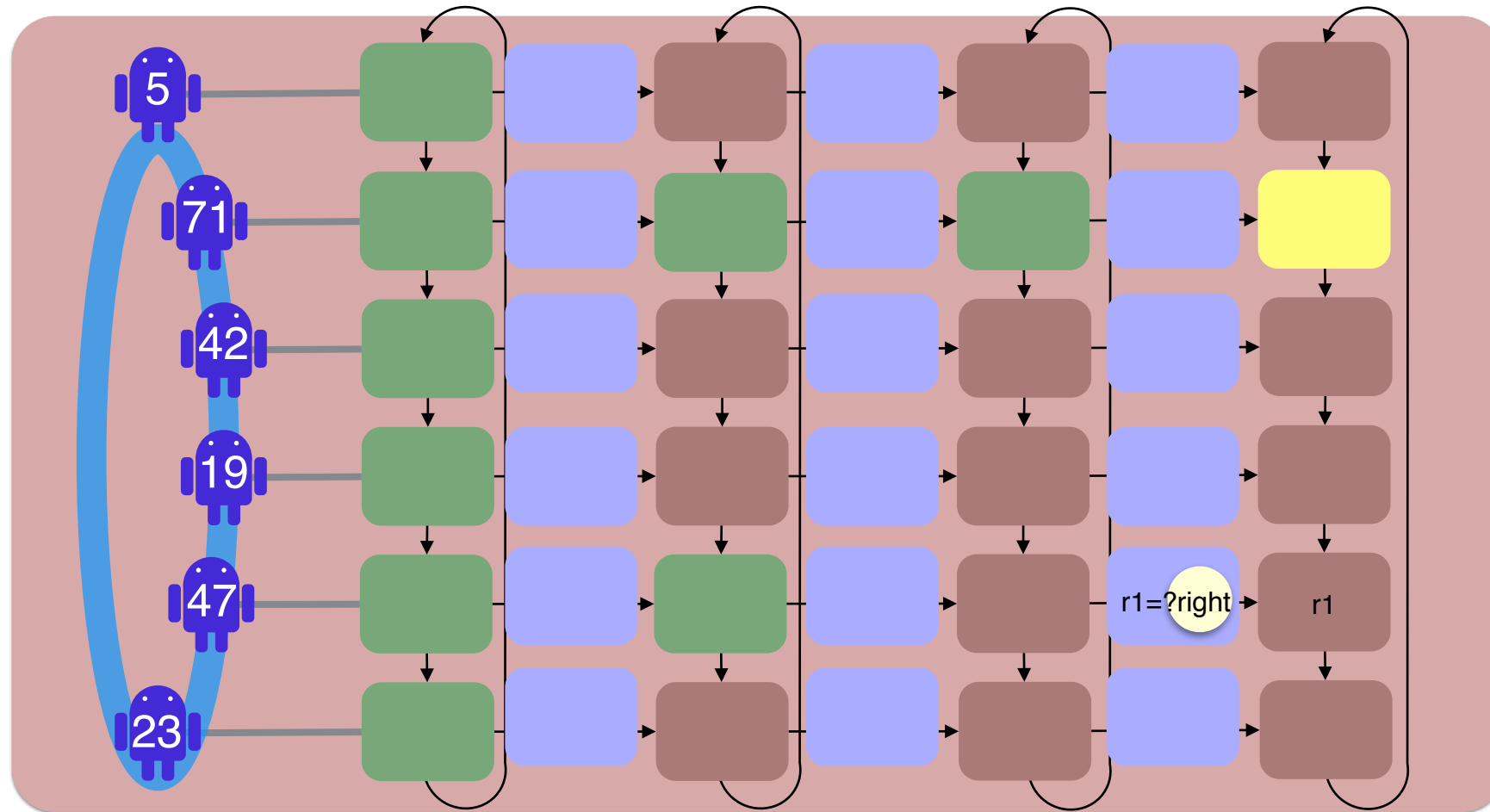
Data abstraction: symbolic runs + tracking data



- Register updates

Distributed algorithm

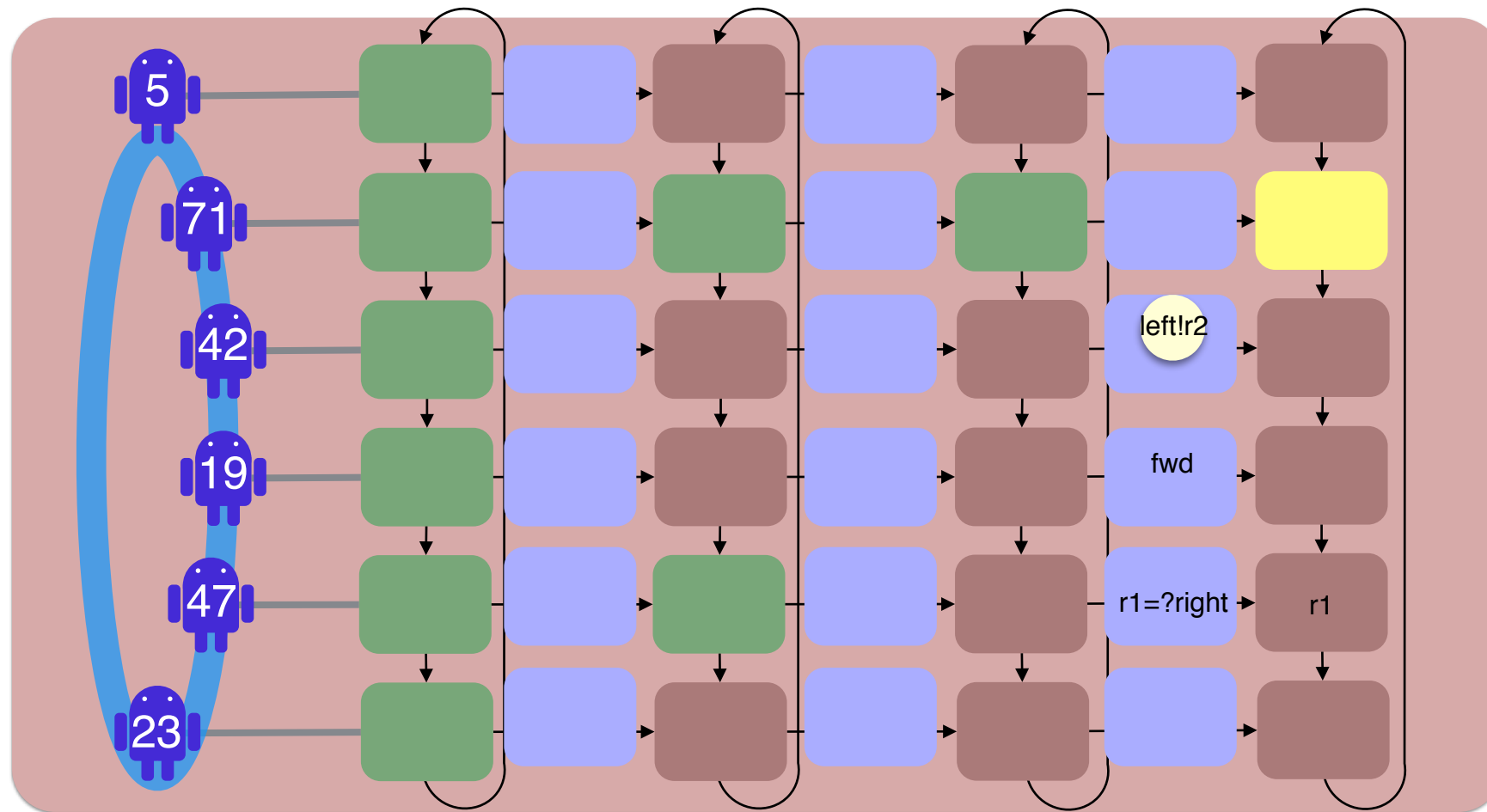
Data abstraction: symbolic runs + tracking data



- Register updates

Distributed algorithm

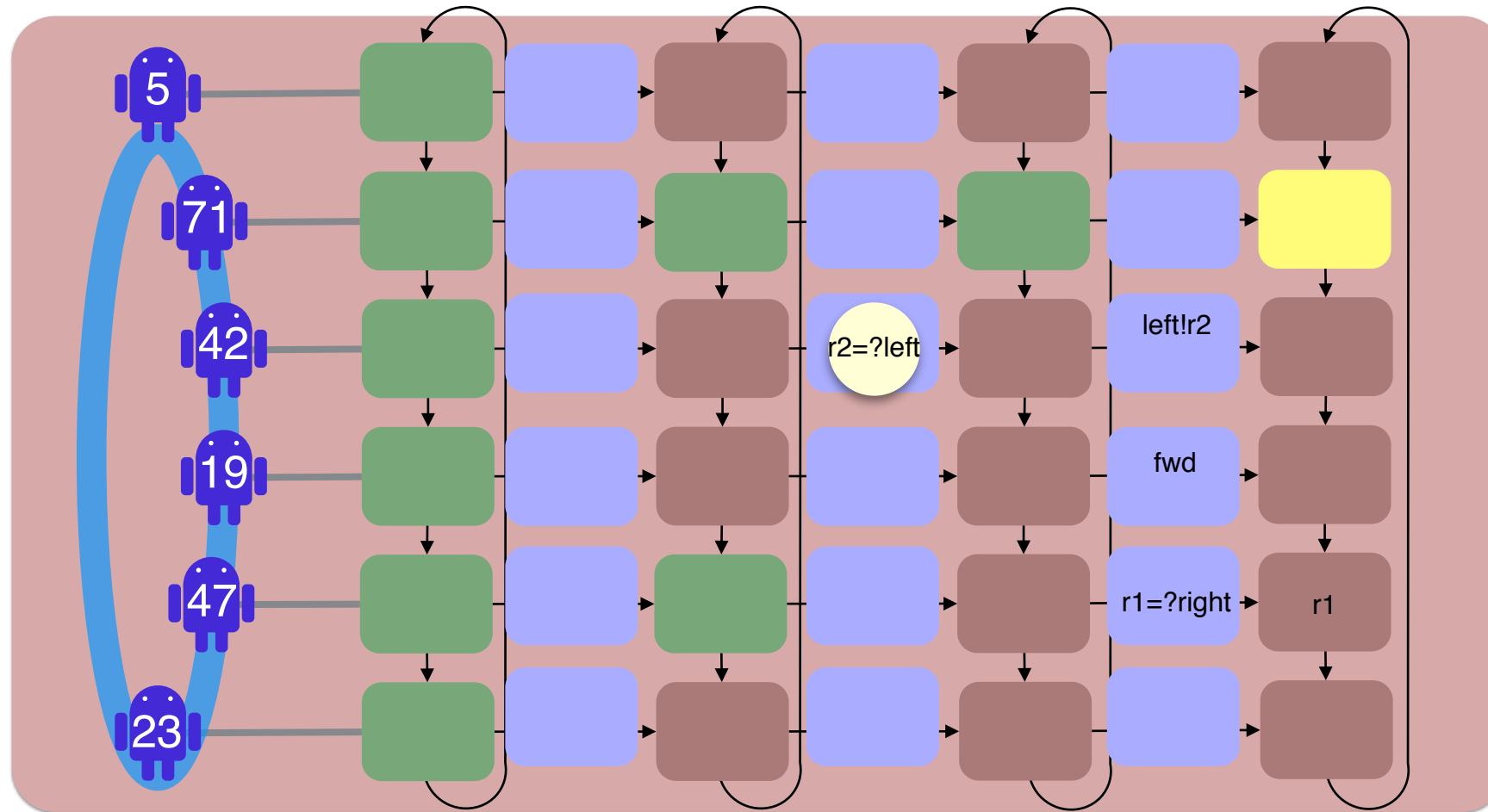
Data abstraction: symbolic runs + tracking data



- Register updates

Distributed algorithm

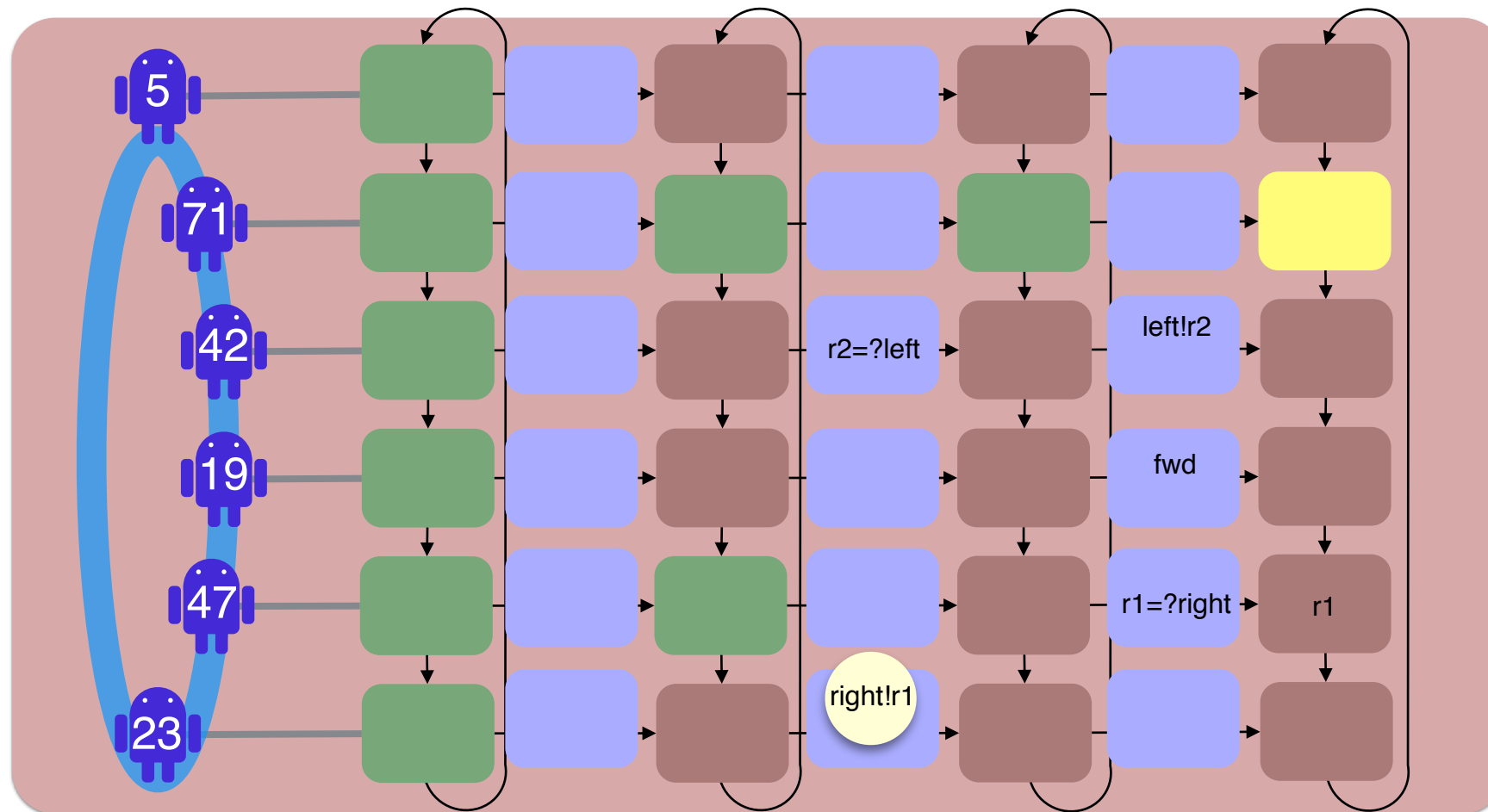
Data abstraction: symbolic runs + tracking data



- Register updates

Distributed algorithm

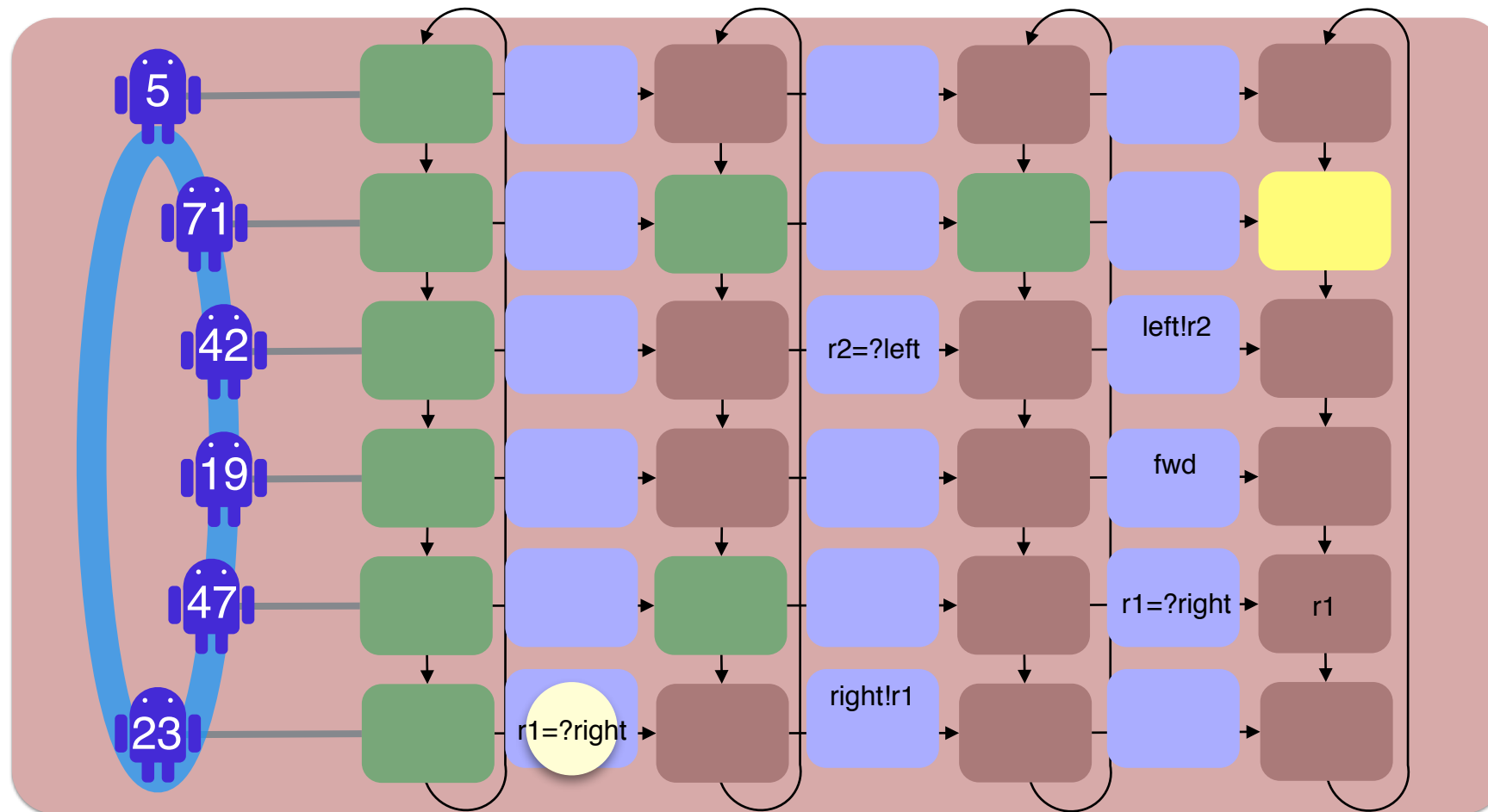
Data abstraction: symbolic runs + tracking data



- Register updates

Distributed algorithm

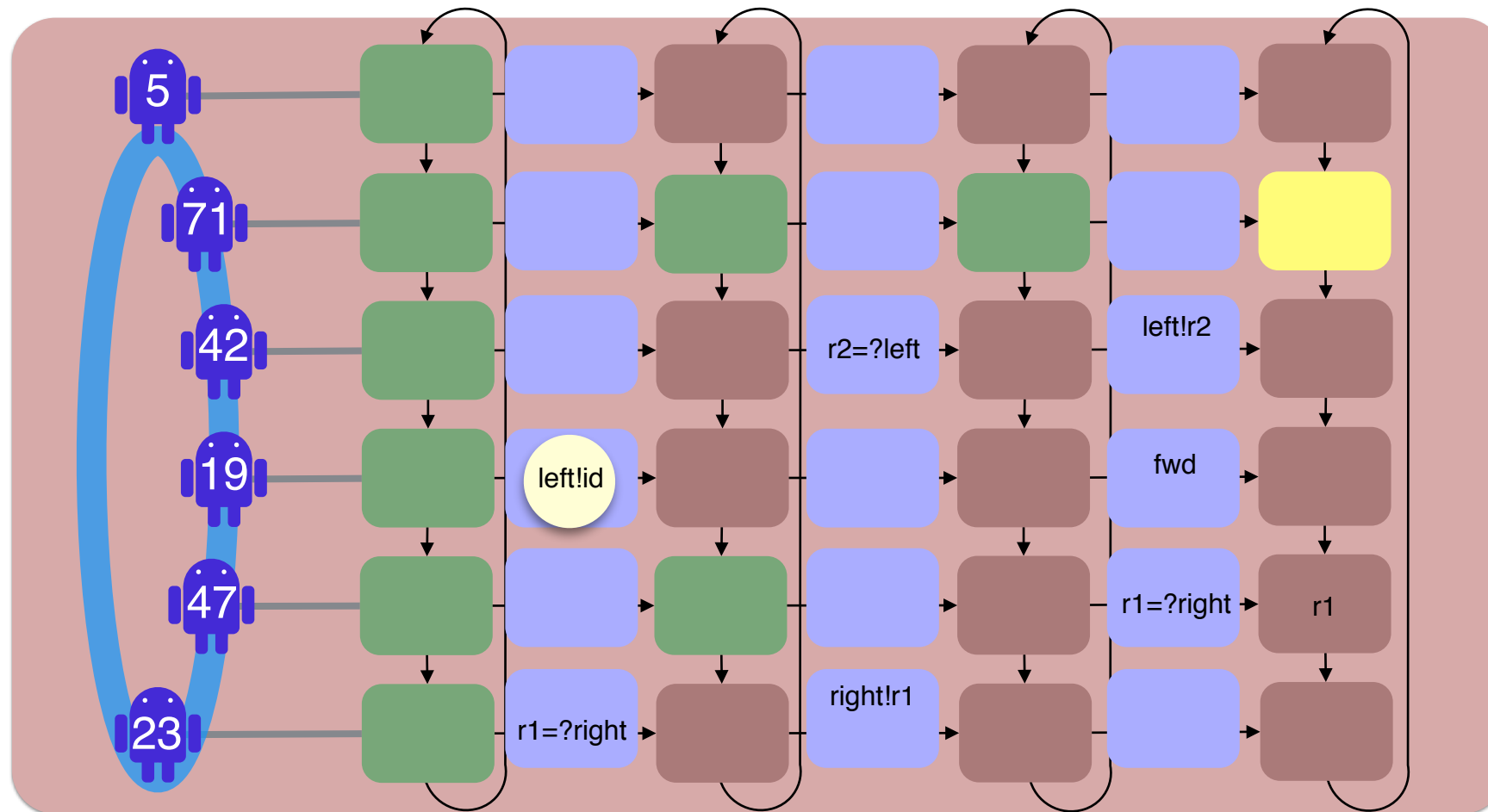
Data abstraction: symbolic runs + tracking data



- Register updates

Distributed algorithm

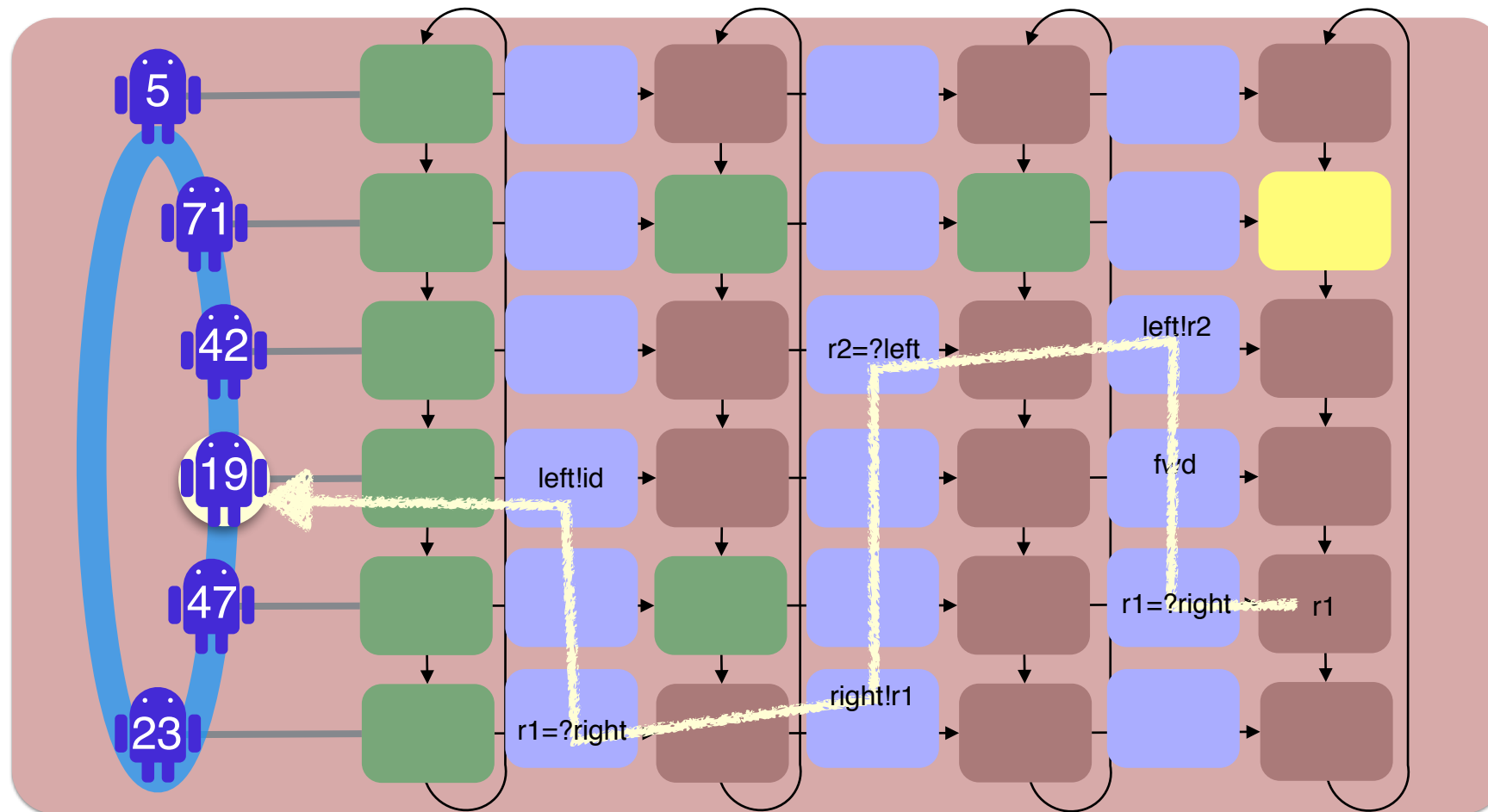
Data abstraction: symbolic runs + tracking data



- Register updates

Distributed algorithm

Data abstraction: symbolic runs + tracking data



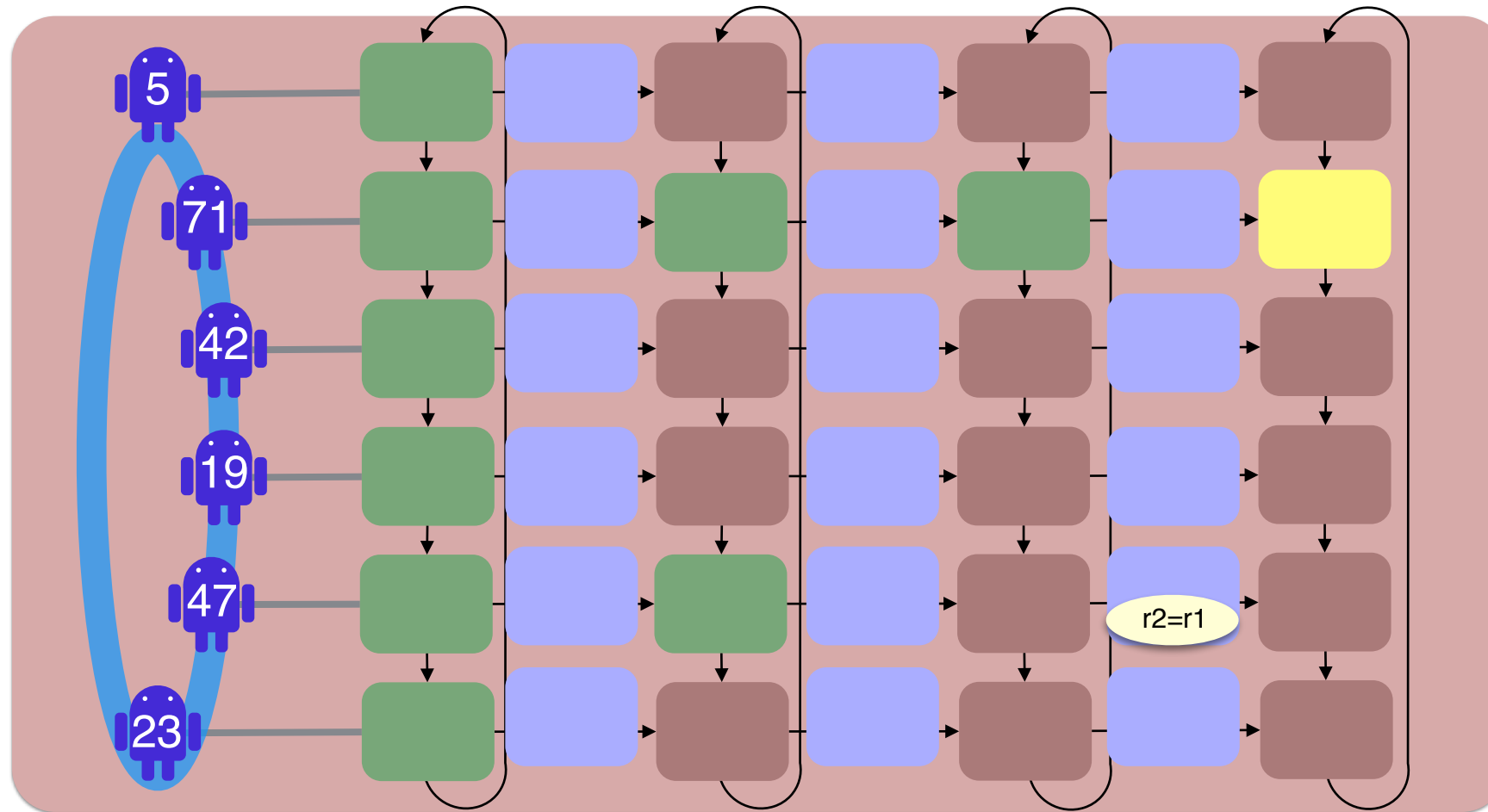
- Register updates

Distributed algorithm

(r_1, id) -path

can be expressed in PDL

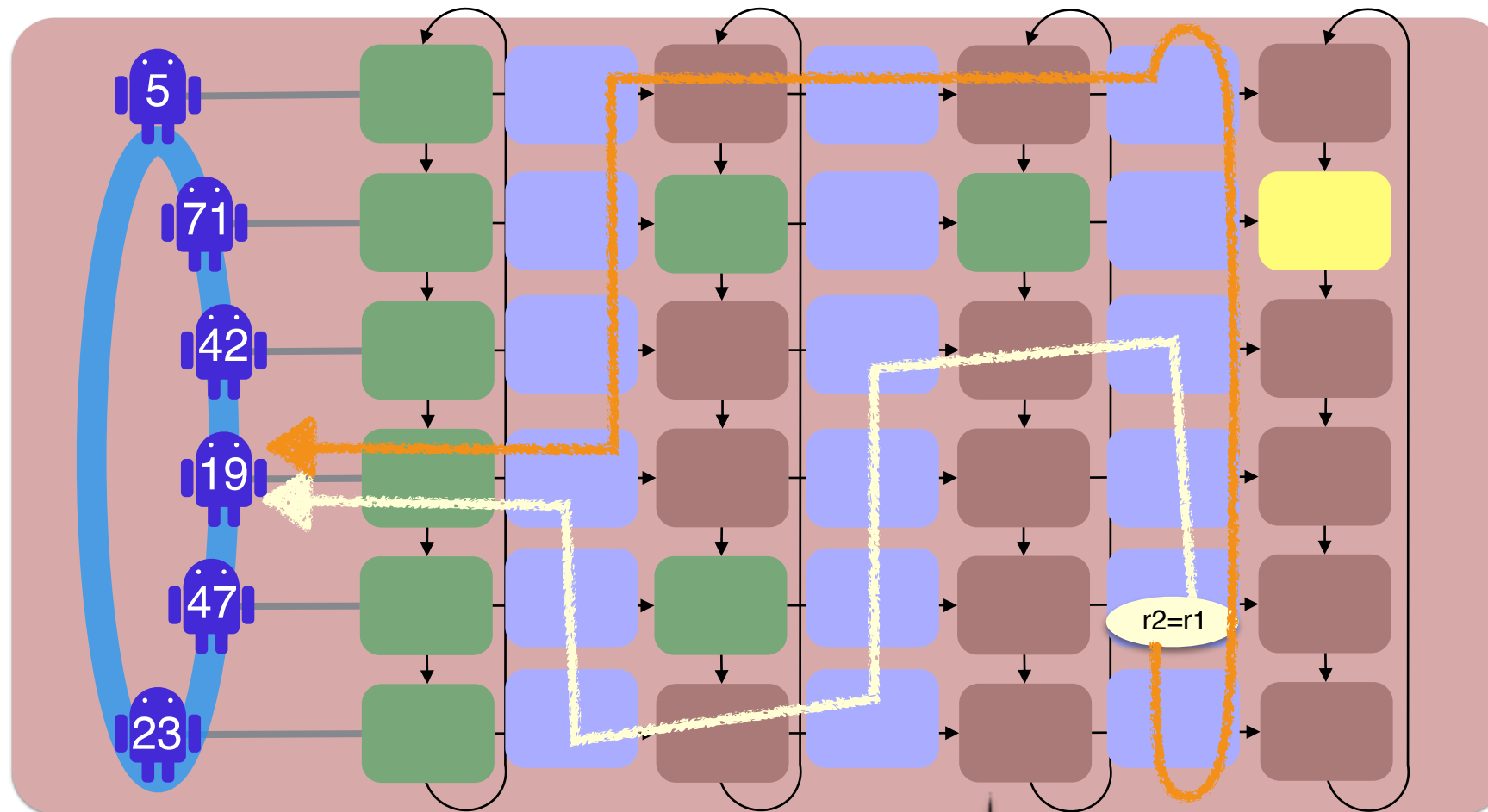
Data abstraction: symbolic runs + tracking data



- Register updates
- Register equality check

Distributed algorithm

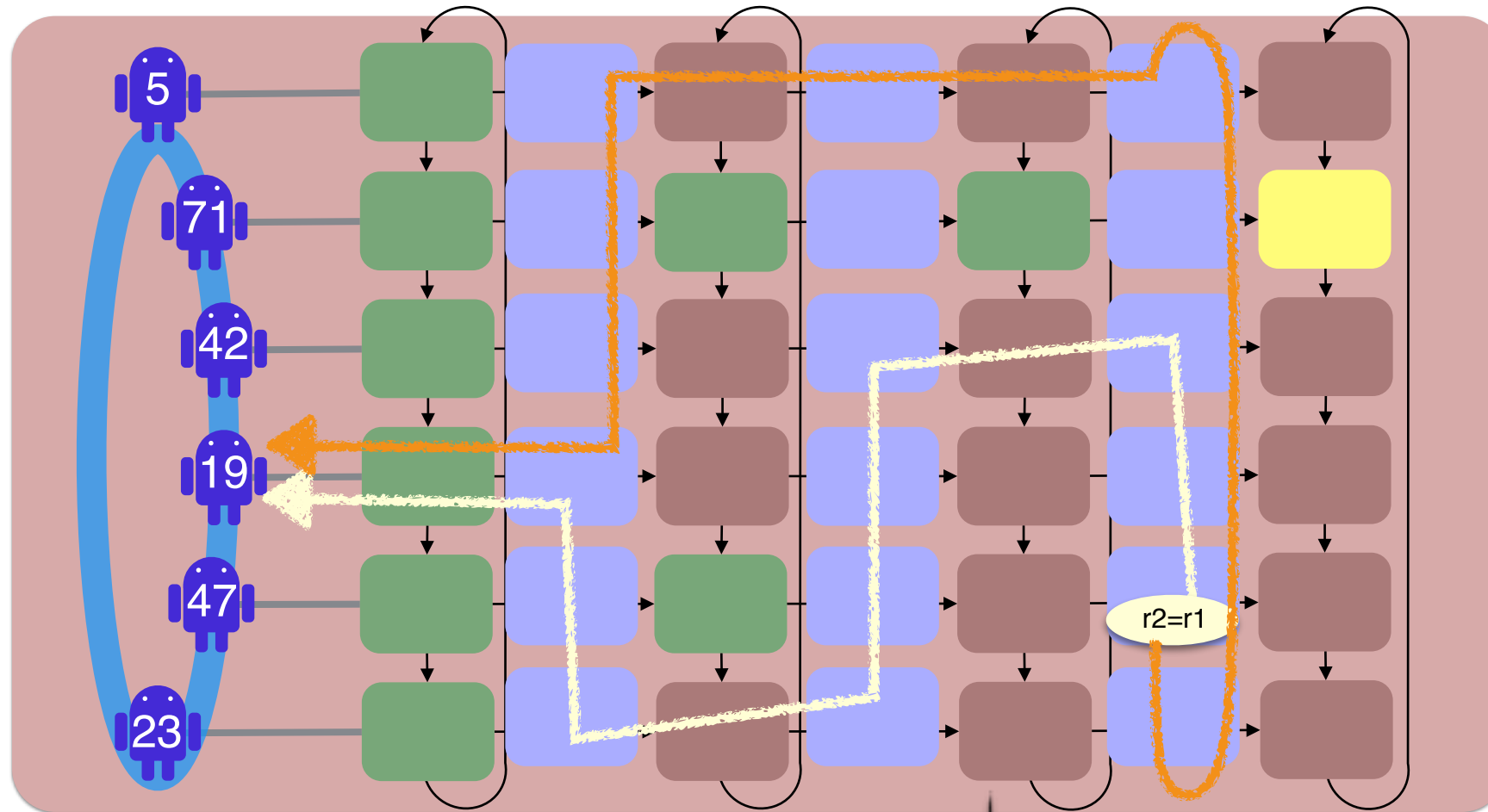
Data abstraction: symbolic runs + tracking data



- Register updates
- Register equality check

$\pi_1:(r_1, id)\text{-path}$
 $\pi_2:(r_2, id)\text{-path}$

Data abstraction: symbolic runs + tracking data



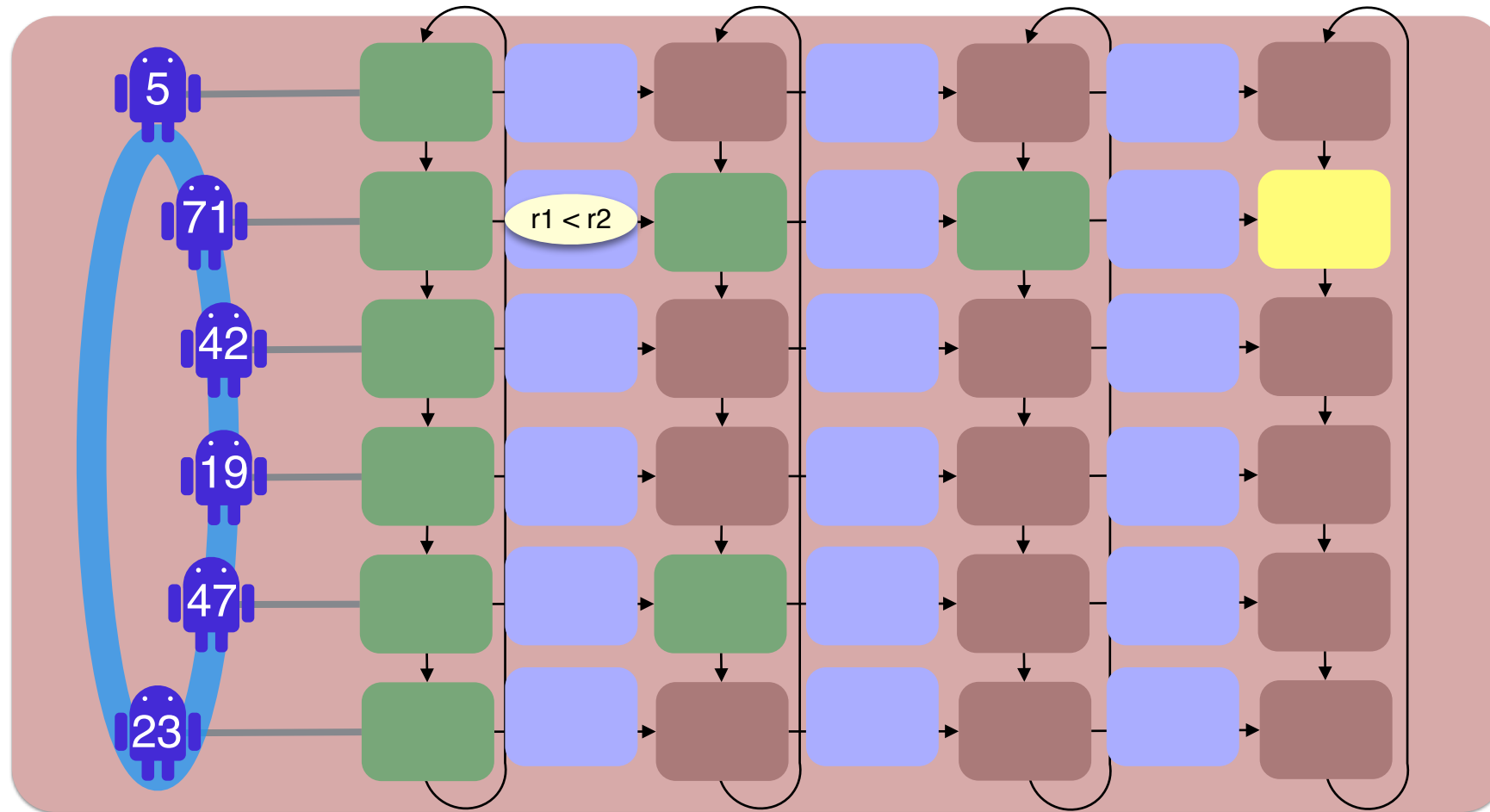
- Register updates
- Register equality check

Distributed algorithm

$\pi_1:(r_1, id)\text{-path}$
 $\pi_2:(r_2, id)\text{-path}$
 $\text{loop}(\pi_1 \pi_2^{-1})$

can be expressed
in PDL with loop

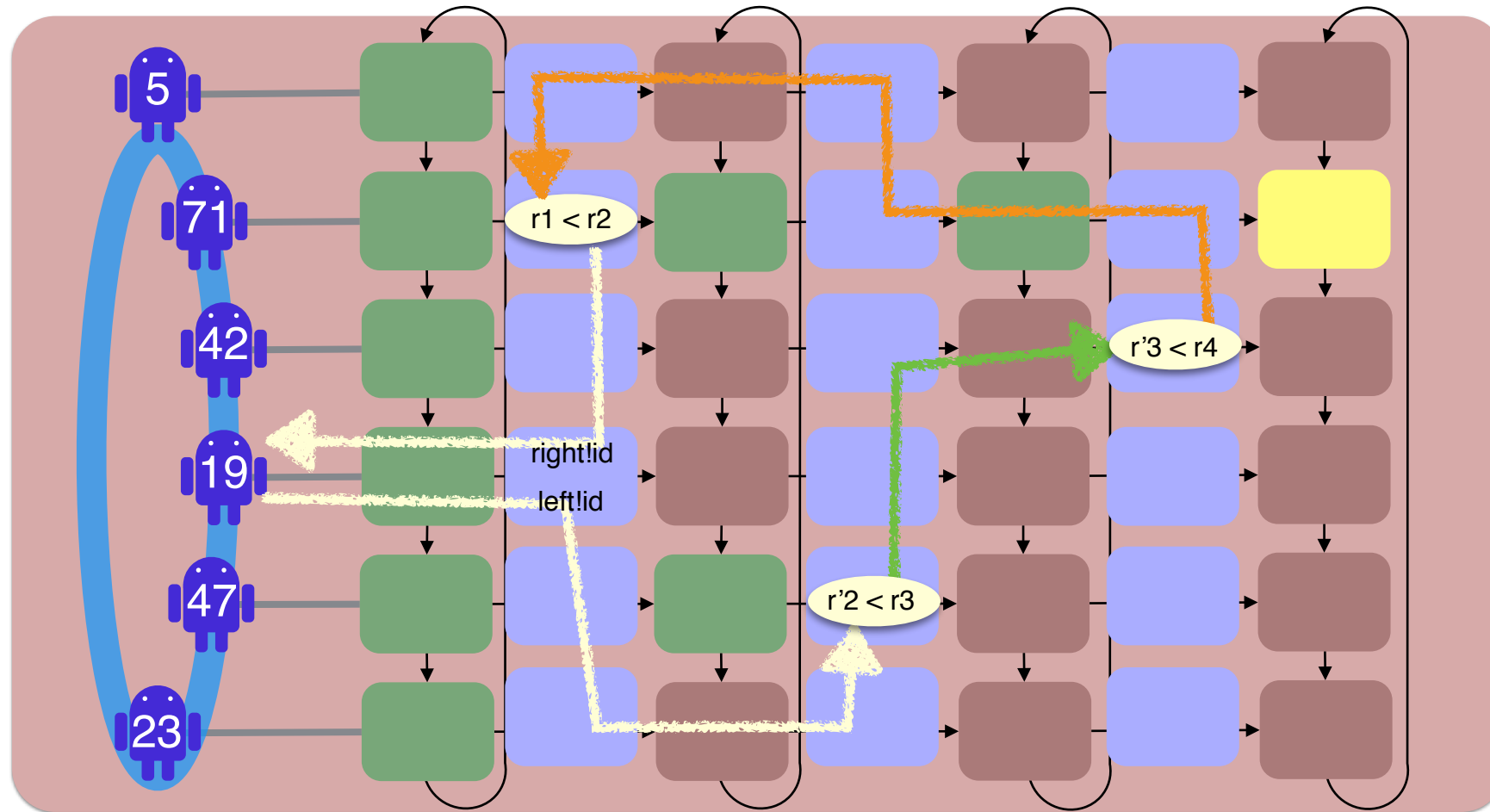
Data abstraction: symbolic runs + tracking data



- Register updates
- Register equality check
- Register comparison

Distributed algorithm

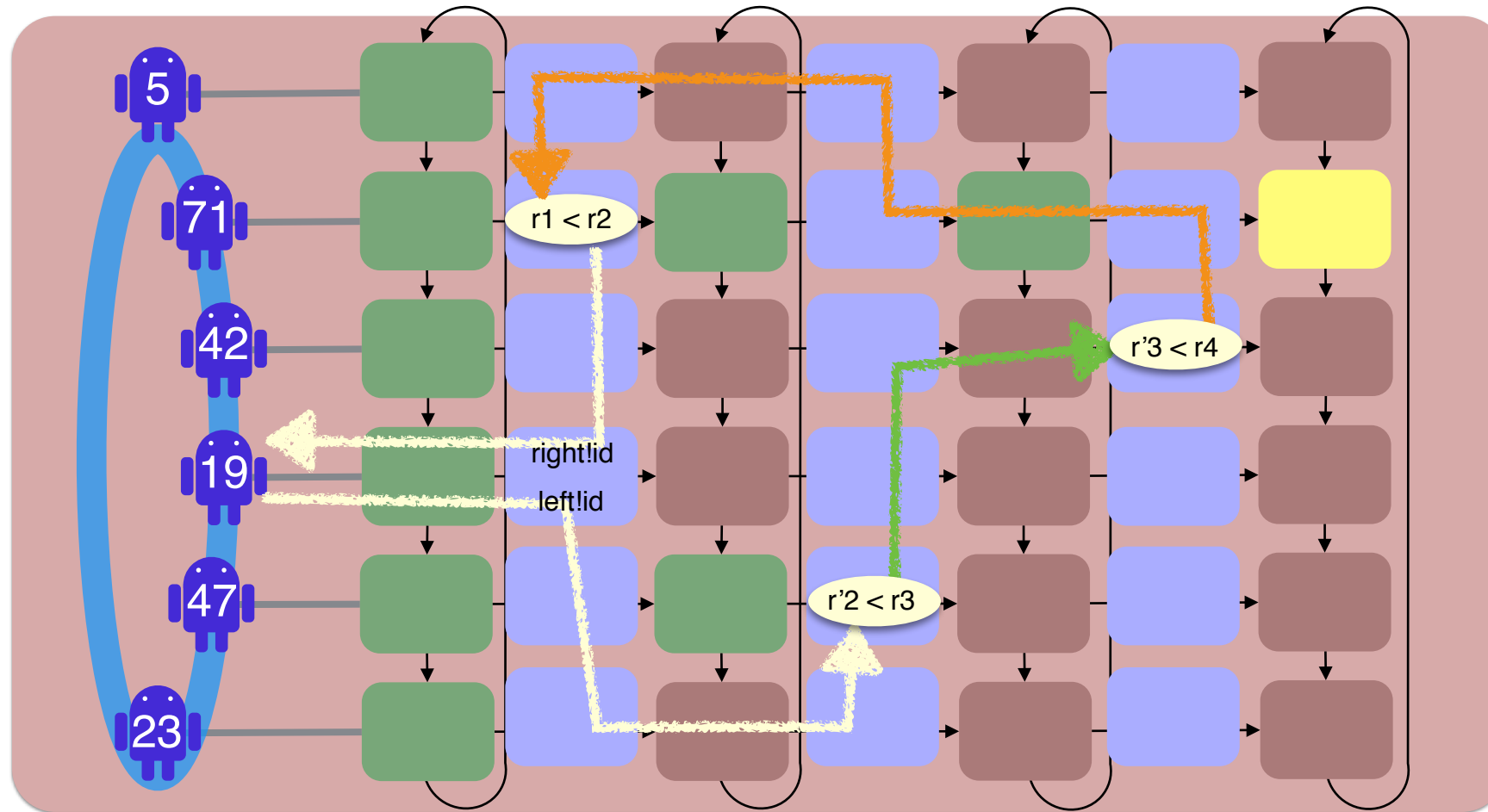
Data abstraction: symbolic runs + tracking data



- Register updates
- Register equality check
- Register comparison

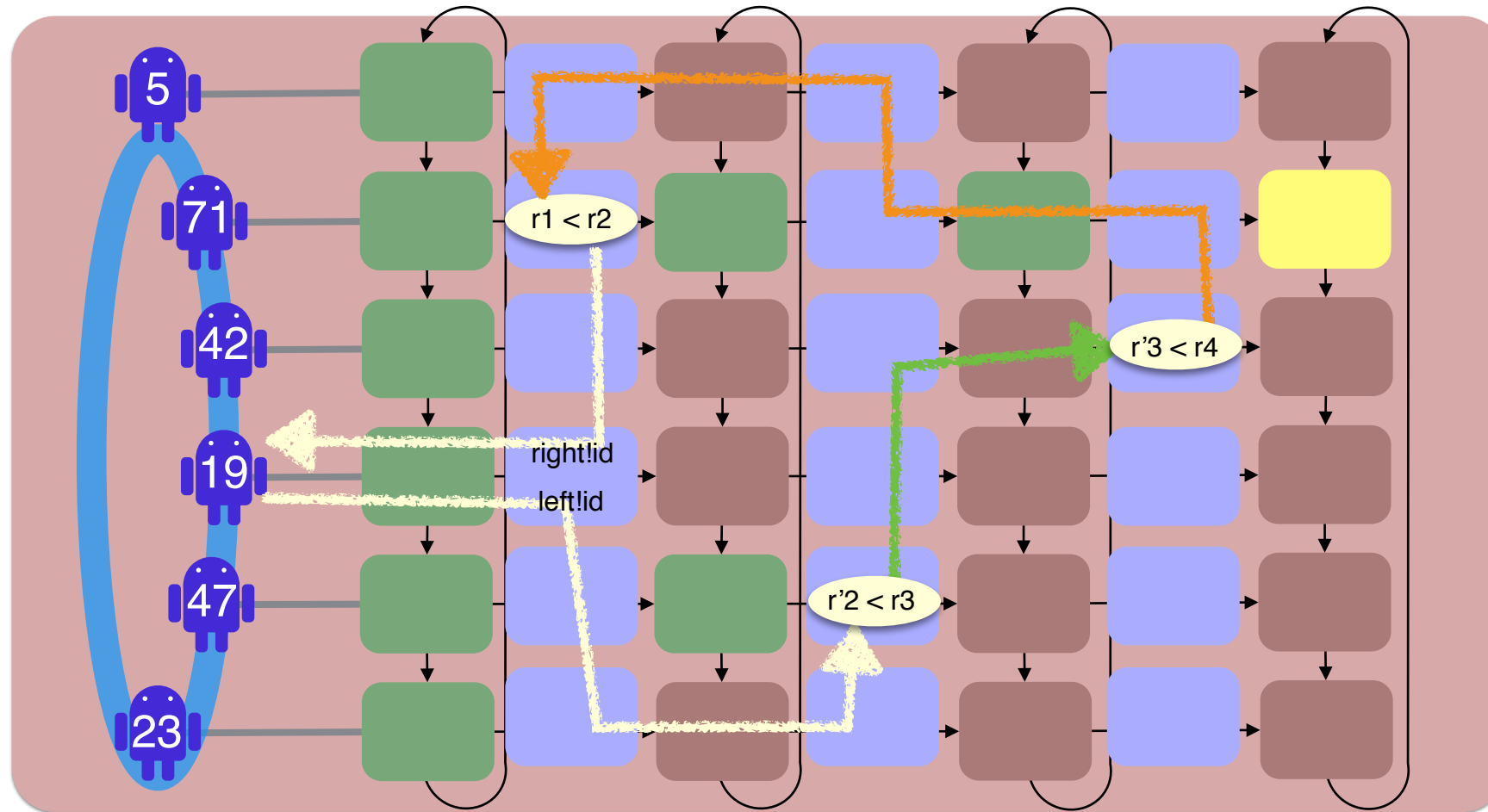
Distributed algorithm

Data abstraction: symbolic runs + tracking data



- If there is a loop, no pids assignment can turn the symbolic cylinder into a valid run.
- If no such loops, then there are pids that allow a valid realization of the abstract grid

Data abstraction: symbolic runs + tracking data

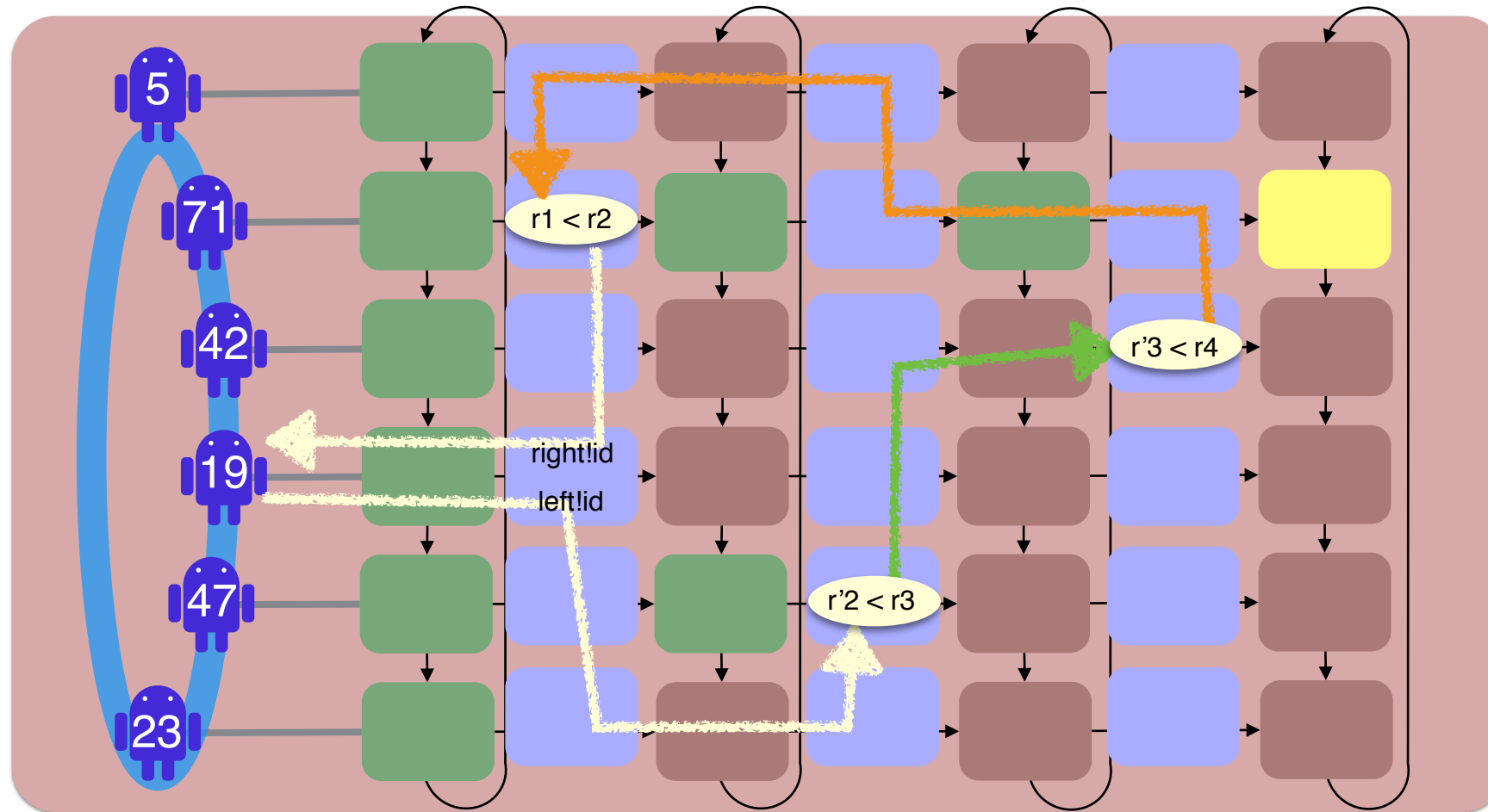


No loop of the form

$r_{i0} < r_{i1}; (r_{i1}, r_{i2})\text{-path}; r_{i2} < r_{i3}; (r_{i3}, r_{i4})\text{-path}; \dots ; r_{in} < r_{i0}$

- If there is a loop, no pids assignment can turn the symbolic cylinder into a valid run.
- If no such loops, then there are pids that allow a valid realization of the abstract grid

Data abstraction: symbolic runs + tracking data



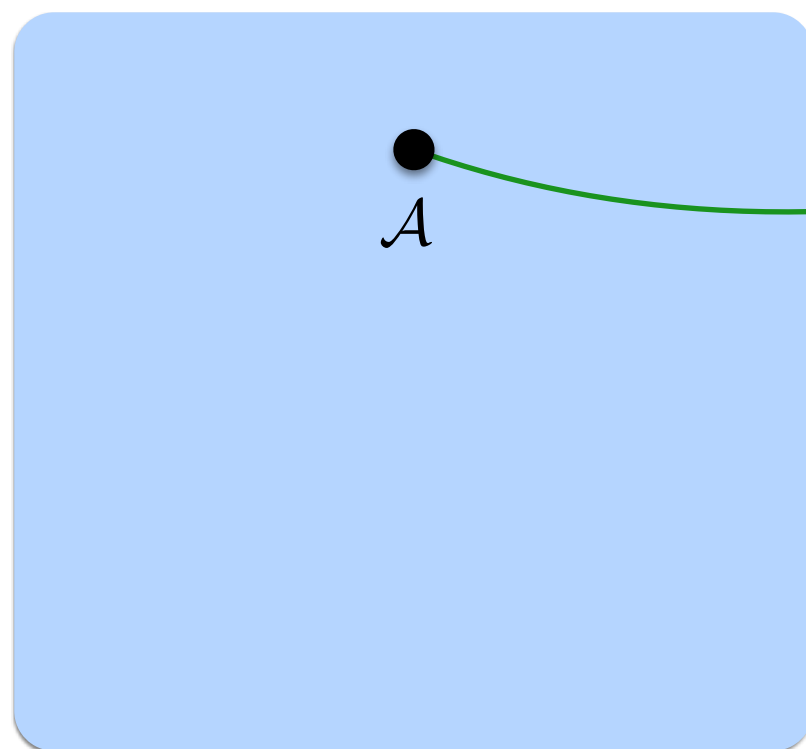
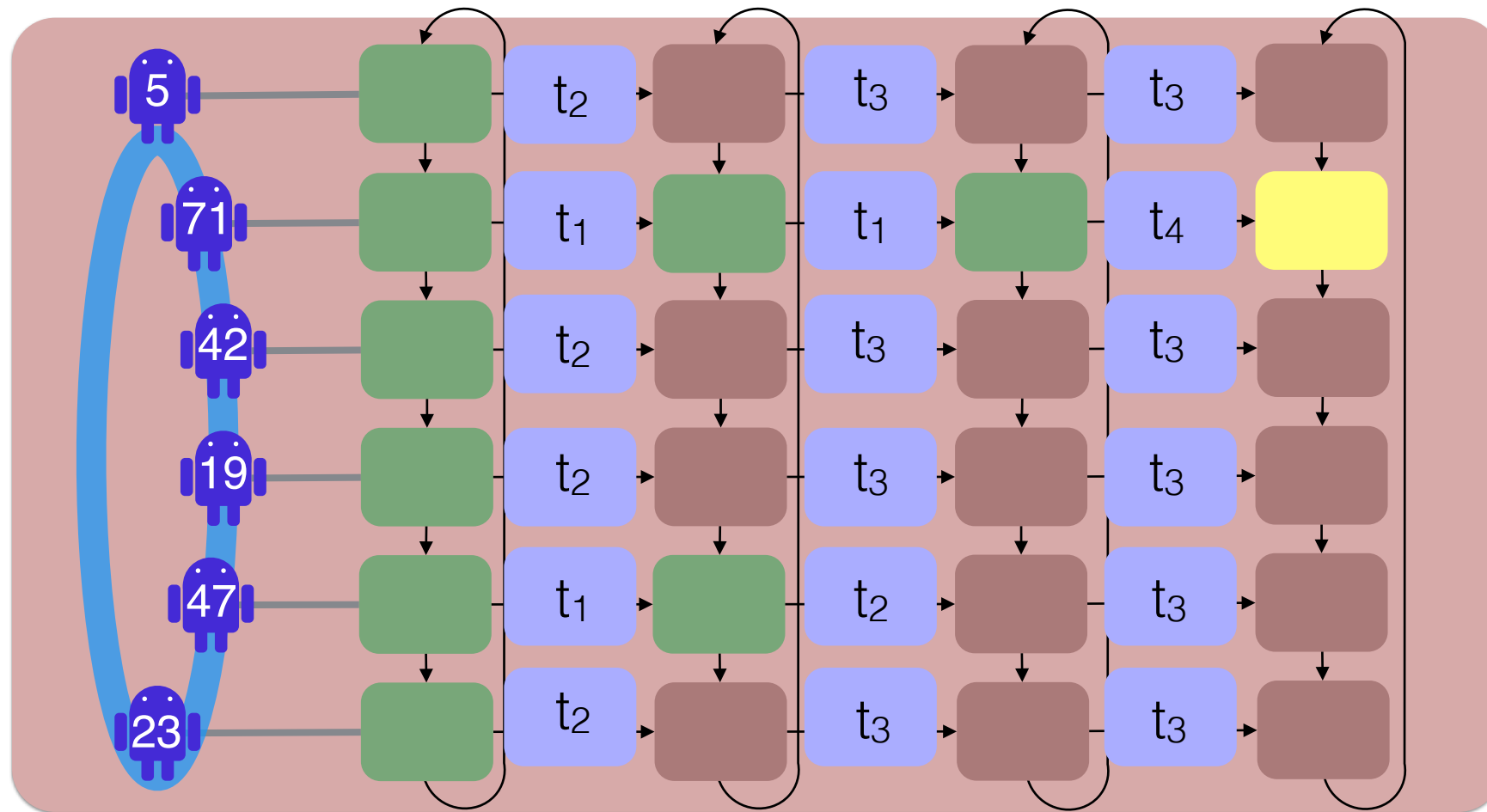
No loop of the form

$r_{i0} < r_{i1}; (r_{i1}, r_{i2})\text{-path}; r_{i2} < r_{i3}; (r_{i3}, r_{i4})\text{-path}; \dots ; r_{in} < r_{i0}$

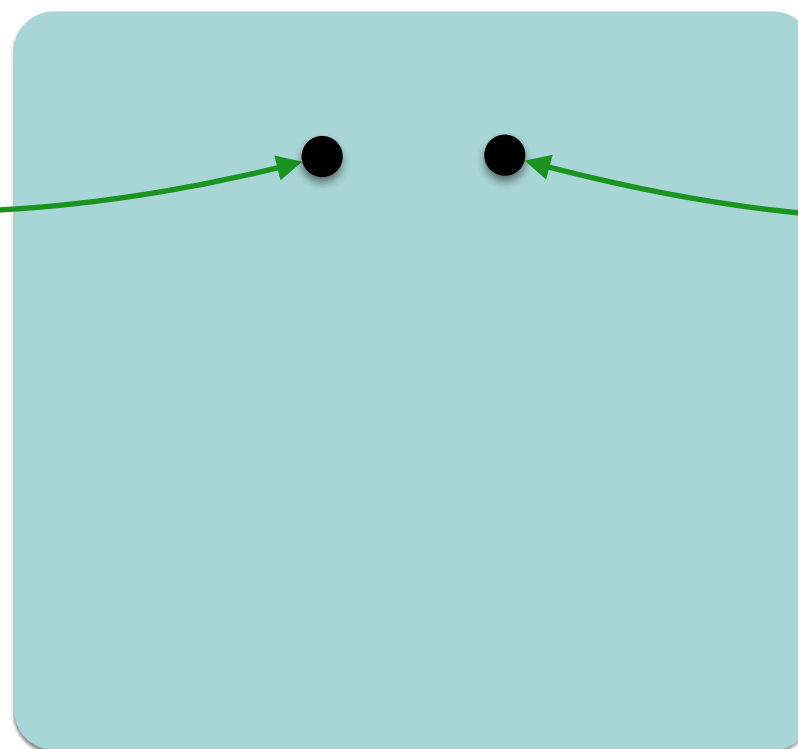
- Register updates
- Register equality check
- Register comparison

can be expressed
in PDL with loop

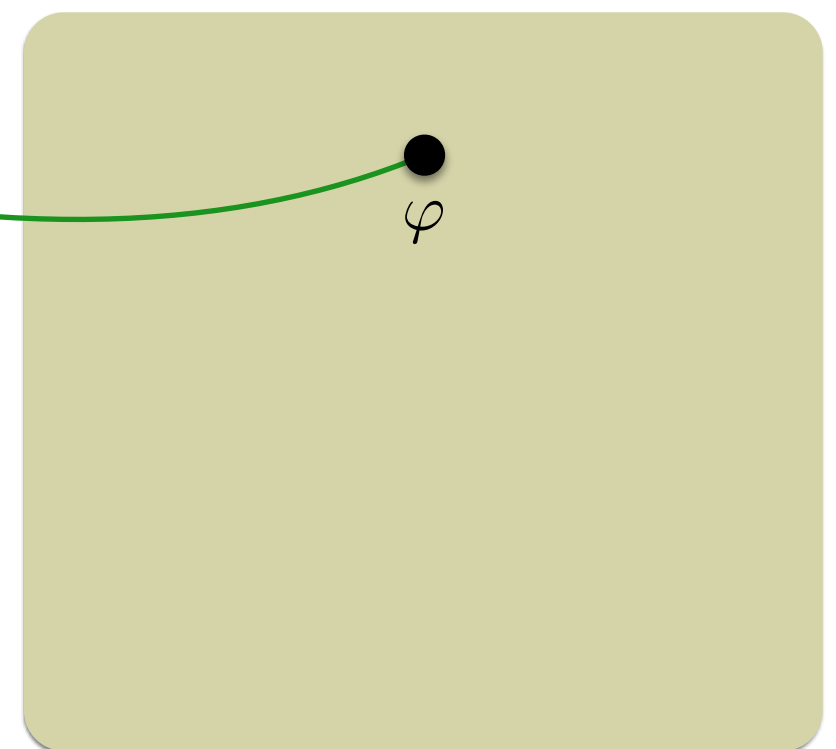
Data abstraction: symbolic runs + tracking data



Distributed algorithm

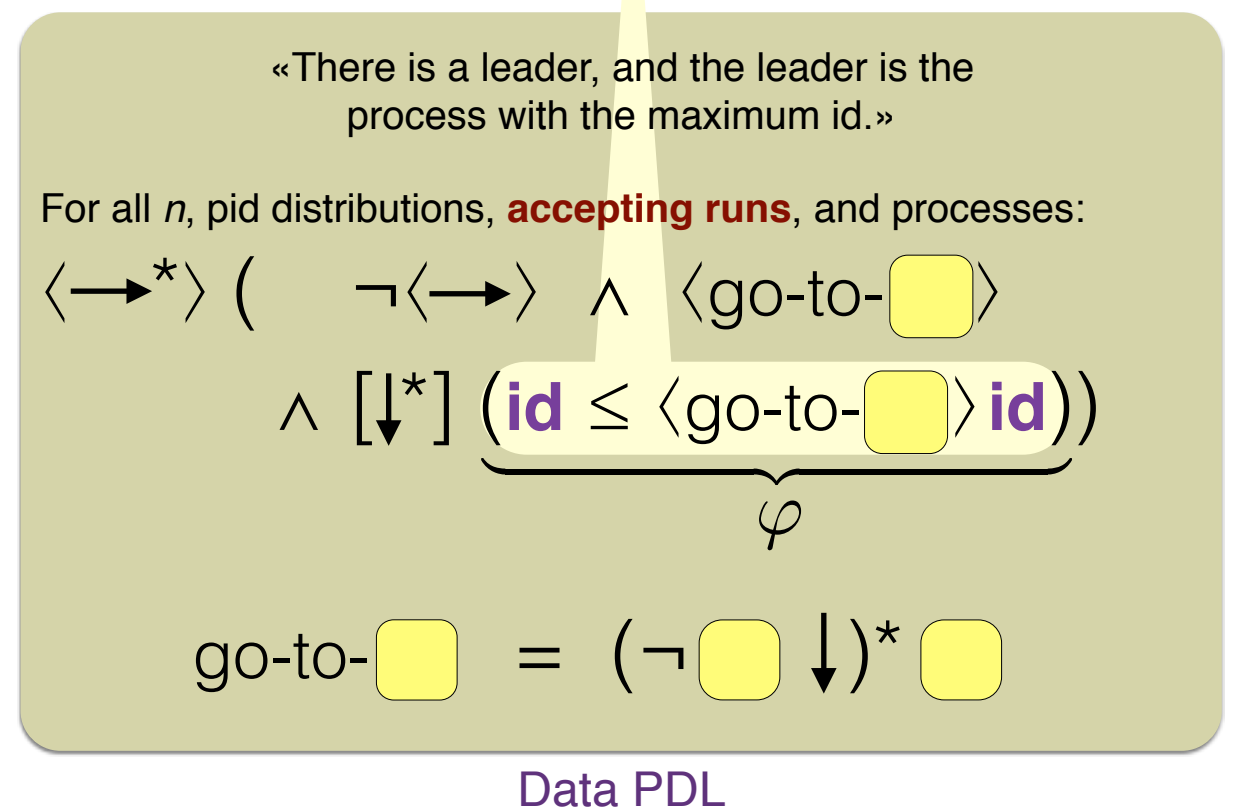
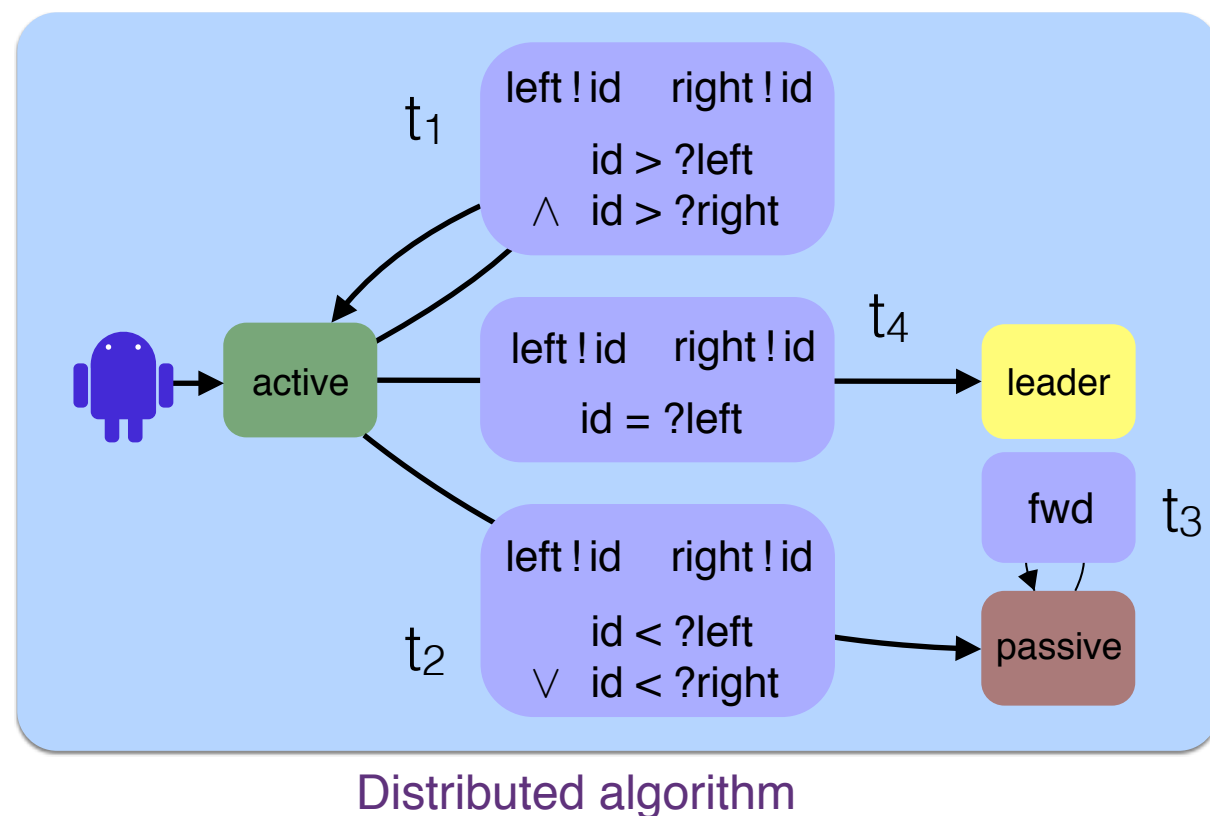
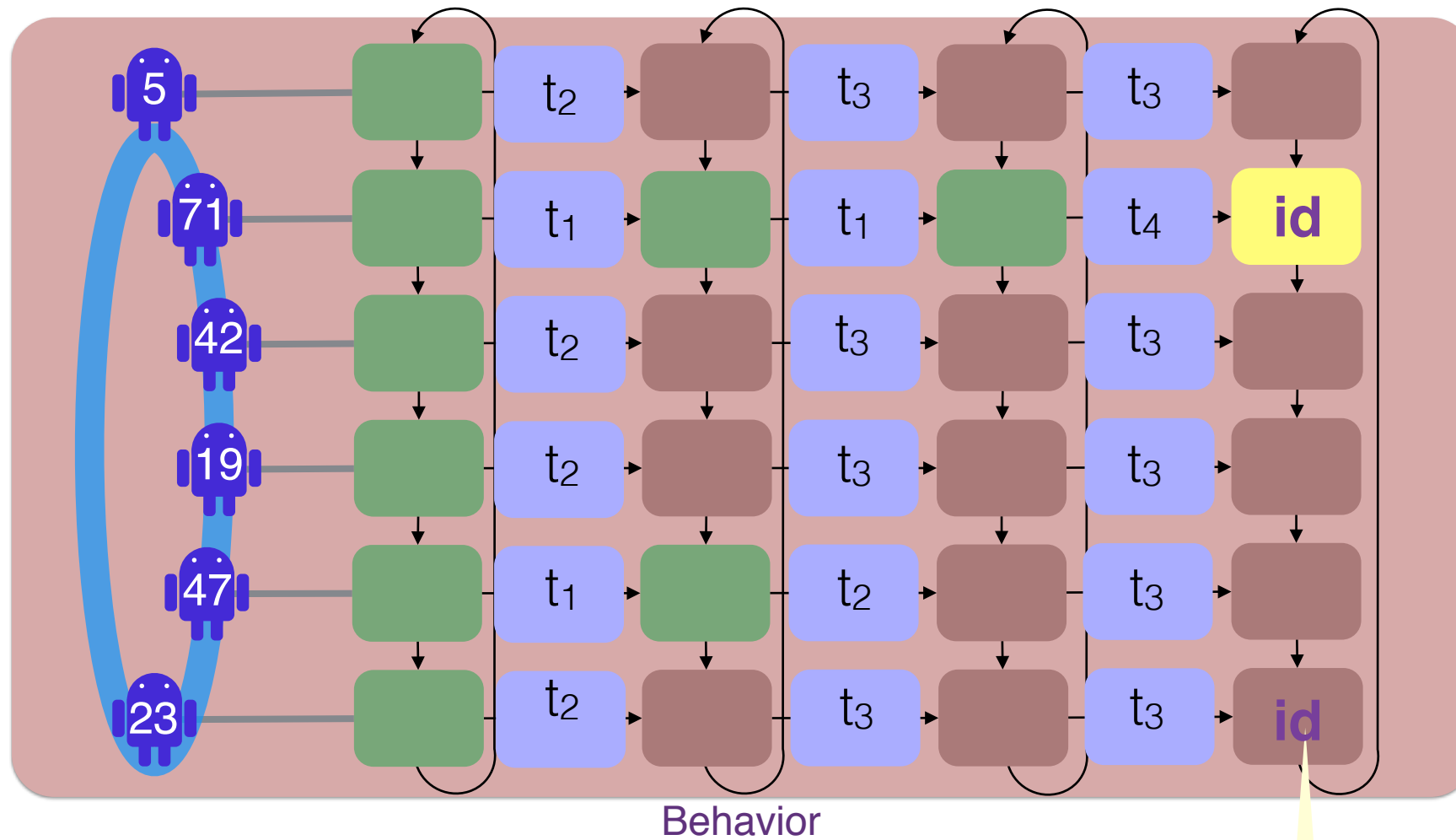


PDL with loop (over finite alphabet)

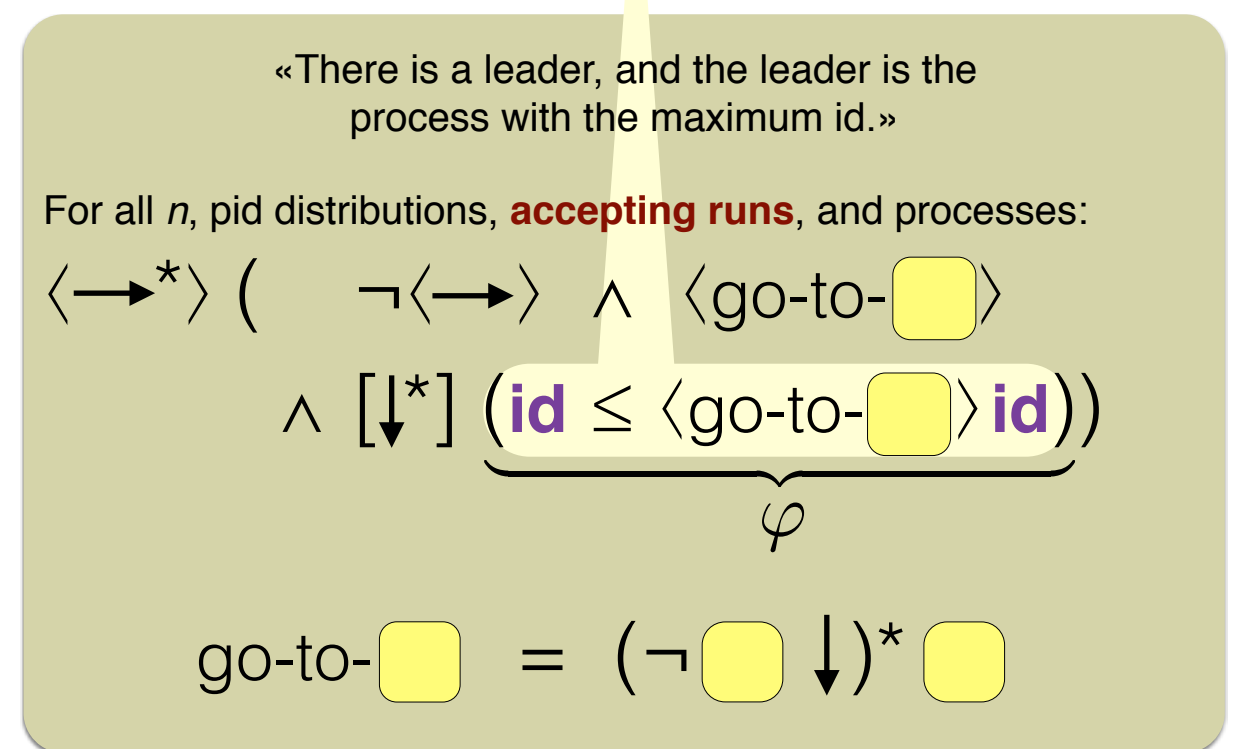
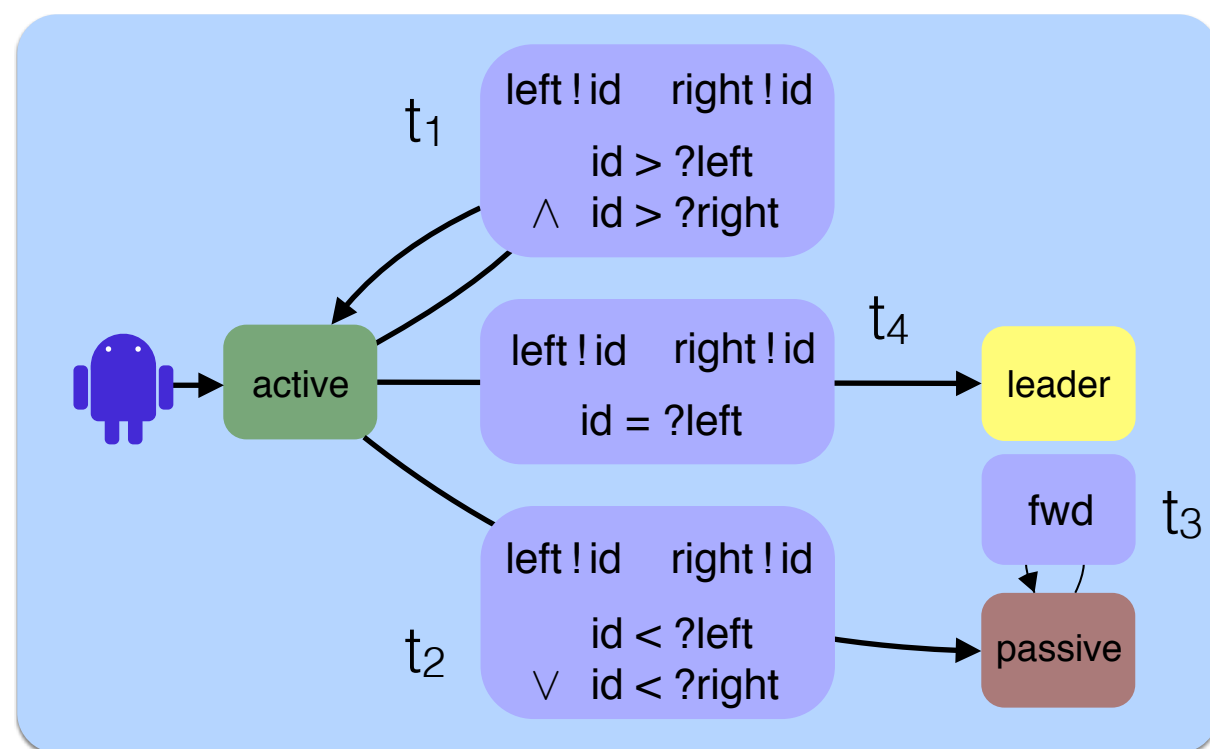
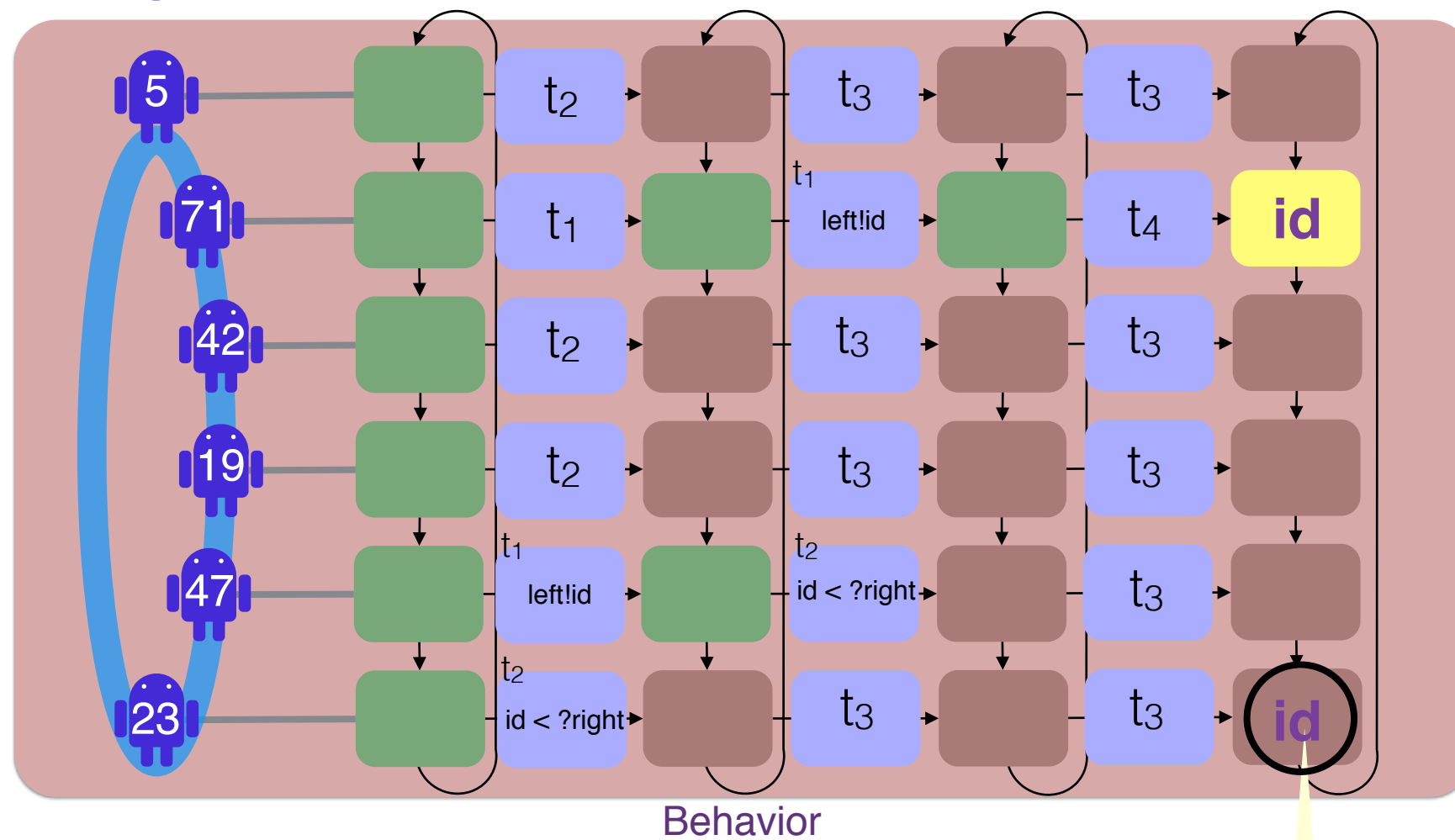


Data PDL

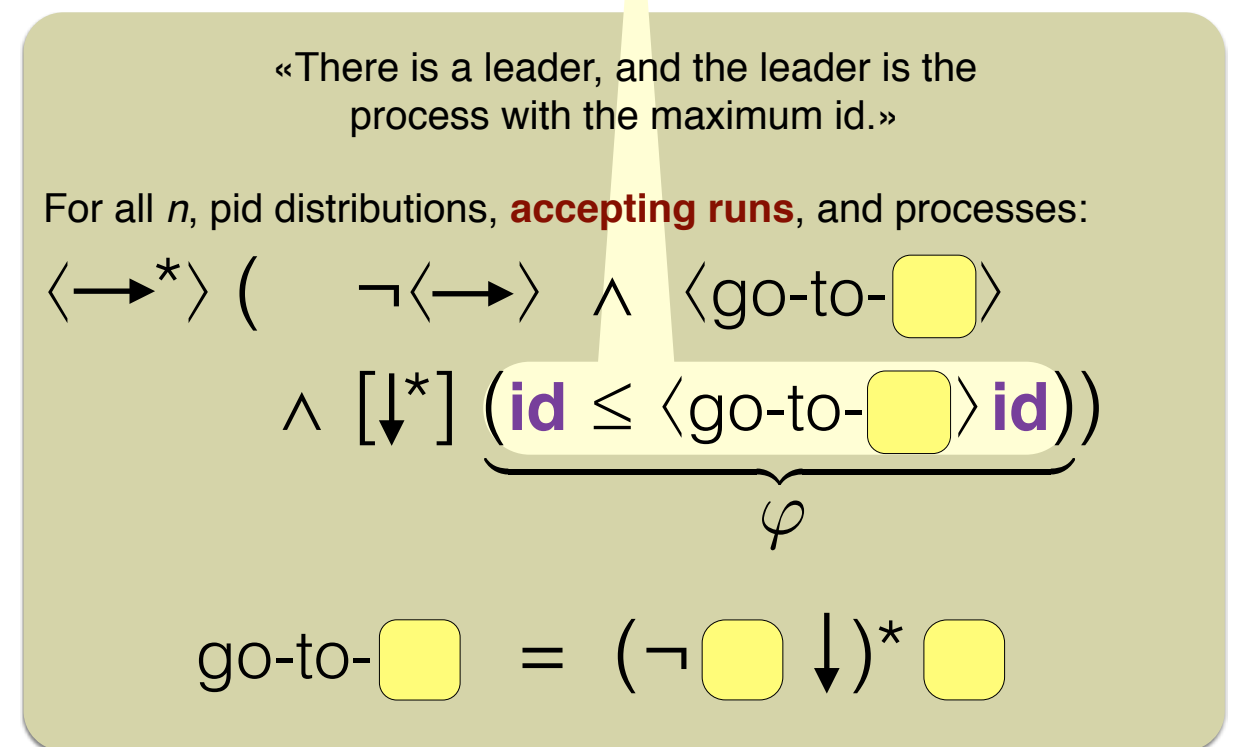
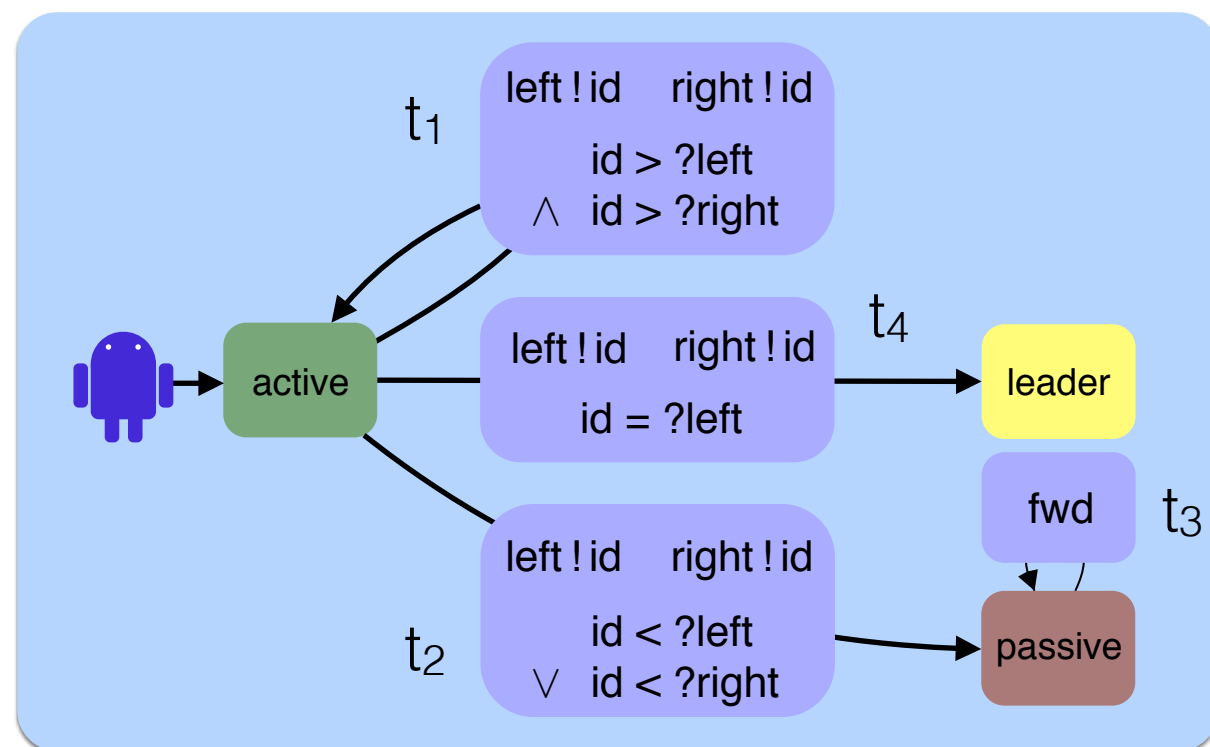
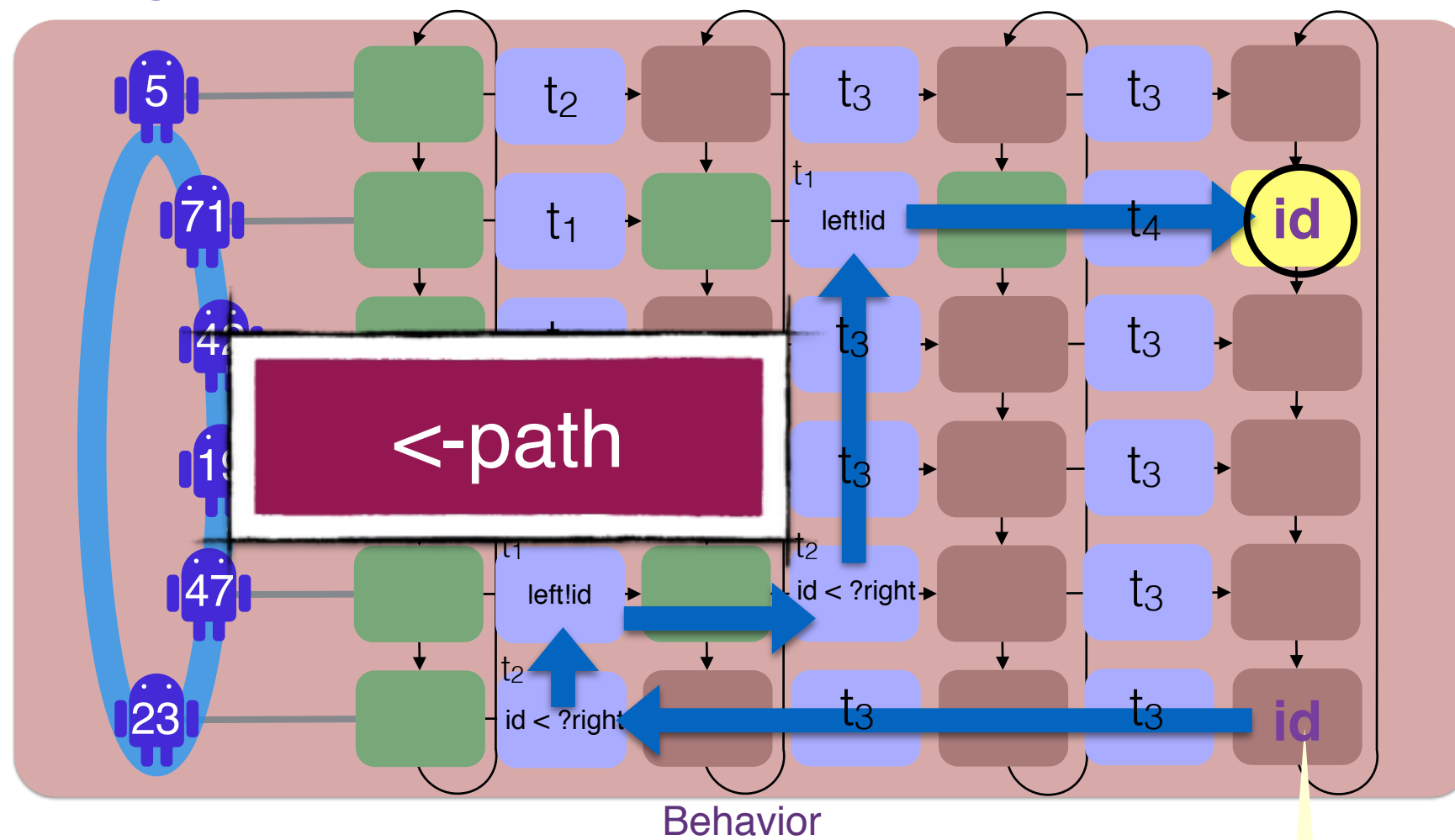
Distributed algorithms



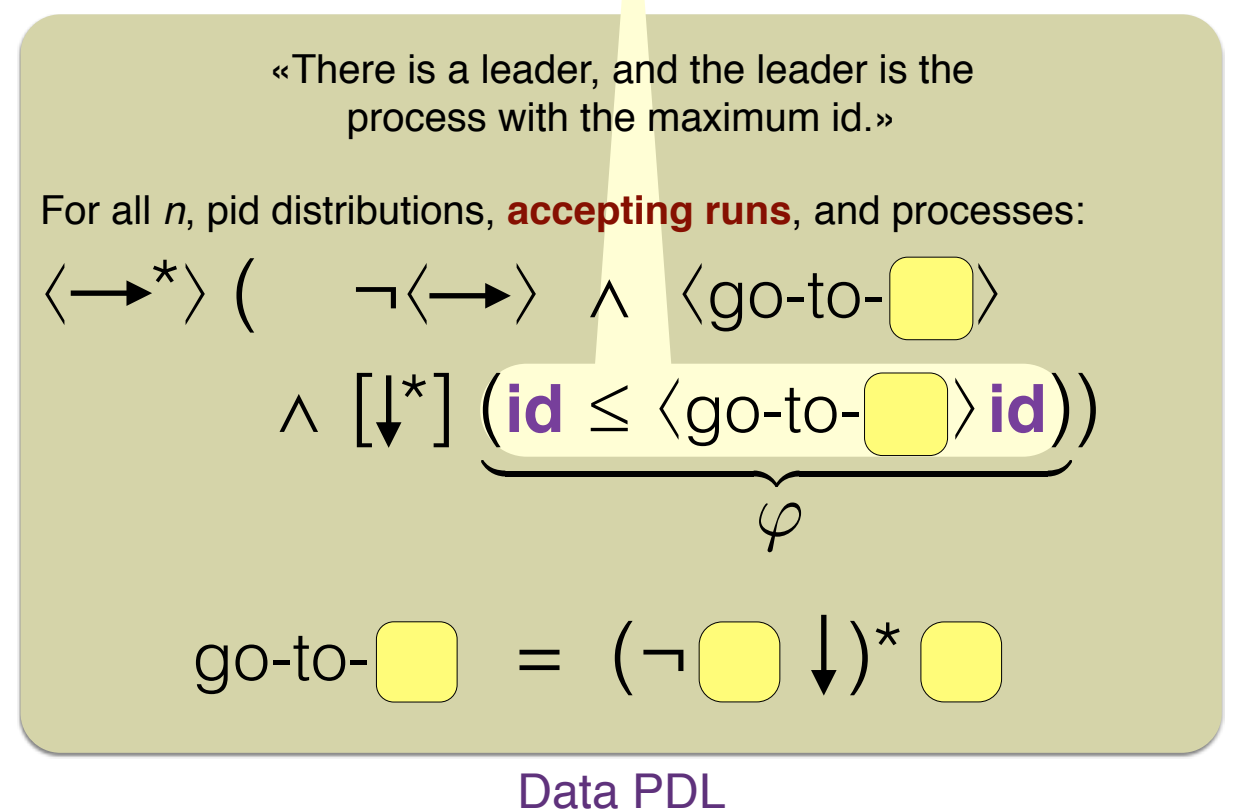
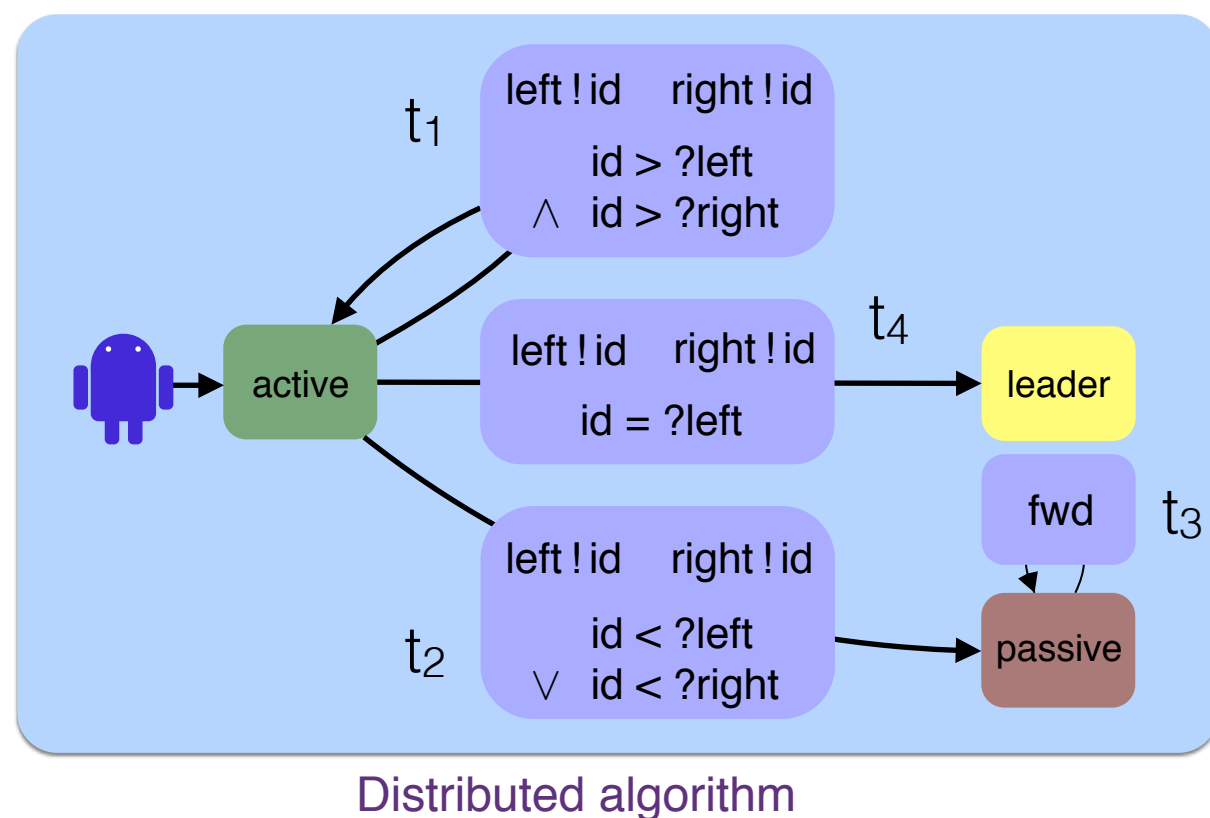
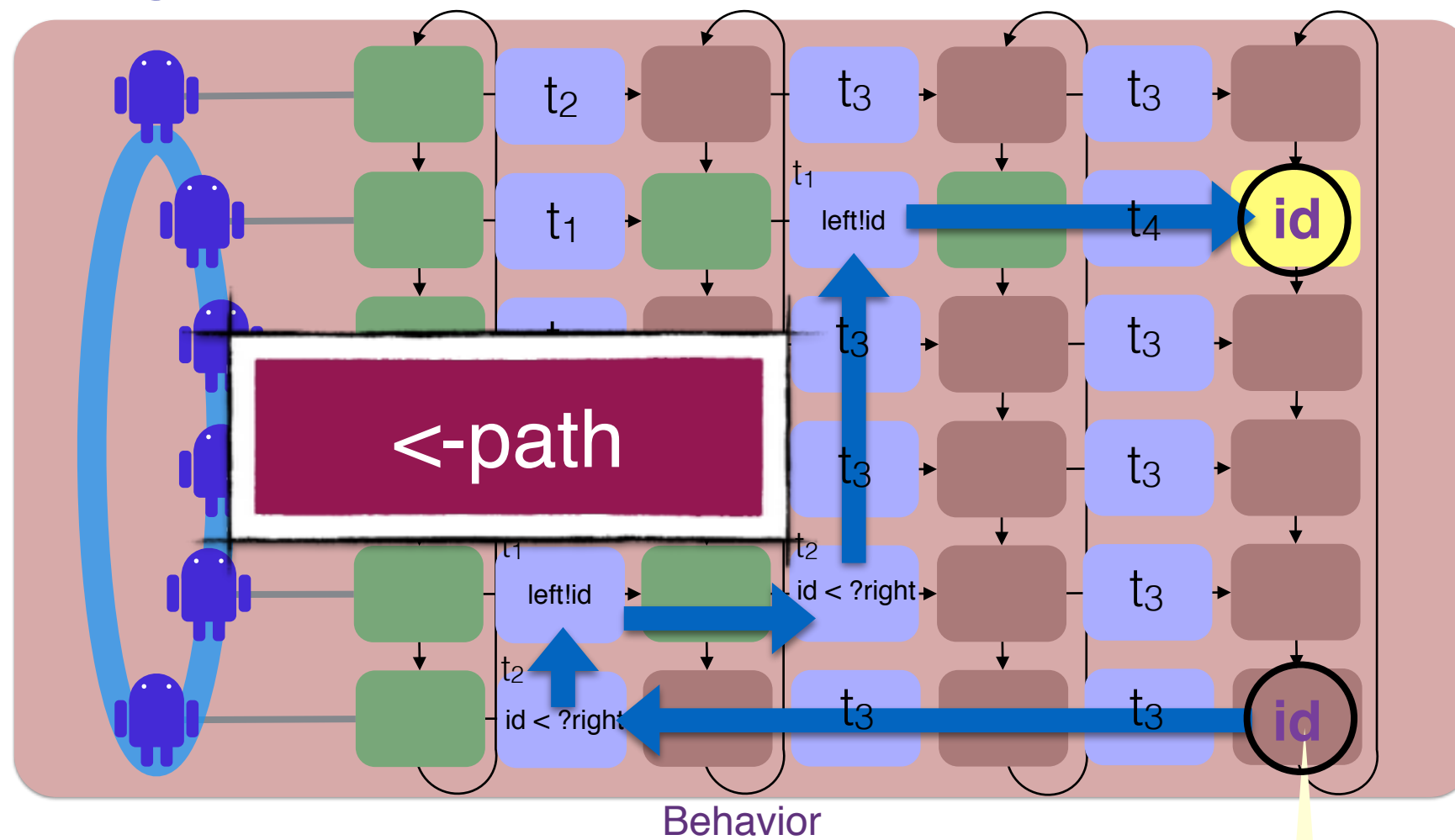
Distributed algorithms



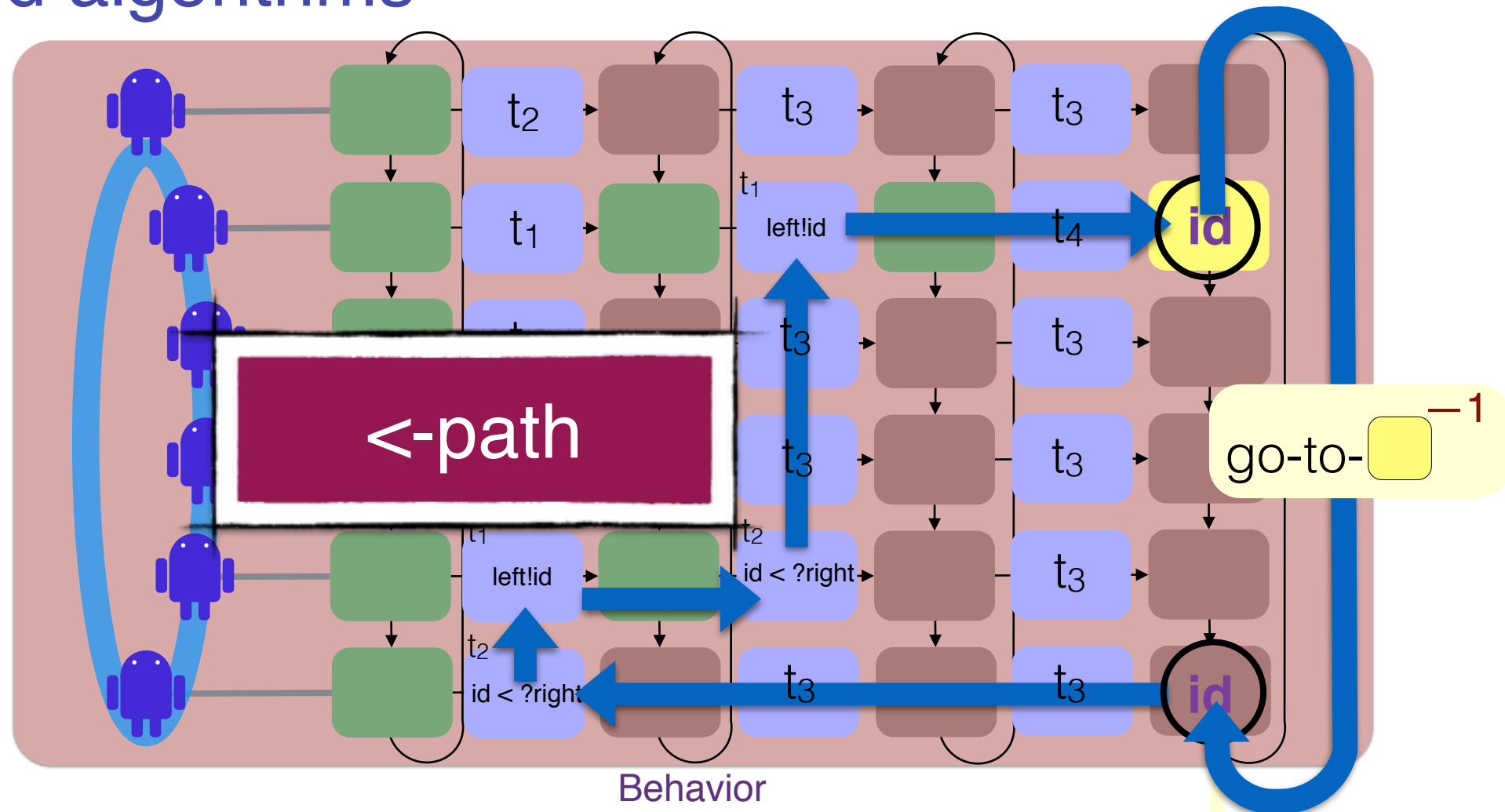
Distributed algorithms



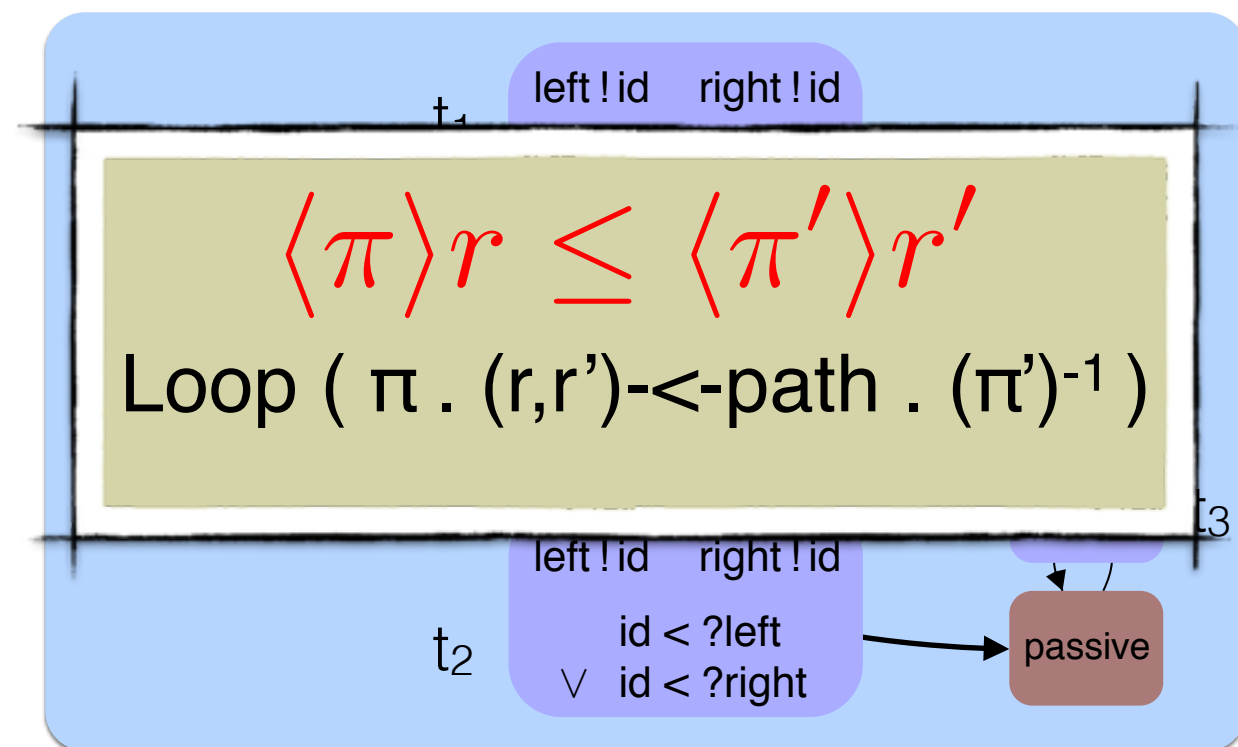
Distributed algorithms



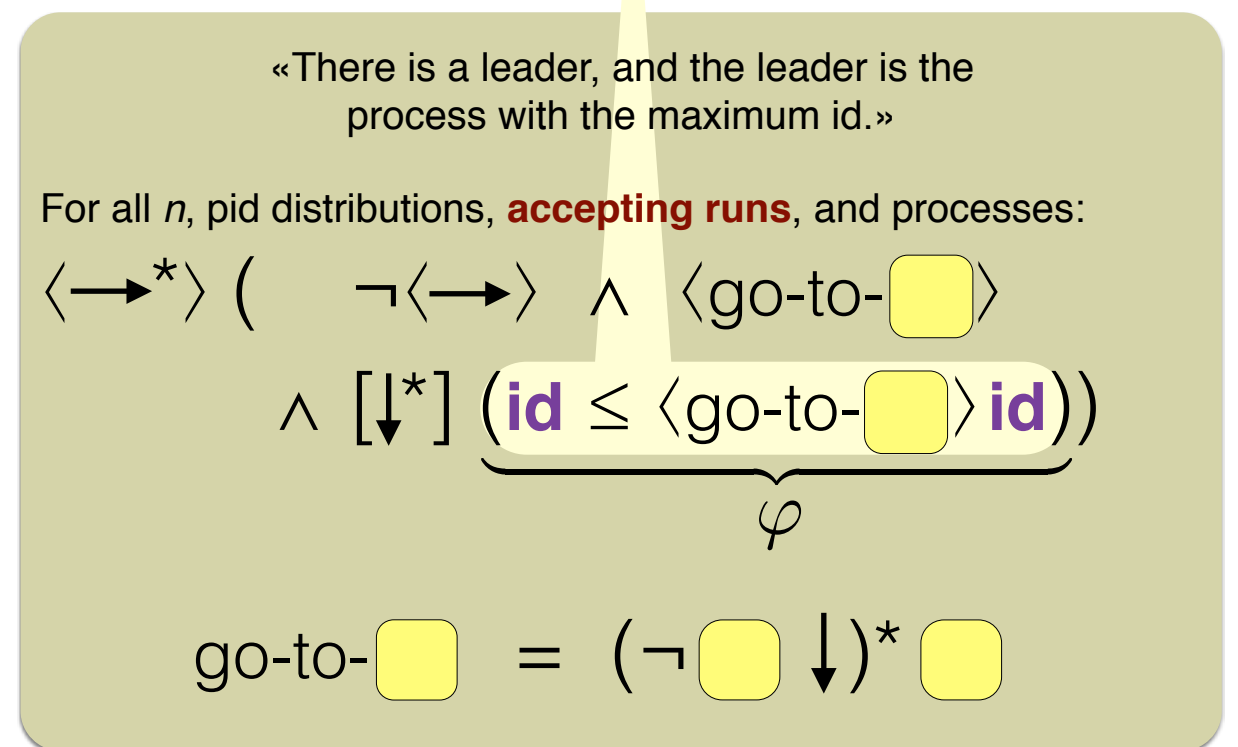
Distributed algorithms



Behavior

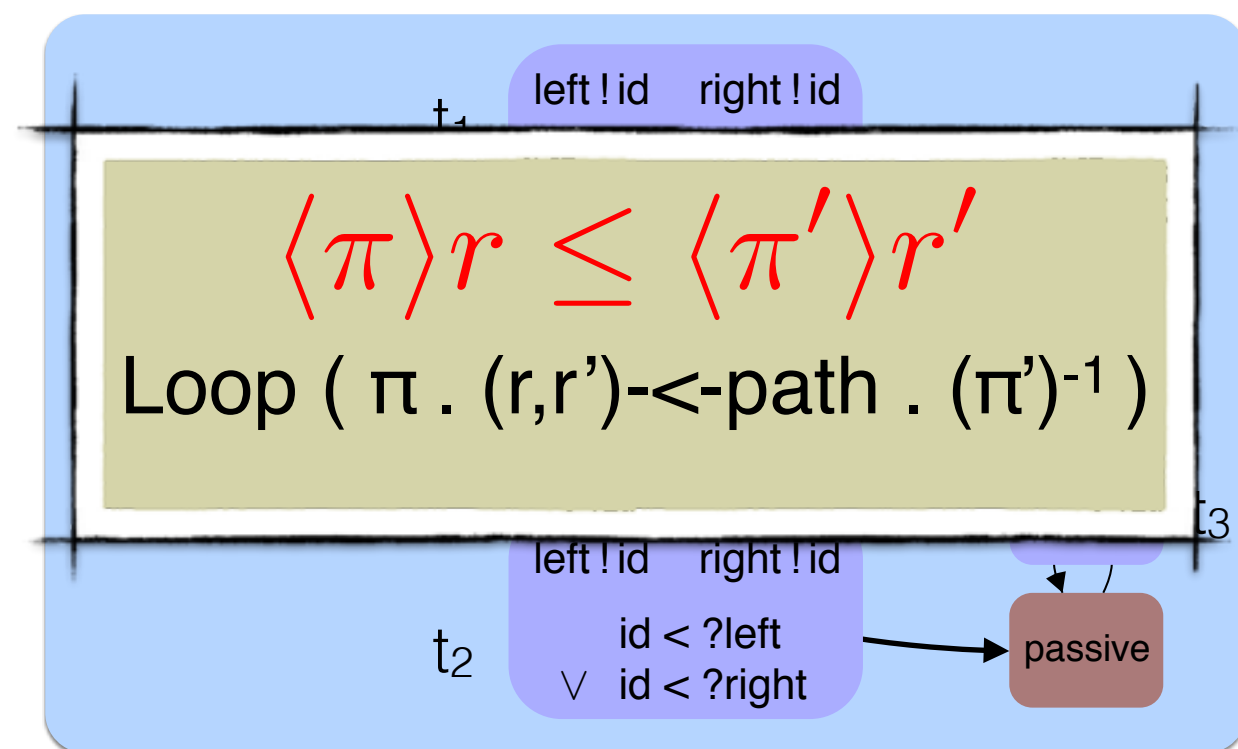
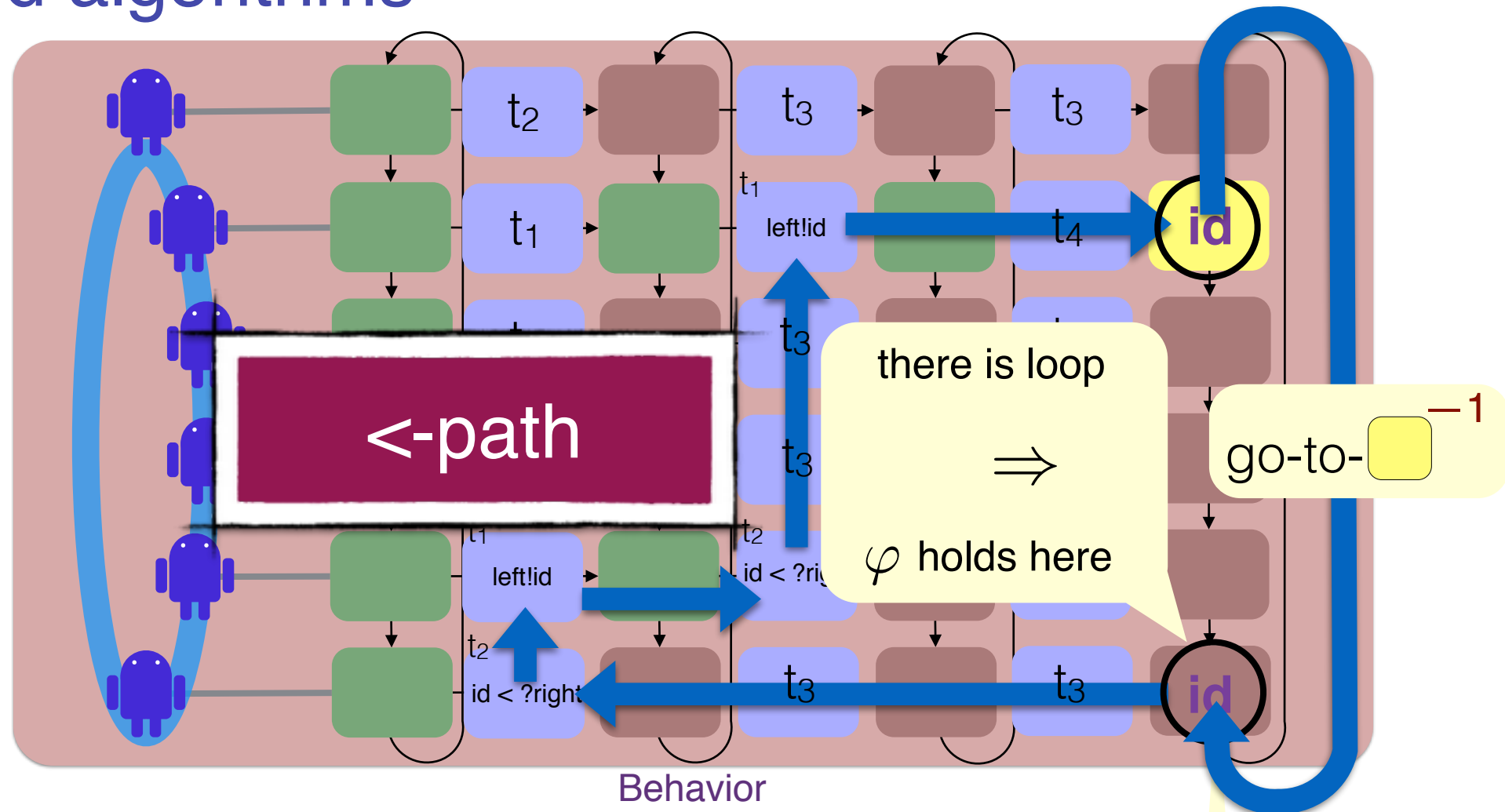


Distributed algorithm

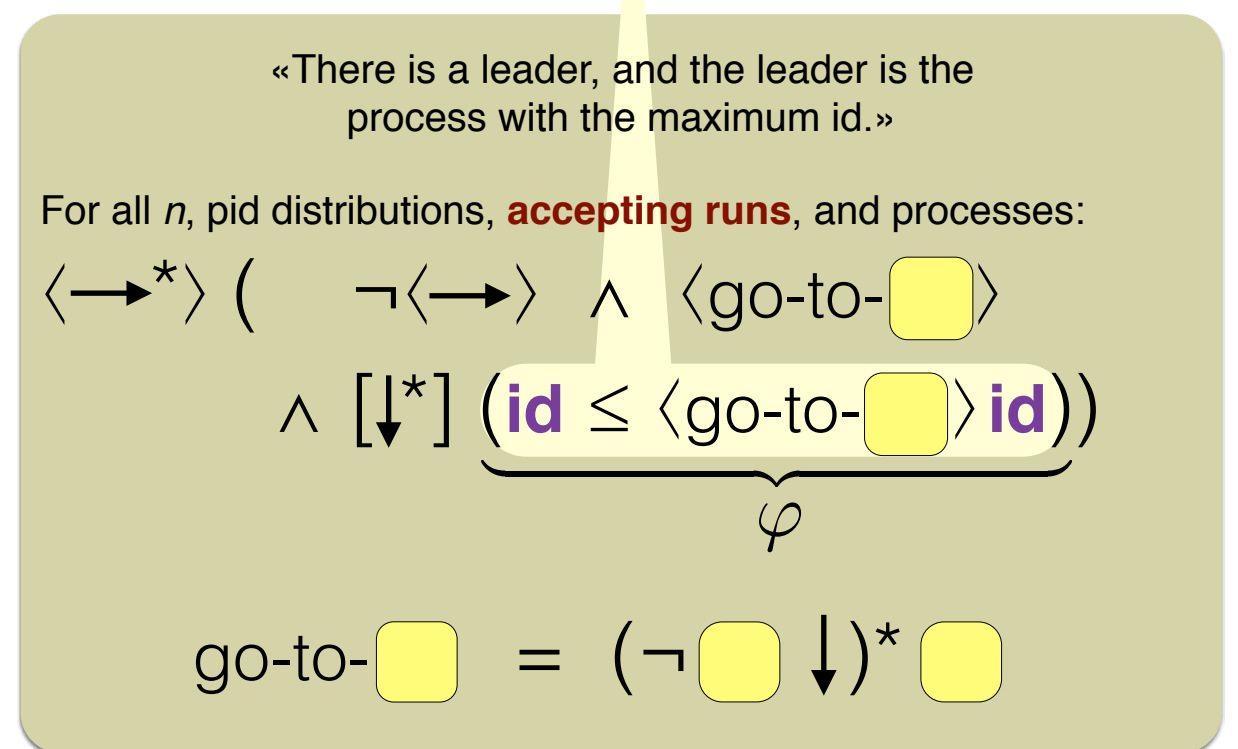


Data PDL

Distributed algorithms

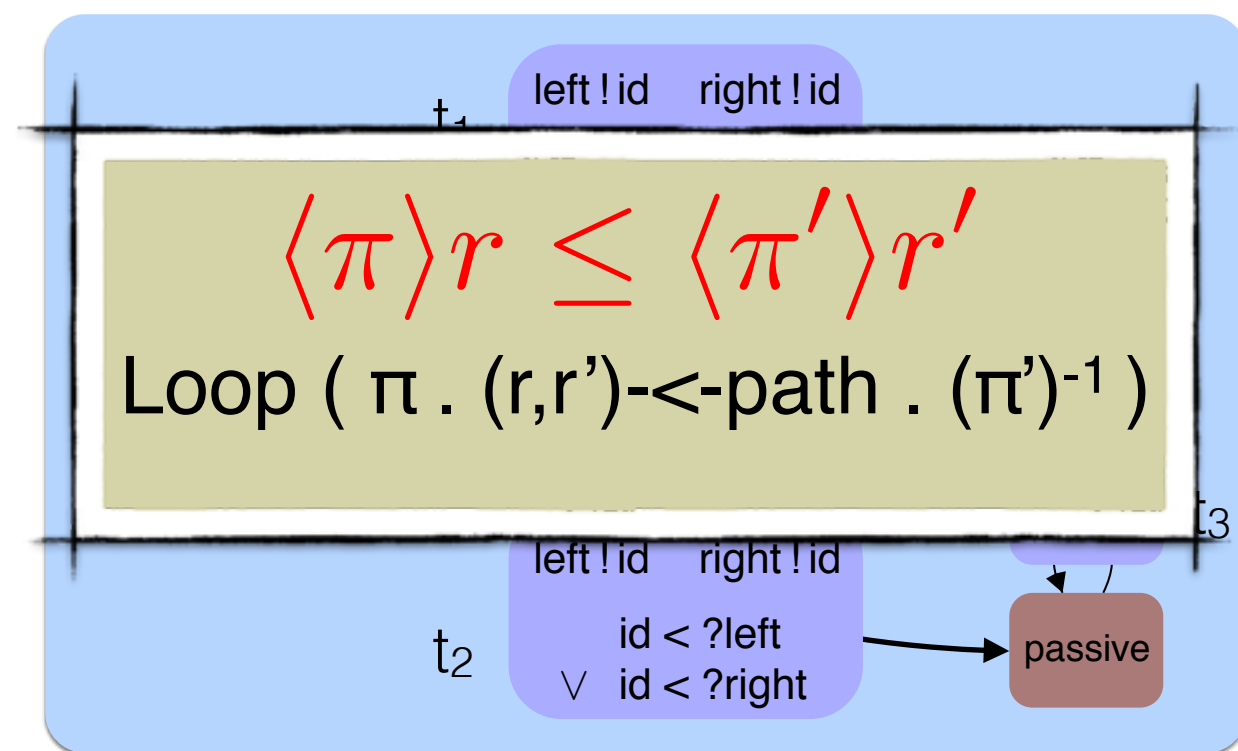
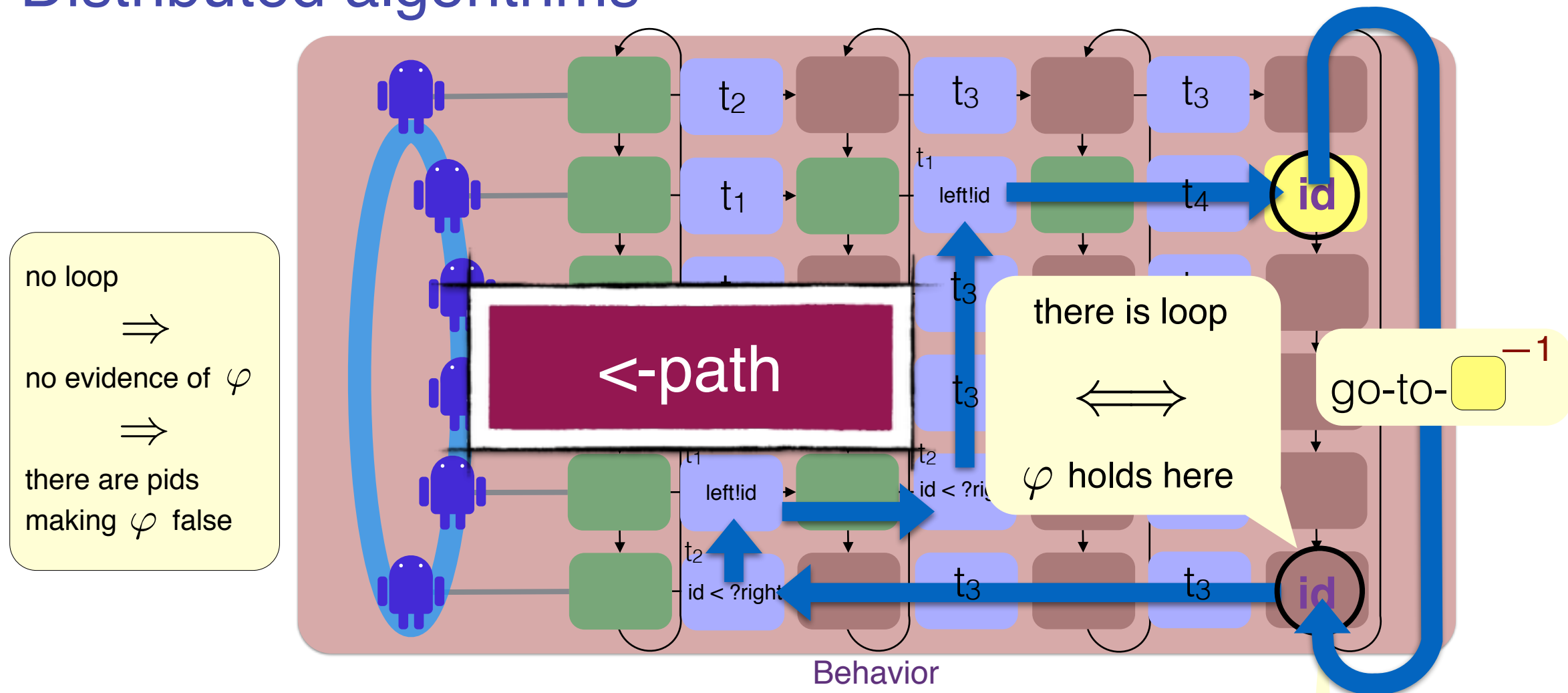


Distributed algorithm

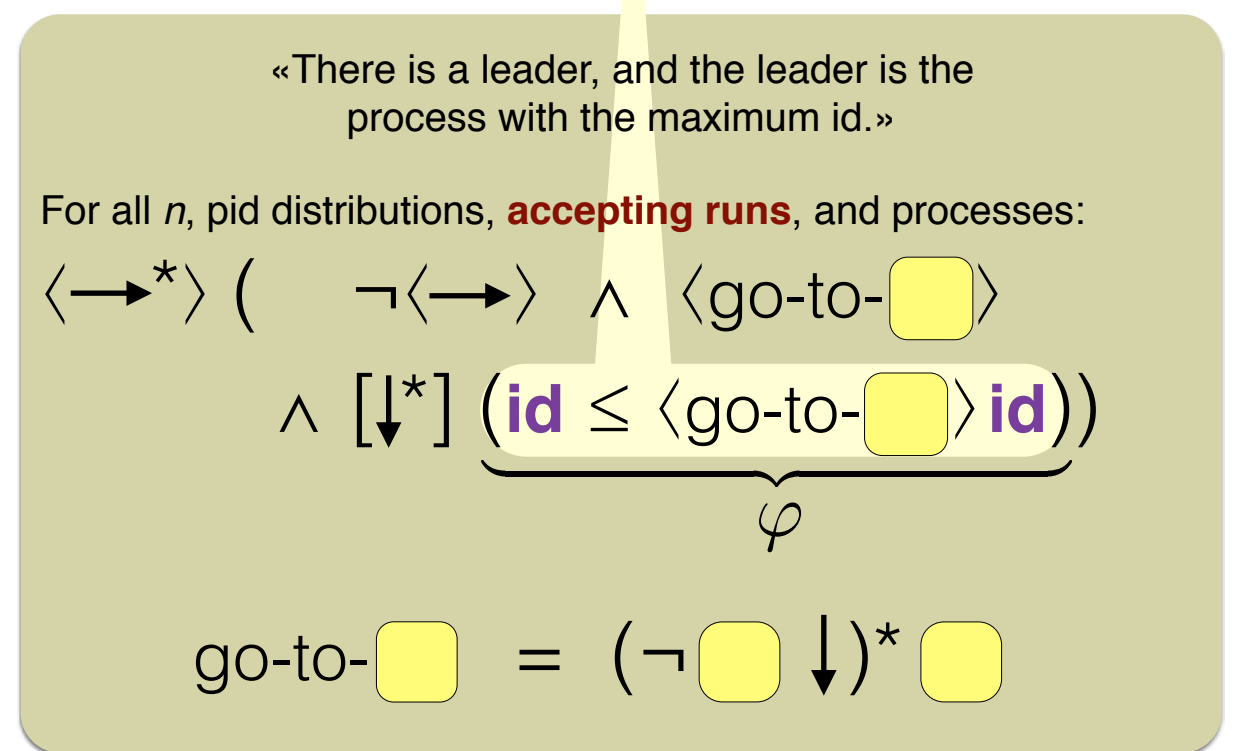


Data PDL

Distributed algorithms

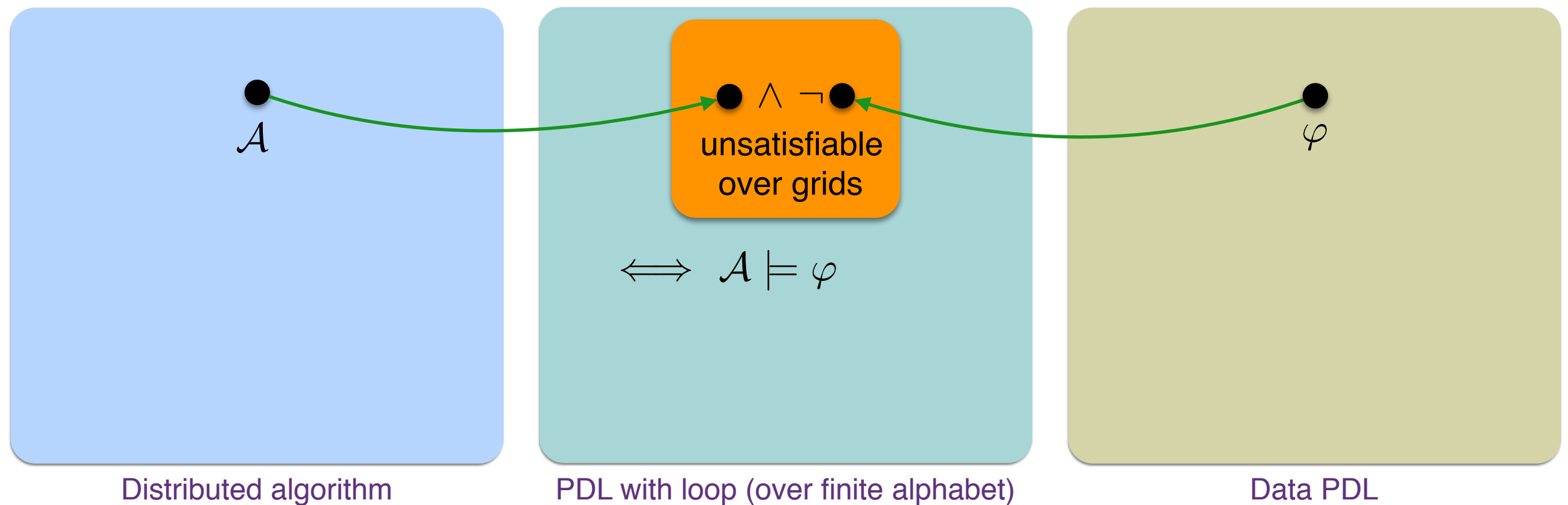
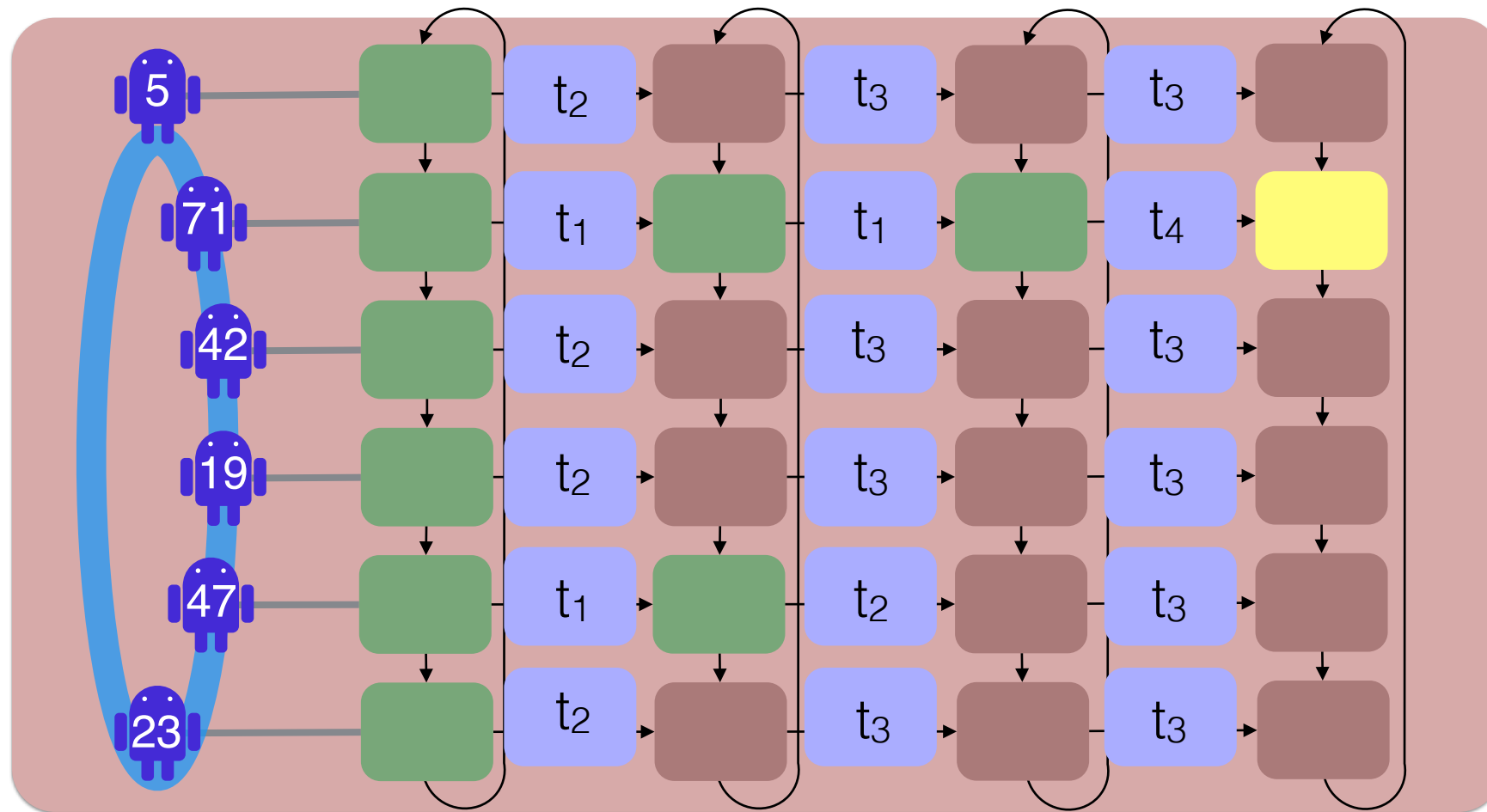


Distributed algorithm

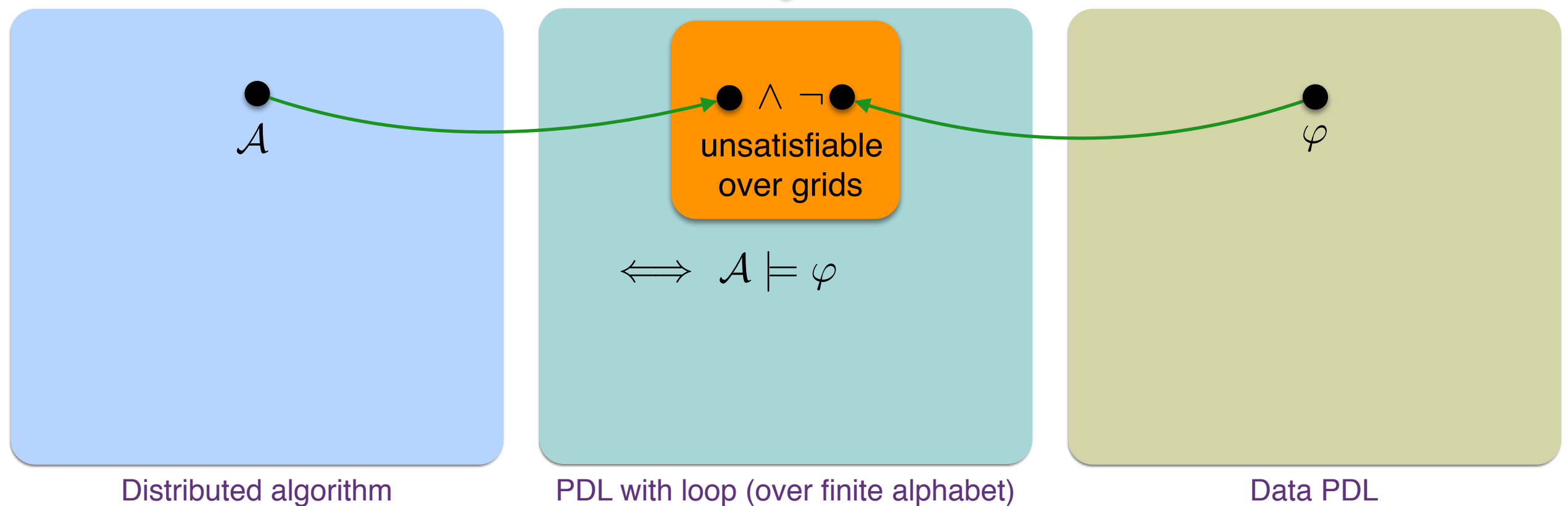
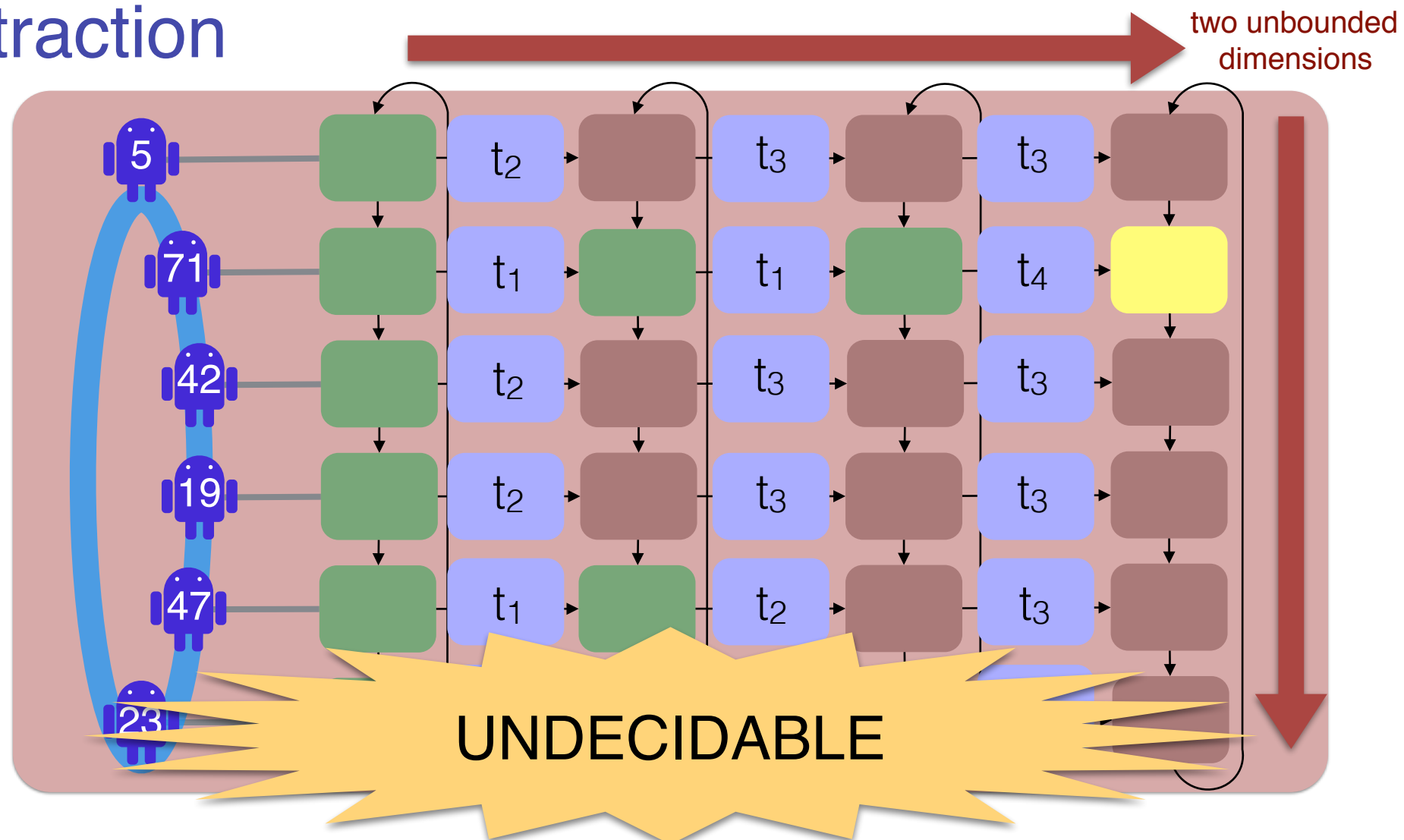


Data PDL

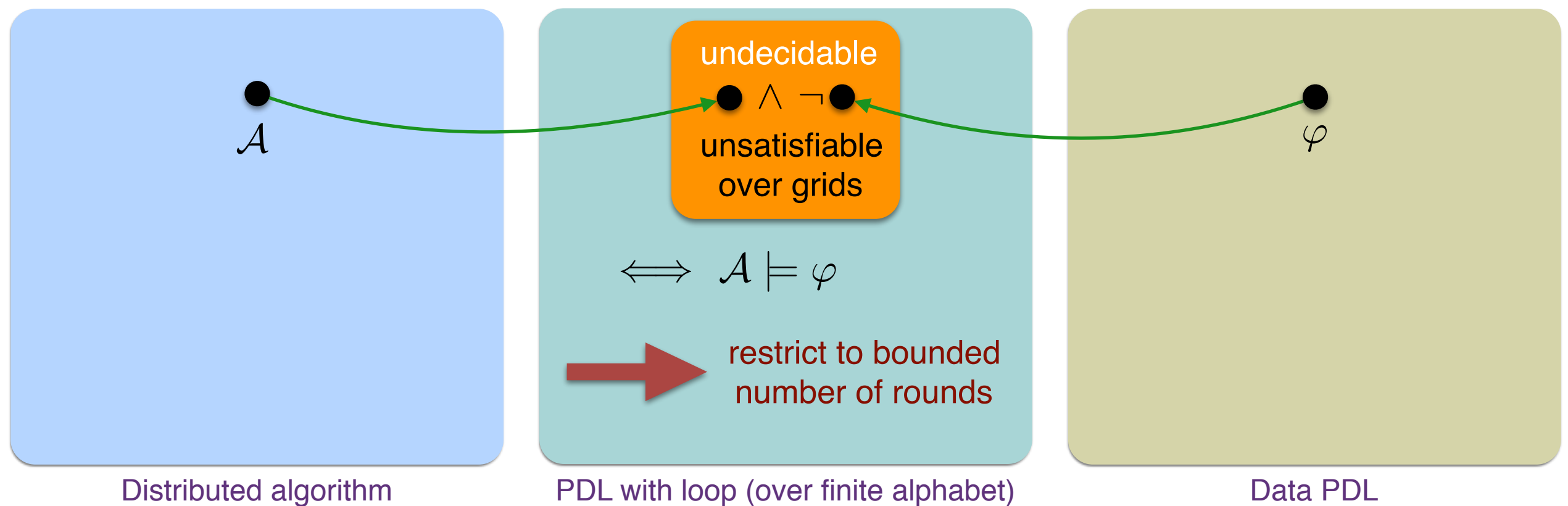
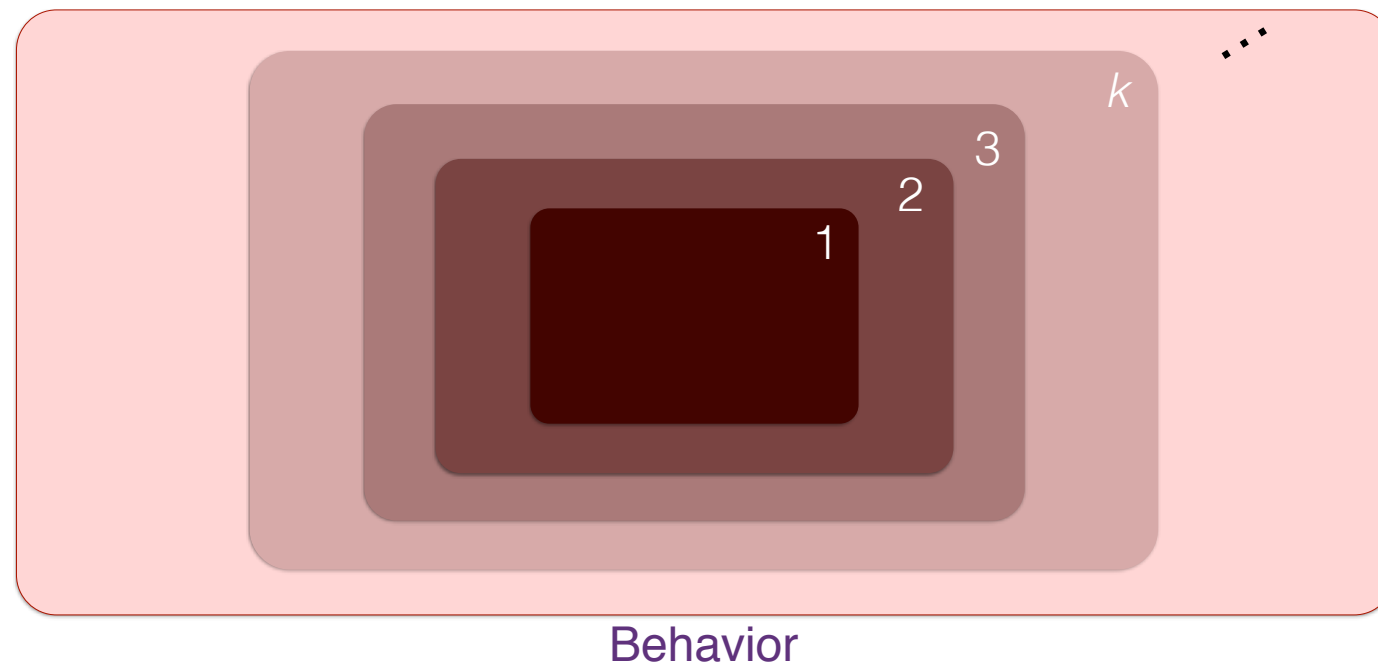
Data abstraction

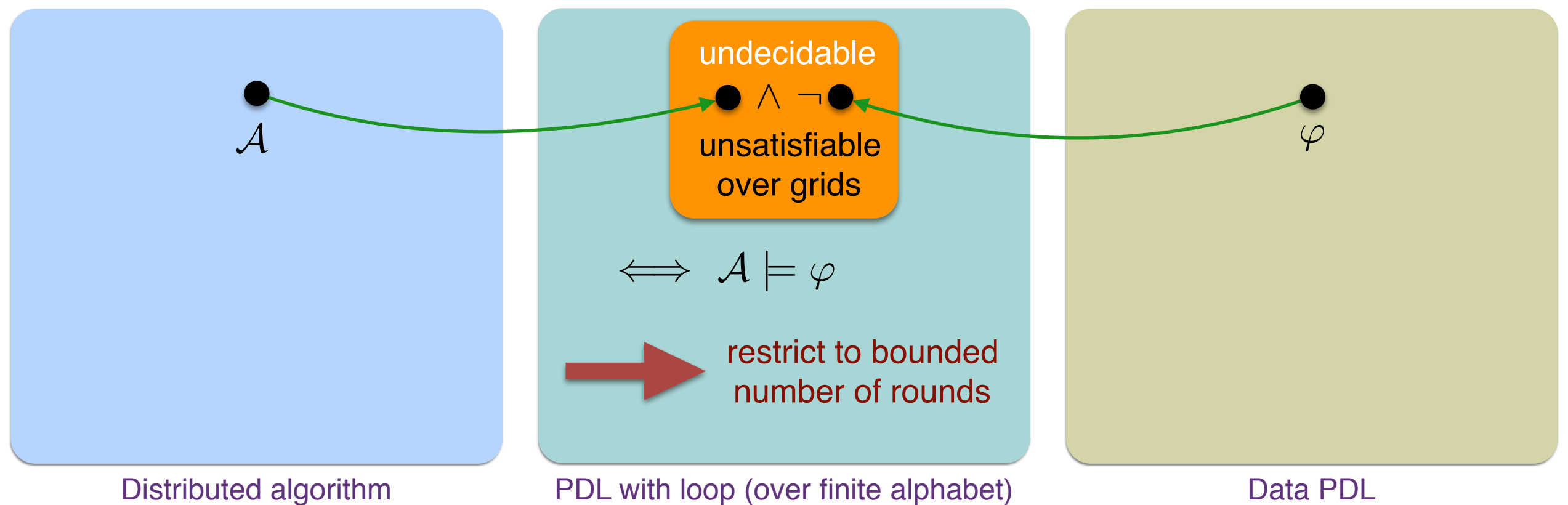
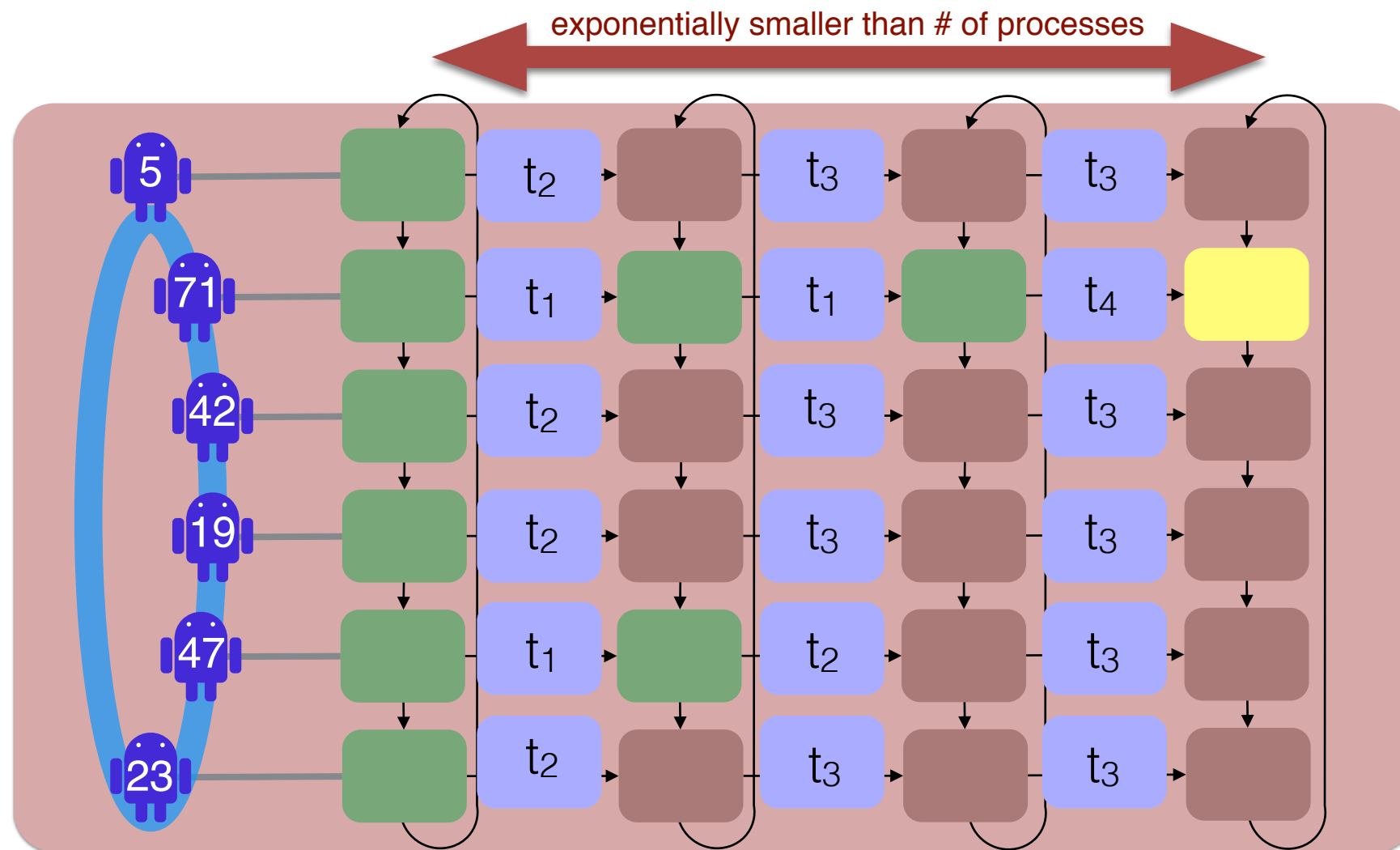


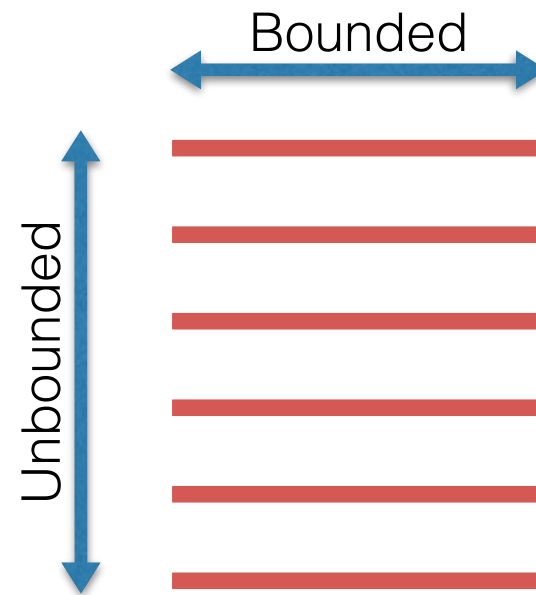
Data abstraction



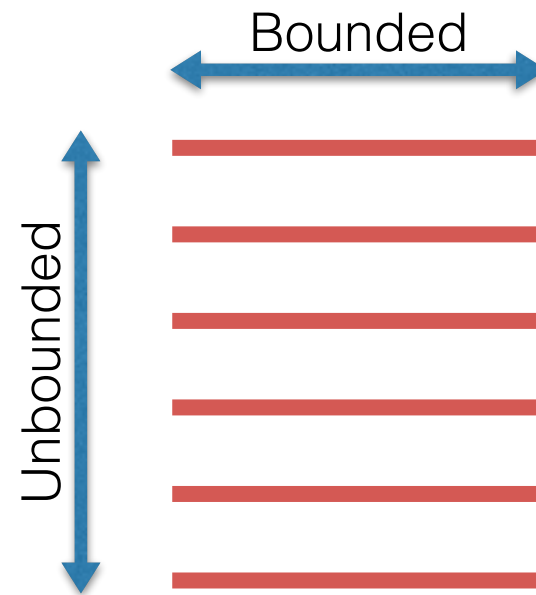
Under approximate verification







PDL with loop over bounded grids

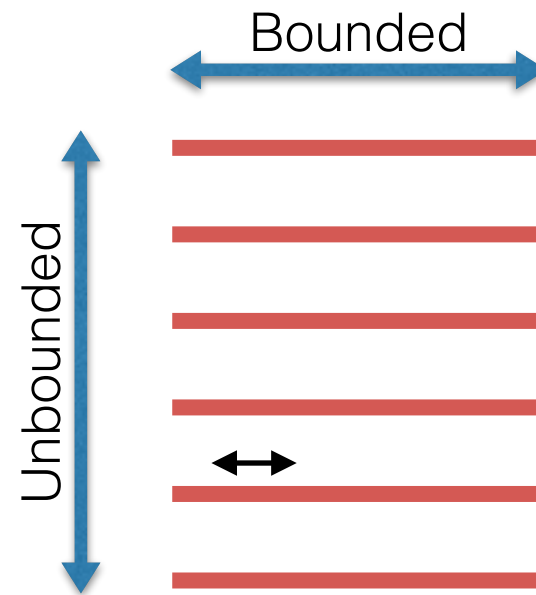


PDL with loop over bounded grids



PDL with loop over words

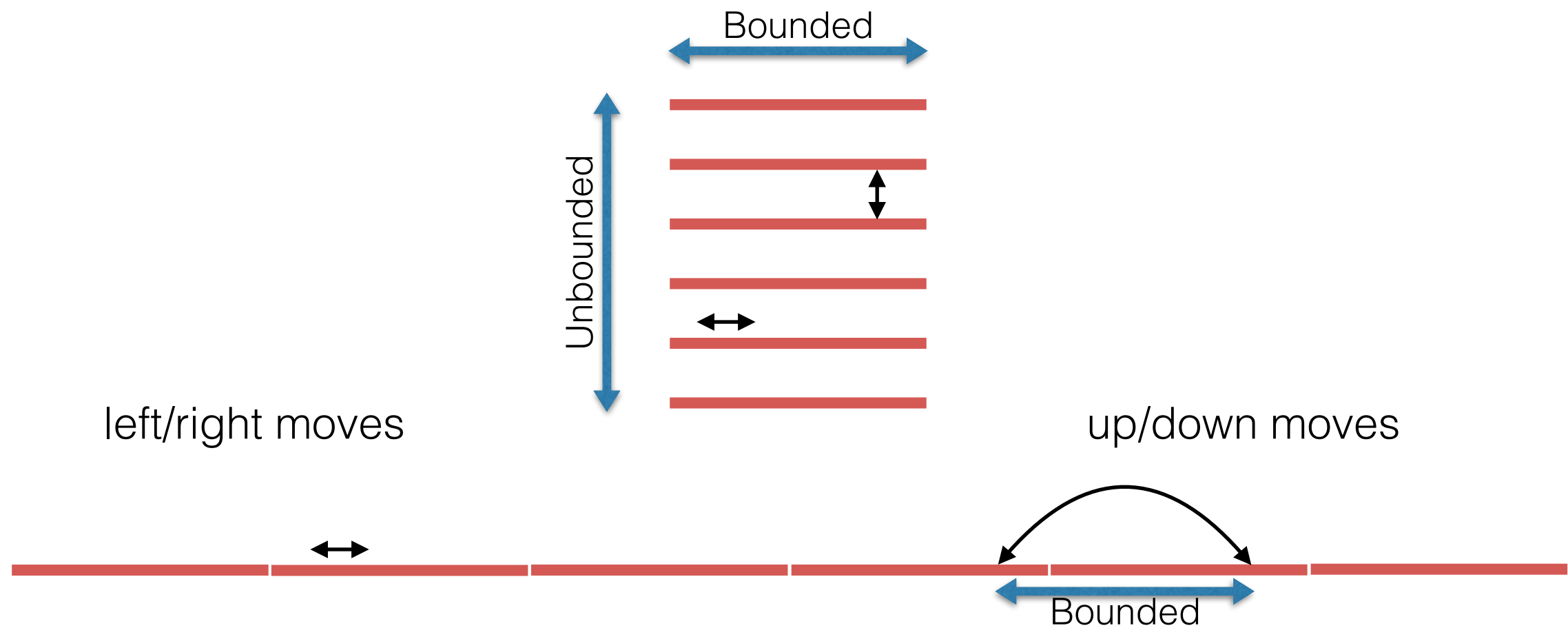
left/right moves



PDL with loop over bounded grids



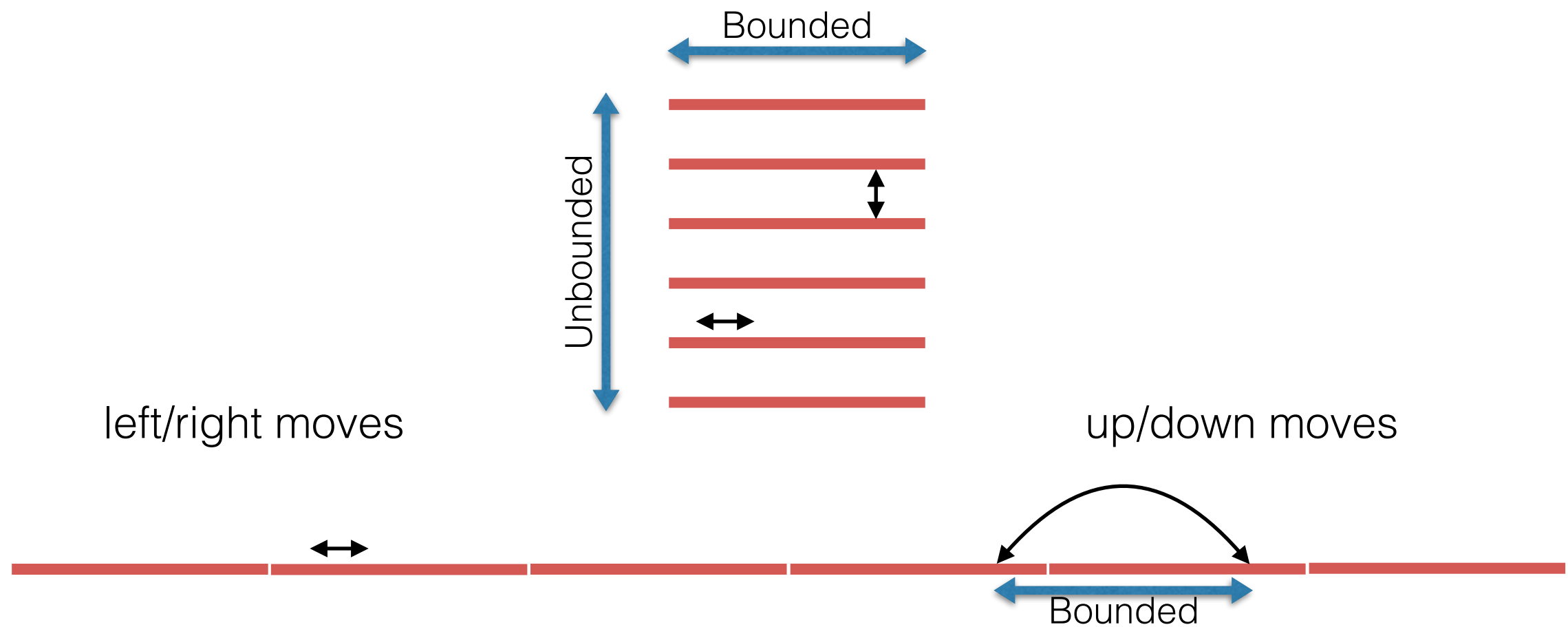
PDL with loop over words



PDL with loop over bounded grids



PDL with loop over words



PDL with loop over bounded grids



PDL with loop over words

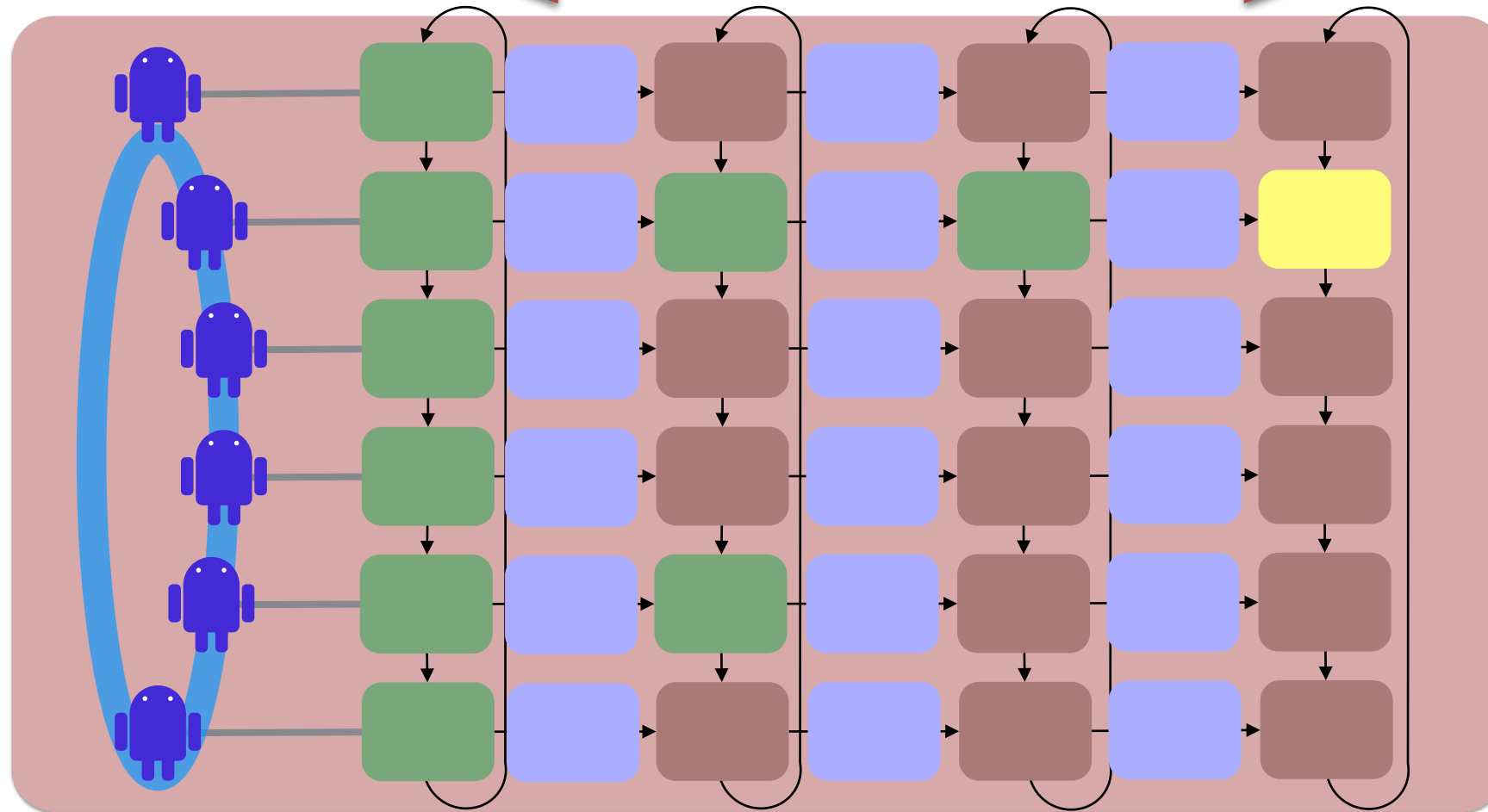


Alternating 2-way Automata



PSPACE

Distributed algorithms



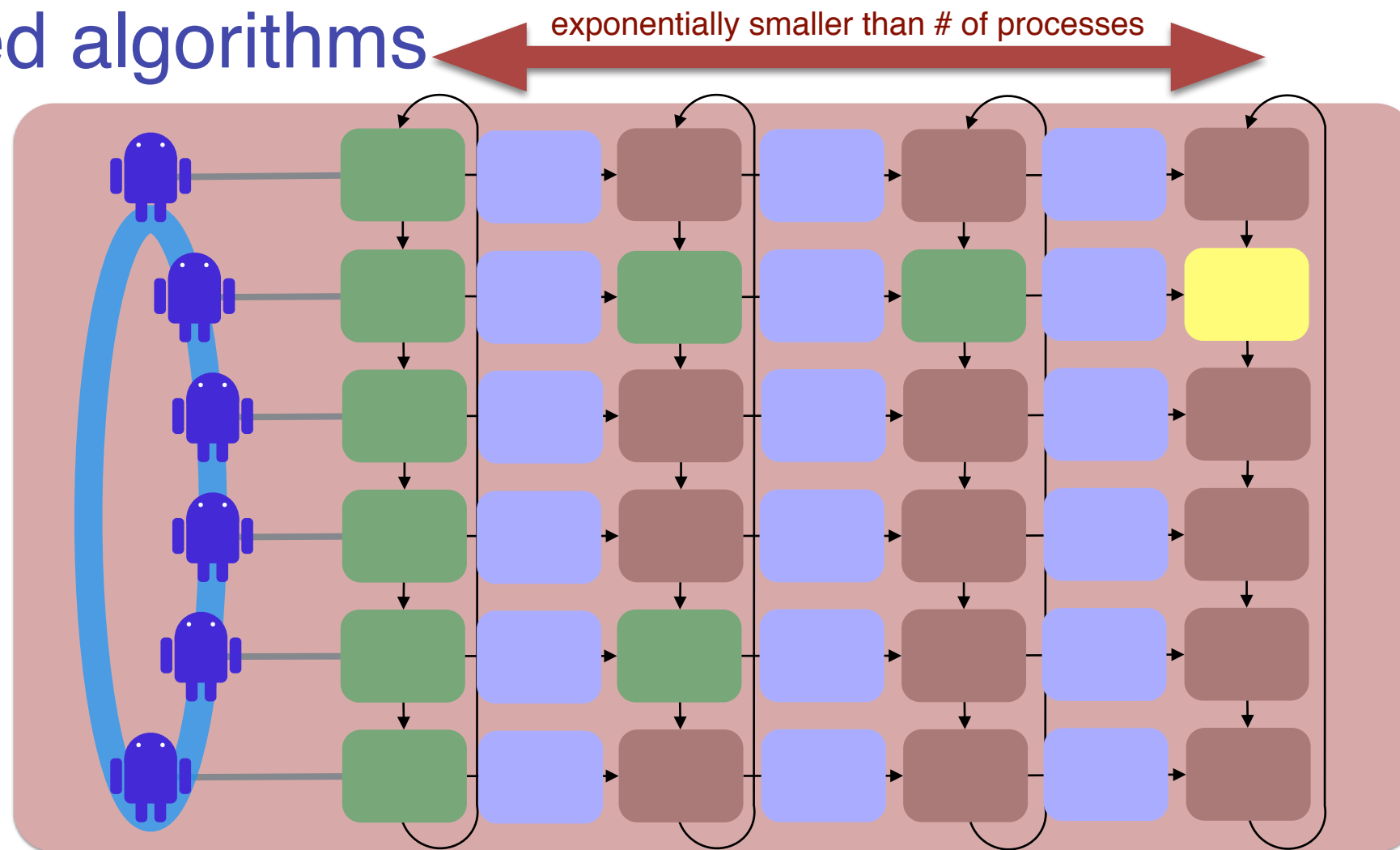
Theorem (Aiswarya-Bollig-Gastin; CONCUR '15).

Round-bounded model checking distributed algorithms* against Data PDL is PSPACE-complete**.

* with registers, register guards, and register updates

** unary encoding of # of rounds

Distributed algorithms



Theorem (Aiswarya-Bollig-Gastin; CONCUR '15).

Round-bounded model checking distributed algorithms* against Data PDL is PSPACE-complete**.

Summary

- ▶ What is the right temporal logic? Use generic Data PDL.
- ▶ How to deal with data? Use symbolic technique.
- ▶ How to deal with undecidability? Under-approximation.

Conclusions

Getting rid of Data

Translation of Distributed Algorithms and DataPDL to PDL with loops over finitely labelled cylinders

Independent of the restriction to rings

Independent of the number of rounds

Future work..

- Other operations?
- Other topologies?
- Other restrictions?
- Other communications?

