On the verification and control of timed systems

Patricia Bouyer-Decitre

LSV, CNRS & ENS Cachan, France

Two main parts:

- An introduction to timed systems (this morning)
- Ø Modelling resources in timed systems (this afternoon)

An introduction to timed systems

Patricia Bouyer-Decitre

LSV, CNRS & ENS Cachan, France

Outline

1. Introduction

- 2. The timed automaton model
- 3. Timed automata, decidability issues
- 4. How far can we extend the model and preserve decidability? Hybrid systems Smaller extensions of timed automata An alternative way of proving decidability
- 5. Timed automata in practice
- 6. Conclusion

Time!

Context: verification of critical systems

Time

- naturally appears in real systems (for ex. protocols, embedded systems)
- appears in properties (for ex. bounded response time)
 "Will the airbag oben within 5ms after the car crashes?"

 \rightsquigarrow Need of models and specification languages integrating timing aspects

- Untimed case: sequence of observable events
 - *a*: send message *b*: receive message

 $a b a b a b a b a b a b \cdots = (a b)^{\omega}$

- Untimed case: sequence of observable events
 - *a*: send message *b*: receive message

```
a b a b a b a b a b a b \cdots = (a b)^{\omega}
```

• Timed case: sequence of dated observable events

 (a, d_1) (b, d_2) (a, d_3) (b, d_4) (a, d_5) (b, d_6) · · ·

 d_1 : date at which the first *a* occurs d_2 : date at which the first *b* occurs, ...

- Untimed case: sequence of observable events
 - *a*: send message *b*: receive message

```
a b a b a b a b a b a \cdots = (a b)^{\omega}
```

• Timed case: sequence of dated observable events

 (a, d_1) (b, d_2) (a, d_3) (b, d_4) (a, d_5) (b, d_6) · · ·

- d_1 : date at which the first *a* occurs
- d_2 : date at which the first **b** occurs, ...
 - Discrete-time semantics: dates are e.g. taken in N
 Ex: (a, 1)(b, 3)(c, 4)(a, 6)

- Untimed case: sequence of observable events
 - *a*: send message *b*: receive message

 $a b a b a b a b a b a \cdots = (a b)^{\omega}$

• Timed case: sequence of dated observable events

 (a, d_1) (b, d_2) (a, d_3) (b, d_4) (a, d_5) (b, d_6) · · ·

- d_1 : date at which the first *a* occurs
- d_2 : date at which the first **b** occurs, ...
 - Discrete-time semantics: dates are e.g. taken in N
 Ex: (a, 1)(b, 3)(c, 4)(a, 6)
 - Dense-time semantics: dates are *e.g.* taken in Q₊, or in R₊
 Ex: (a, 1.28).(b, 3.1).(c, 3.98)(a, 6.13)

A case for dense-time

Time domain: discrete (*e.g.* \mathbb{N}) or dense (*e.g.* \mathbb{Q}_+ or \mathbb{R}_+)

- A compositionality problem with discrete time
- Dense-time is a more general model than discrete time
- But, can we not always discretize?

Discussion in the context of reachability problems for asynchronous digital circuits [BS91]



[Alu91] Alur. Techniques for automatic verification of real-time systems. *PhD thesis*, 1991. [BS91] Brzozowski, Seger. Advances in asynchronous circuit theory *BEATCS'91*.

Discussion in the context of reachability problems for asynchronous digital circuits [BS91]



Start with x=0 and y=[101] (stable configuration)

[Alu91] Alur. Techniques for automatic verification of real-time systems. *PhD thesis*, 1991. [BS91] Brzozowski, Seger. Advances in asynchronous circuit theory *BEATCS'91*.

Discussion in the context of reachability problems for asynchronous digital circuits [BS91]



Start with x=0 and y=[101] (stable configuration)

The input x changes to 1. The corresponding stable state is y=[011]

[Alu91] Alur. Techniques for automatic verification of real-time systems. *PhD thesis*, 1991. [BS91] Brzozowski, Seger. Advances in asynchronous circuit theory *BEATCS'91*.

Discussion in the context of reachability problems for asynchronous digital circuits [BS91]



Start with x=0 and y=[101] (stable configuration)

The input x changes to 1. The corresponding stable state is y=[011]However, many possible behaviours, e.g.

$$\begin{bmatrix} 101 \end{bmatrix} \xrightarrow{y_2} \\ 1.2 \end{bmatrix} \begin{bmatrix} 111 \end{bmatrix} \xrightarrow{y_3} \\ 2.5 \end{bmatrix} \begin{bmatrix} 110 \end{bmatrix} \xrightarrow{y_1} \\ 2.8 \end{bmatrix} \begin{bmatrix} 010 \end{bmatrix} \xrightarrow{y_3} \\ 4.5 \end{bmatrix} \begin{bmatrix} 011 \end{bmatrix}$$

[[]Alu91] Alur. Techniques for automatic verification of real-time systems. PhD thesis, 1991.

[[]BS91] Brzozowski, Seger. Advances in asynchronous circuit theory BEATCS'91.

Discussion in the context of reachability problems for asynchronous digital circuits [BS91]



Start with x=0 and y=[101] (stable configuration)

The input x changes to 1. The corresponding stable state is y=[011]However, many possible behaviours, e.g.

[[]Alu91] Alur. Techniques for automatic verification of real-time systems. PhD thesis, 1991.

[[]BS91] Brzozowski, Seger. Advances in asynchronous circuit theory BEATCS'91.



• This digital circuit is not 1-discretizable.



- This digital circuit is not 1-discretizable.
- Why that? (initially *x* = 0 and *y* = [11100000], *x* is set to 1)



- This digital circuit is not 1-discretizable.
- Why that? (initially x = 0 and y = [11100000], x is set to 1)

 $[11100000] \xrightarrow{y_1}{1} [01100000] \xrightarrow{y_2}{1.5} [00100000] \xrightarrow{y_3,y_5}{2} [00001000] \xrightarrow{y_5,y_7}{3} [00000010] \xrightarrow{y_7,y_8}{4} [00000001]$



- This digital circuit is not 1-discretizable.
- Why that? (initially x = 0 and y = [11100000], x is set to 1)

 $\begin{array}{c} [11100000] \xrightarrow{y_1}{1} [01100000] \xrightarrow{y_2}{1.5} [00100000] \xrightarrow{y_3,y_5}{2} [00001000] \xrightarrow{y_5,y_7}{3} [00000010] \xrightarrow{y_7,y_8}{4} [00000001] \\ [11100000] \xrightarrow{y_1,y_2,y_3}{1} [00000000] \end{array}$



- This digital circuit is not 1-discretizable.
- Why that? (initially x = 0 and y = [11100000], x is set to 1)

 $\begin{array}{c} [11100000] \xrightarrow{y_1}{1} [01100000] \xrightarrow{y_2}{1.5} [00100000] \xrightarrow{y_3,y_5}{2} [00001000] \xrightarrow{y_5,y_7}{3} [00000010] \xrightarrow{y_7,y_8}{4} [00000001] \\ [11100000] \xrightarrow{y_1,y_2,y_3}{1} [00000000] \\ [11100000] \xrightarrow{y_1}{1} [01111000] \xrightarrow{y_2,y_3,y_4,y_5}{2} [00000000] \\ \end{array}$



- This digital circuit is not 1-discretizable.
- Why that? (initially x = 0 and y = [11100000], x is set to 1)

$$\begin{array}{c} [11100000] \xrightarrow{y_1} [01100000] \xrightarrow{y_2} 1.5 \\ 1.5 \\ [11100000] \xrightarrow{y_1, y_2, y_3} [00000000] \\ [11100000] \xrightarrow{y_1, y_2, y_3} [00000000] \\ [11100000] \xrightarrow{y_1} 1 \\ 1 \\ [01111000] \xrightarrow{y_2, y_3, y_4, y_5} [00000000] \\ [11100000] \xrightarrow{y_1, y_2} [00100000] \xrightarrow{y_3, y_5, y_6} [00001100] \xrightarrow{y_5, y_6} [00000000] \\ [11100000] \xrightarrow{y_1, y_2} 1 \\ 1 \\ [00100000] \xrightarrow{y_3, y_5, y_6} [00001100] \xrightarrow{y_5, y_6} [00000000] \\ \end{array}$$



- This digital circuit is not 1-discretizable.
- Why that? (initially x = 0 and y = [11100000], x is set to 1)

$$\begin{array}{c} 11100000] \xrightarrow{y_1} [01100000] \xrightarrow{y_2} 1.5 \\$$

Theorem [BS91]

For every $k \ge 1$, there exists a digital circuit such that the reachability set of states in dense-time is strictly larger than the one in discrete time (with granularity $\frac{1}{k}$).

Theorem [BS91]

For every $k \ge 1$, there exists a digital circuit such that the reachability set of states in dense-time is strictly larger than the one in discrete time (with granularity $\frac{1}{k}$).

Claim

Finding a correct granularity is as difficult as computing the set of reachable states in dense-time.

Theorem [BS91]

For every $k \ge 1$, there exists a digital circuit such that the reachability set of states in dense-time is strictly larger than the one in discrete time (with granularity $\frac{1}{k}$).

Claim

Finding a correct granularity is as difficult as computing the set of reachable states in dense-time.

Further counter-example

There exist systems for which no granularity exists.

(see later)

Theorem [BS91]

For every $k \ge 1$, there exists a digital circuit such that the reachability set of states in dense-time is strictly larger than the one in discrete time (with granularity $\frac{1}{k}$).

Claim

Finding a correct granularity is as difficult as computing the set of reachable states in dense-time.

Further counter-example

There exist systems for which no granularity exists.

(see later)

Hence, we better consider a dense-time domain!

[BS91] Brzozowski, Seger. Advances in asynchronous circuit theory BEATCS'91.

Outline

1. Introduction

2. The timed automaton model

- 3. Timed automata, decidability issues
- 4. How far can we extend the model and preserve decidability? Hybrid systems Smaller extensions of timed automata An alternative way of proving decidability
- 5. Timed automata in practice
- 6. Conclusion

A plethora ot models...

• ... for real-time systems:

- timed circuits,
- time(d) Petri nets,
- timed process algebra,
- timed automata,
- ...
- ... and for real-time properties:
 - timed observers,
 - real-time logics: MTL, TPTL, TCTL, QTL, MITL...

A plethora ot models...

• ... for real-time systems:

- timed circuits,
- time(d) Petri nets,
- timed process algebra,
- timed automata,
- ...
- ... and for real-time properties:
 - timed observers,
 - real-time logics: MTL, TPTL, TCTL, QTL, MITL...

Timed automata [AD90]

- A finite control structure + variables (clocks)
- A transition is of the form:



• An enabling condition (or guard) is:

$$g$$
 ::= $x \sim c \mid x - y \sim c \mid g \wedge g$

where $\sim \in \{<,\leq,=,\geq,>\}$

[AD90] Alur, Dill. Automata for modeling real-time systems (ICALP'90).

Timed automata [AD90]

- A finite control structure + variables (clocks)
- A transition is of the form:



• An enabling condition (or guard) is:

$$g ::= x \sim c \mid x - y \sim c \mid g \wedge g$$

where $\sim \in \{<,\leq,=,\geq,>\}$

[AD90] Alur, Dill. Automata for modeling real-time systems (ICALP'90).









y 0











	safe	$\xrightarrow{23}$	safe	 alarm	$\xrightarrow{15.6}$	alarm
х	0		23	0		15.6
у	0		23	23		38.6



	safe	$\xrightarrow{23}$	safe	$\xrightarrow{\text{problem}}$	alarm	$\xrightarrow{15.6}$	alarm	$\xrightarrow{\text{delayed}}$	failsafe	
х	0		23		0		15.6		15.6	
у	0		23		23		38.6		0	

failsafe

- ... 15.6
 - 0


	safe	$\xrightarrow{23}$	safe	$\xrightarrow{\text{problem}}$	alarm	$\xrightarrow{15.6}$	alarm	$\xrightarrow{\text{delayed}}$	failsafe	
х	0		23		0		15.6		15.6	
у	0		23		23		38.6		0	

failsafe	$\xrightarrow{2.3}$	failsafe
 15.6		17.9
0		2.3



	safe	$\xrightarrow{23}$ safe	probl	\xrightarrow{em}	alarm	$\xrightarrow{15.6}$	alarm	$\xrightarrow{\text{delayed}}$	failsafe	
х	0	23			0		15.6		15.6	
у	0	23			23		38.6		0	
	failsafe	$\xrightarrow{2.3}$	failsafe	repair	: 	repairing				

Tunibure	/ Tunisure	,	repuiling
 15.6	17.9		17.9
0	2.3		0



	safe -	$\xrightarrow{23}$ safe	$\xrightarrow{\text{problem}}$	alarn	$\xrightarrow{15.6}$	alarm	$\xrightarrow{\text{delayed}}$	failsafe	
х	0	23		0		15.6		15.6	
у	0	23		23		38.6		0	
	failsafe	$\xrightarrow{2.3}$	failsafe —	\xrightarrow{pair}	repairing	$\xrightarrow{22.1}$	repairing		
	15.6		17.9		17.9		40		
	0		2.3		0		22.1		



	safe –	$\xrightarrow{23}$ safe	$\xrightarrow{\text{problem}}$	alarm	$\xrightarrow{15.6}$	alarm	$\xrightarrow{\text{delayed}}$	failsafe	
х	0	23		0		15.6		15.6	
у	0	23		23		38.6		0	
	failsafe	$\xrightarrow{2.3}$	failsafe		repairing	$\xrightarrow{22.1}$	repairing	$\xrightarrow{\text{done}}$	safe
•••	15.6		17.9		17.9		40		40
	0		2.3		0		22.1		22.1



	safe -	$\xrightarrow{23}$ safe	e	lem → alarr	$\xrightarrow{15.6}$	alarm	$\xrightarrow{\text{delayed}}$	failsafe	
х	0	23		0		15.6		15.6	
У	0	23		23		38.6		0	
	failsafe	$\xrightarrow{2.3}$	failsafe	$\xrightarrow{\text{repair}}$	repairing	$\xrightarrow{22.1}$	repairing	$\xrightarrow{\text{done}}$	safe
	15.6		17.9		17.9		40		40
	0		2.3		0		22.1		22.1

This run read the timed word (problem, 23)(delayed, 38.6)(repair, 40.9), (done, 63).

Timed automata semantics

•
$$\mathcal{A} = (\Sigma, L, X, \longrightarrow)$$
 is a TA

- Configurations: (ℓ, ν) ∈ L × T^X where T is the time domain
 v is called the (clock) valuation
- Timed transition system:

• action transition:
$$(\ell, v) \xrightarrow{a} (\ell', v')$$
 if $\exists \ell \xrightarrow{g,a,r} \ell' \in \mathcal{A}$ s.t.

$$\begin{cases} v \models g \\ v' = v[r \leftarrow 0] \end{cases}$$

• delay transition: $(\ell, v) \xrightarrow{\delta(d)} (\ell, v + d)$ if $d \in T$







• Discrete-time: $L_{discrete} = \emptyset$



• Discrete-time: $L_{discrete} = \emptyset$

However, it does result from the following parallel composition:



Classical verification problems

- reachability of a control state
- $\mathcal{S} \sim \mathcal{S}'$: bisimulation, etc...
- $L(S) \subseteq L(S')$: language inclusion
- $\mathcal{S} \models \varphi$ for some formula φ : model-checking
- $S \parallel A_T$ + reachability: testing automata
- . . .

Classical temporal logics



→ LTL: Linear Temporal Logic [Pnu77], CTL: Computation Tree Logic [EC82]

[Pnu77] Pnueli. The temporal logic of programs (FoCS'77).

[EC82] Emerson, Clarke. Using branching time temporal logic to synthesize synchronization skeletons (Science of Computer Programming 1982).

Classical temporal logics allow us to express that "any problem is followed by an alarm"

[ACD90] Alur, Courcoubetis, Dill. Model-checking for real-time systems (LICS'90).
[ACD93] Alur, Courcoubetis, Dill. Model-checking in dense real-time (Information and Computation).
[HNS'94] Henzinger, Nicollin, Sifakis, Yovine. Symbolic model-checking for real-time systems (ACM Transactions on Computational Logic).

Classical temporal logics allow us to express that "any problem is followed by an alarm"

With CTL:

 $\mathbf{AG}\,(\mathtt{problem} \Rightarrow \mathbf{AF}\,\mathtt{alarm})$

[ACD90] Alur, Courcoubetis, Dill. Model-checking for real-time systems (LICS'90).
[ACD93] Alur, Courcoubetis, Dill. Model-checking in dense real-time (Information and Computation).
[HNS'94] Henzinger, Nicollin, Sifakis, Yovine. Symbolic model-checking for real-time systems (ACM Transactions on Computational Logic).

Classical temporal logics allow us to express that "any problem is followed by an alarm"

With CTL:

 $\mathbf{AG}\left(\mathtt{problem}\Rightarrow\mathbf{AF}\;\mathtt{alarm}
ight)$

How can we express:

"any problem is followed by an alarm within 20 time units"?

[ACD90] Alur, Courcoubetis, Dill. Model-checking for real-time systems (LICS'90).
[ACD93] Alur, Courcoubetis, Dill. Model-checking in dense real-time (Information and Computation).
[HNS'94] Henzinger, Nicollin, Sifakis, Yovine. Symbolic model-checking for real-time systems (ACM Transactions on Computational Logic).

Classical temporal logics allow us to express that "any problem is followed by an alarm"

With CTL:

```
\mathbf{AG}\left(\mathtt{problem} \Rightarrow \mathbf{AF} \; \mathtt{alarm}\right)
```

How can we express:

"any problem is followed by an alarm within 20 time units"?

• Temporal logics with subscripts.

ex: CTL +
$$\begin{bmatrix} \mathbf{E} \varphi \, \mathbf{U}_{\sim k} \, \psi \\ \mathbf{A} \varphi \, \mathbf{U}_{\sim k} \, \psi \end{bmatrix}$$

Classical temporal logics allow us to express that "any problem is followed by an alarm"

With CTL:

```
\mathbf{AG}\left(\mathtt{problem}\Rightarrow\mathbf{AF}\;\mathtt{alarm}
ight)
```

How can we express:

"any problem is followed by an alarm within 20 time units"?

• Temporal logics with subscripts.

 $\mathbf{AG}(\mathtt{problem} \Rightarrow \mathbf{AF}_{\leq 20} \; \mathtt{alarm})$

Classical temporal logics allow us to express that "any problem is followed by an alarm"

With CTL:

```
\mathbf{AG}\left(\mathtt{problem}\Rightarrow\mathbf{AF}\;\mathtt{alarm}
ight)
```

How can we express:

"any problem is followed by an alarm within 20 time units"?

• Temporal logics with subscripts.

$$\mathbf{AG}\left(\mathtt{problem} \Rightarrow \mathbf{AF}_{<20} \;\; \mathtt{alarm}
ight)$$

• Temporal logics with clocks.

 $\mathbf{AG} (\texttt{problem} \Rightarrow (x \texttt{ in } \mathbf{AF} (x \leq 20 \land \texttt{alarm})))$

Classical temporal logics allow us to express that "any problem is followed by an alarm"

With CTL:

```
\mathbf{AG}\left(\mathtt{problem}\Rightarrow\mathbf{AF}\;\mathtt{alarm}
ight)
```

How can we express:

"any problem is followed by an alarm within 20 time units"?

• Temporal logics with subscripts.

$$\mathbf{AG}\left(\mathtt{problem} \Rightarrow \mathbf{AF}_{< 20} \;\; \mathtt{alarm}
ight)$$

• Temporal logics with clocks.

 $\mathbf{AG} (\texttt{problem} \Rightarrow (x \texttt{ in } \mathbf{AF} (x \leq 20 \land \texttt{alarm})))$

→ TCTL: Timed CTL [ACD90, ACD93, HNSY94]

[ACD90] Alur, Courcoubetis, Dill. Model-checking for real-time systems (LICS'90).
[ACD93] Alur, Courcoubetis, Dill. Model-checking in dense real-time (Information and Computation).
[HNSY94] Heuringer, Nicolini, Sifakis, Yovine. Symbolic model-checking for real-time systems (ACM Transactions on Computational Logic).

(1)

The train crossing example

Train_{*i*} with i = 1, 2, ...



(2)

The train crossing example

The gate:



The controller:



(4)

We use the synchronization function f:

$Train_1$	$Train_2$	Gate	Controller	
App!			App?	Арр
•	App!		App?	Арр
Exit!		•	Exit?	Exit
	Exit!	•	Exit?	Exit
а				а
	а			а
		а		а
		GoUp?	GoUp!	GoUp
		GoDown?	GoDown!	GoDown

to define the parallel composition (Train₁ || Train₂ || Gate || Controller)

NB: the parallel composition does not add expressive power!

(5)

Some properties one could check:

• Is the gate closed when a train crosses the road?

(5)

Some properties one could check:

• Is the gate closed when a train crosses the road?

AG (train.On \Rightarrow gate.Close)

(5)

Some properties one could check:

• Is the gate closed when a train crosses the road?

AG (train.On \Rightarrow gate.Close)

• Is the gate always closed for less than 5 minutes?

(5)

Some properties one could check:

• Is the gate closed when a train crosses the road?

AG (train.On \Rightarrow gate.Close)

• Is the gate always closed for less than 5 minutes?

 $\neg \mathbf{EF}$ (gate.Close $\land \mathbf{E}$ (gate.Close $\mathbf{U}_{>5 \text{ min}} \neg$ gate.Close))

Another example: A Fischer protocol

A mutual exclusion protocol with a shared variable *id* [AL94].

Another example: A Fischer protocol

A mutual exclusion protocol with a shared variable *id* [AL94].

Process i:

- a: await (id = 0);
- b : set id to i;
- c: await (id = i);
- d : enter critical section.

 \sim a max. delay k_1 between a and b a min. delay k_2 between b and c

Another example: A Fischer protocol

A mutual exclusion protocol with a shared variable *id* [AL94].

Process i:

- a: await (id = 0);
- *b* : set *id* to *i*;
- c: await (id = i);
- d : enter critical section.

 \sim a max. delay k_1 between a and b a min. delay k_2 between b and c

 \sim See the demo with the tool Uppaal (can be downloaded freely on http://www.uppaal.com/)

Outline

1. Introduction

2. The timed automaton model

3. Timed automata, decidability issues

- 4. How far can we extend the model and preserve decidability? Hybrid systems Smaller extensions of timed automata An alternative way of proving decidability
- 5. Timed automata in practice
- 6. Conclusion

Emptiness problem: is the language accepted by a timed automaton empty?

- basic reachability/safety properties
- basic liveness properties

(final states)

(ω -regular conditions)

Emptiness problem: is the language accepted by a timed automaton empty?

● Problem: the set of configurations is infinite
 ~> classical methods for finite-state systems cannot be applied

Emptiness problem: is the language accepted by a timed automaton empty?

- \bullet Problem: the set of configurations is infinite \sim classical methods for finite-state systems cannot be applied
- Positive key point: variables (clocks) increase at the same speed

Emptiness problem: is the language accepted by a timed automaton empty?

- Problem: the set of configurations is infinite \sim classical methods for finite-state systems cannot be applied
- Positive key point: variables (clocks) increase at the same speed

Theorem [AD90, AD94]

The emptiness problem for timed automata is decidable and PSPACE-complete.

Emptiness problem: is the language accepted by a timed automaton empty?

- Problem: the set of configurations is infinite \sim classical methods for finite-state systems cannot be applied
- Positive key point: variables (clocks) increase at the same speed

Theorem [AD90, AD94]

The emptiness problem for timed automata is decidable and PSPACE-complete.

Method: construct a finite abstraction

[AD90] Alur, Dill. Automata for modeling real-time systems (ICALP'90). [AD94] Alur, Dill. A theory of timed automata (Theoretical Computer Science).




• "compatibility" between regions and constraints



- "compatibility" between regions and constraints
- "compatibility" between regions and time elapsing



- "compatibility" between regions and constraints
- "compatibility" between regions and time elapsing



- "compatibility" between regions and constraints
- "compatibility" between regions and time elapsing

→ an equivalence of finite index a time-abstract bisimulation













 $(\ell_0, v_0) \xrightarrow{a_1, t_1} (\ell_1, v_1) \xrightarrow{a_2, t_2} (\ell_2, v_2) \xrightarrow{a_3, t_3} \dots$









 $\begin{cases} 2 < x < 3 \\ 1 < y < 2 \\ \{x\} < \{y\} \end{cases}$



time successors



reset of clock y



reset of clock x

The region graph

A finite graph representing time elapsing and reset of clocks:



Region automaton \equiv finite bisimulation quotient

timed automaton \otimes region graph

Region automaton \equiv finite bisimulation quotient

timed automaton \otimes region graph

$$\ell \xrightarrow{g,a,C:=0} \ell'$$
 is transformed into:
 $(\ell, R) \xrightarrow{a} (\ell', R')$ if there exists $R'' \in S$

$$(\ell, R) \longrightarrow (\ell', R')$$
 if there exists $R'' \in \operatorname{Succ}_t^*(R)$ s.t.

Region automaton \equiv finite bisimulation quotient

timed automaton \otimes region graph

$$\ell \xrightarrow{g,a,C:=0} \ell' \text{ is transformed into:}$$

$$(\ell, R) \xrightarrow{a} (\ell', R') \text{ if there exists } R'' \in \operatorname{Succ}_t^*(R) \text{ s.t.}$$

$$\bullet R'' \subseteq g$$

$$\bullet [C \leftarrow 0]R'' \subseteq R'$$

 $\mathcal{L}(reg. aut.) = UNTIME(\mathcal{L}(timed aut.))$

where $\mathsf{UNTIME}((a_1, t_1)(a_2, t_2) \dots) = a_1 a_2 \dots$



large (but finite) automaton (region automaton)

 $\mathcal{L}(reg. aut.) = UNTIME(\mathcal{L}(timed aut.))$

An example [AD94]







The size of the region graph is in $\mathcal{O}(|X|!.2^{|X|})$

• One configuration: a discrete location + a region

The size of the region graph is in $\mathcal{O}(|X|!.2^{|X|})$

- One configuration: a discrete location + a region
 - a discrete location: log-space

The size of the region graph is in $\mathcal{O}(|X|!.2^{|X|})$

- One configuration: a discrete location + a region
 - a discrete location: log-space
 - a region:
 - an interval for each clock
 - an interval for each pair of clocks

The size of the region graph is in $\mathcal{O}(|X|!.2^{|X|})$

• One configuration: a discrete location + a region

- a discrete location: log-space
- a region:
 - an interval for each clock
 - an interval for each pair of clocks

 \rightsquigarrow requires polynomial space

The size of the region graph is in $\mathcal{O}(|X|!.2^{|X|})$

• One configuration: a discrete location + a region

- a discrete location: log-space
- a region:
 - an interval for each clock
 - an interval for each pair of clocks

 \rightsquigarrow requires polynomial space

• By guessing a path of length at most exponential: needs only to store two consecutive configurations

The size of the region graph is in $\mathcal{O}(|X|!.2^{|X|})$

• One configuration: a discrete location + a region

- a discrete location: log-space
- a region:
 - an interval for each clock
 - an interval for each pair of clocks

 \rightsquigarrow requires polynomial space

• By guessing a path of length at most exponential: needs only to store two consecutive configurations

 \sim in NPSPACE, thus in PSPACE

PSPACE-hardness

 $\begin{array}{c} \mathcal{M} \mbox{ LBTM } \\ w_0 \in \{a, b\}^* \end{array} \right\} \ \ \, \sim \ \ \, A_{\mathcal{M}, w_0} \mbox{ s.t. } \mathcal{M} \mbox{ accepts } w_0 \mbox{ iff the final state } \\ \ \ \, of \ \, A_{\mathcal{M}, w_0} \mbox{ is reachable } \end{array}$

$$w_0 \qquad C_j \\ \downarrow \\ \{x_j, y_j\}$$

 C_j contains an "a" if $x_j = y_j$ C_j contains a "b" if $x_j < y_j$

(these conditions are invariant by time elapsing)

LBTM: linearly bounded Turing machine (a witness for PSPACE-complete problems)

PSPACE-hardness (cont.)

If $q \xrightarrow{\alpha, \alpha', \delta} q'$ is a transition of \mathcal{M} , then for each position *i* of the tape, we have a transition

$$(q,i) \xrightarrow{g,r:=0} (q',i')$$

where:

•
$$g$$
 is $x_i = y_i$ (resp. $x_i < y_i$) if $\alpha = a$ (resp. $\alpha = b$)
• $r = \{x_i, y_i\}$ (resp. $r = \{x_i\}$) if $\alpha' = a$ (resp. $\alpha' = b$)
• $i' = i + 1$ (resp. $i' = i - 1$) if δ is right and $i < n$ (resp. left)

Enforcing time elapsing: on each transition, add the condition t = 1 and clock t is reset.

Initialization: init $\xrightarrow{t=1,r_0:=0}$ $(q_0,1)$ where $r_0 = \{x_i \mid w_0[i] = b\} \cup \{t\}$ Termination: $(q_f, i) \longrightarrow$ end

The case of single-clock timed automata

Exercise [LMS04]

Think of the special case of single-clock timed automata. Can we do better than PSPACE?

Consequence of region automata construction

Region automata:

correct finite (and exponential) abstraction for checking reachability/Büchi-like properties.

Consequence of region automata construction

Region automata:

correct finite (and exponential) abstraction for checking reachability/Büchi-like properties.

However...

everything can not be reduced to finite automata...

A model not far from undecidability

Some bad news...

- Language universality is undecidable
- Language inclusion is undecidable
- Complementability is undecidable

• ...

[AD90] [AD90] [Tri03,Fin06]

[Fin06] Finkel. Undecidable problems about timed automata (FORMATS'06).

[AM04] Alur, Madhusudan. Decision problems for timed automata: A survey (SFM-04:RT)).

A model not far from undecidability

Some bad news...

• ...

- Language universality is undecidable
- Language inclusion is undecidable
- Complementability is undecidable

[AD90] [AD90] [Tri03,Fin06]

An example of non-determinizable/non-complementable timed aut.:



[Tri03] Tripakis. Folk theorems on the determinization and minimization of timed automata (FORMATS'03).

[Fin06] Finkel. Undecidable problems about timed automata (FORMATS'06).

[AM04] Alur, Madhusudan. Decision problems for timed automata: A survey (SFM-04:RT)).
A model not far from undecidability

Some bad news...

• ...

- Language universality is undecidable
- Language inclusion is undecidable
- Complementability is undecidable

[AD90] [AD90] [Tri03,Fin06]

An example of non-determinizable/non-complementable timed aut.: [AM04]



[Tri03] Tripakis. Folk theorems on the determinization and minimization of timed automata (FORMATS'03).

[Fin06] Finkel. Undecidable problems about timed automata (FORMATS'06).

[AM04] Alur, Madhusudan. Decision problems for timed automata: A survey (SFM-04:RT)).

A model not far from undecidability

Some bad news...

...

- Language universality is undecidable
- Language inclusion is undecidable
- Complementability is undecidable

[AD90] [AD90] [Tri03,Fin06]

An example of non-determinizable/non-complementable timed aut.: [AM04]



UNTIME $(\overline{L} \cap \{(a^*b^*, \tau) \mid all \ a's \text{ happen before 1 and no two } a's \text{ simultaneously}\})$ is not regular (exercise!)

[Tri03] Tripakis. Folk theorems on the determinization and minimization of timed automata (FORMATS'03).

[Fin06] Finkel. Undecidable problems about timed automata (FORMATS'06).

[AM04] Alur, Madhusudan. Decision problems for timed automata: A survey (SFM-04:RT)).

The two-counter machine

Definition

A two-counter machine is a finite set of instructions over two counters (c and d):

• Incrementation:

(p): c := c + 1; goto (q)

• Decrementation:

(p): if c > 0 then c := c - 1; goto (q) else goto (r)

Theorem [Minsky 67]

The halting problem for two counter machines is undecidable.

Undecidability of universality

Theorem [AD90]

Universality of timed automata is undecidable.



- one configuration is encoded in one time unit
- number of c's: value of counter c
- number of d's: value of counter d
- one time unit between two corresponding c's (resp. d's)

\rightsquigarrow We encode "non-behaviours" of a two-counter machine

Example

Module to check that if instruction *i* does not decrease counter *c*, then all actions *c* appearing less than 1 t.u. after b_i has to be followed by an other *c* 1 t.u. later.



Example

Module to check that if instruction *i* does not decrease counter *c*, then all actions *c* appearing less than 1 t.u. after b_i has to be followed by an other *c* 1 t.u. later.



The union of all small modules is not universal iff The two-counter machine has a recurring computation

- This idea of a finite bisimulation quotient has been applied to many "timed" or "hybrid" systems:
 - various extensions of timed automata

[Bérard,Diekert,Gastin,Petit 1998] [Choffrut,Goldwurm 2000] [Bouyer,Dufourd,Fleury,Petit 2004] · · ·

- This idea of a finite bisimulation quotient has been applied to many "timed" or "hybrid" systems:
 - various extensions of timed automata [Bérard,Diekert,Gastin,Petit 1998] [Choffrut,Goldwurm 2000] [Bouyer,Dufourd,Fleury,Petit 2004] · · ·
 - model-checking of branching-time properties (TCTL, timed μ-calculus)
 [Alur,Courcoubetis,Dill 1993] [Laroussinie,Larsen,Weise 1995]

- This idea of a finite bisimulation quotient has been applied to many "timed" or "hybrid" systems:
 - various extensions of timed automata [Bérard,Diekert,Gastin,Petit 1998] [Choffrut,Goldwurm 2000] [Bouyer,Dufourd,Fleury,Petit 2004] · · ·
 - model-checking of branching-time properties (TCTL, timed μ-calculus)
 [Alur,Courcoubetis,Dill 1993] [Laroussinie,Larsen,Weise 1995]
 - weighted/priced timed automata (e.g. WCTL model-checking, optimal games)
 [Bouyer,Larsen,Markey,Rasmussen 2006] [Bouyer,Larsen,Markey 2007]

- This idea of a finite bisimulation quotient has been applied to many "timed" or "hybrid" systems:
 - various extensions of timed automata [Bérard,Diekert,Gastin,Petit 1998] [Choffrut,Goldwurm 2000] [Bouyer,Dufourd,Fleury,Petit 2004] · · ·
 - model-checking of branching-time properties (TCTL, timed μ-calculus)
 [Alur,Courcoubetis,Dill 1993] [Laroussinie,Larsen,Weise 1995]
 - weighted/priced timed automata (e.g. WCTL model-checking, optimal games)
 [Bouyer,Larsen,Markey,Rasmussen 2006] [Bouyer,Larsen,Markey 2007]
 - o-minimal hybrid systems [Lafferriere,Pappas,Sastry 2000] [Brihaye 2005]

- This idea of a finite bisimulation quotient has been applied to many "timed" or "hybrid" systems:
 - various extensions of timed automata [Bérard,Diekert,Gastin,Petit 1998] [Choffrut,Goldwurm 2000] [Bouyer,Dufourd,Fleury,Petit 2004] · · ·
 - model-checking of branching-time properties (TCTL, timed μ-calculus)
 [Alur,Courcoubetis,Dill 1993] [Laroussinie,Larsen,Weise 1995]
 - weighted/priced timed automata (e.g. WCTL model-checking, optimal games)
 [Bouyer,Larsen,Markey,Rasmussen 2006] [Bouyer,Larsen,Markey 2007]
 - o-minimal hybrid systems [Lafferriere,Pappas,Sastry 2000] [Brihaye 2005]

• • • •

- This idea of a finite bisimulation quotient has been applied to many "timed" or "hybrid" systems:
 - various extensions of timed automata [Bérard,Diekert,Gastin,Petit 1998] [Choffrut,Goldwurm 2000] [Bouyer,Dufourd,Fleury,Petit 2004] · · ·
 - model-checking of branching-time properties (TCTL, timed μ-calculus)
 [Alur,Courcoubetis,Dill 1993] [Laroussinie,Larsen,Weise 1995]
 - weighted/priced timed automata (e.g. WCTL model-checking, optimal games)
 [Bouyer,Larsen,Markey,Rasmussen 2006] [Bouyer,Larsen,Markey 2007]
 - o-minimal hybrid systems [Lafferriere,Pappas,Sastry 2000] [Brihaye 2005]

••••

• Note however that it might be hard to prove there is a finite bisimulation quotient!

Outline

- 1. Introduction
- 2. The timed automaton model
- 3. Timed automata, decidability issues
- 4. How far can we extend the model and preserve decidability?

Hybrid systems Smaller extensions of timed automata An alternative way of proving decidability

- 5. Timed automata in practice
- 6. Conclusion

Outline

- 1. Introduction
- 2. The timed automaton model
- 3. Timed automata, decidability issues
- 4. How far can we extend the model and preserve decidability? Hybrid systems

Smaller extensions of timed automata An alternative way of proving decidability

- 5. Timed automata in practice
- 6. Conclusion

A general model: hybrid systems

[Hen96]

What is a hybrid system?

a discrete control (the mode of the system)

+ a continuous evolution within a mode (given by variables)

A general model: hybrid systems

[Hen96]

What is a hybrid system?

a discrete control (the mode of the system)

+ a continuous evolution within a mode (given by variables)

Example (The thermostat)

A simple thermostat, where T (the temperature) depends on the time:



[Hen96] Henzinger. The theory of hybrid automata (LICS'96).

The thermostat example



The thermostat example





How far can we extend the model and preserve decidability?

Ok...



How far can we extend the model and preserve decidability?

Ok...



Easy...

Ok...



Easy...



Ok...



Easy...



Easy...

Ok... but?



Ok... but?



What about decidability?

 \rightsquigarrow almost everything is undecidable

Negative results [HKPV95]

- The class of hybrid systems with clocks and only one variable having possibly two slopes $k_1 \neq k_2$ is undecidable.
- The class of *stopwatch* automata is undecidable.

Outline

- 1. Introduction
- 2. The timed automaton model
- 3. Timed automata, decidability issues
- 4. How far can we extend the model and preserve decidability? Hybrid systems Smaller extensions of timed automata An alternative way of proving decidability
- 5. Timed automata in practice
- 6. Conclusion

Role of diagonal constraints



Role of diagonal constraints

$$x - y \sim c$$
 and $x \sim c$

• Decidability: yes, using the region abstraction



Role of diagonal constraints

$$x - y \sim c$$
 and $x \sim c$

• Decidability: yes, using the region abstraction



• Expressiveness: no additional expressive power

Role of diagonal constraints (cont.)



[BDGP98] Bérard, Diekert, Gastin, Petit. Characterization of the expressive power of silent transitions in timed automata (Fundamenta Informaticae).

Role of diagonal constraints (cont.)

Exercise [BC05]

Consider, for every positive integer n, the timed language:

$$\mathcal{L}_n = \{(a, t_1) \dots (a, t_{2^n}) \mid 0 < t_1 < \dots < t_{2^n} < 1\}$$

- Construct a timed automaton with diagonal constraints which recognizes L_n. What is the size of this automaton?
- Idem without diagonal constraints. Can you do better?
- Conclude.

Adding silent actions

$$[BDGP98]$$

Adding silent actions

$$g, \varepsilon, C := 0$$
[BDGP98]

• Decidability: yes

(actions have no influence on region automaton construction)

Adding silent actions

$$g, \varepsilon, C := 0$$
[BDGP98]

• Decidability: yes

(actions have no influence on region automaton construction)

• Expressiveness: strictly more expressive!



Adding additive constraints

$$x + y \sim c \quad \text{and} \quad x \sim c \quad [BD00]$$

[BD00] Bérard, Dufourd. Timed automata and additive clock constraints (Information Processing Letters).

Adding additive constraints

$$x + y \sim c \quad \text{and} \quad x \sim c \quad [BD00]$$

• Decidability: - for two clocks, decidable using the abstraction


Adding additive constraints

$$x + y \sim c \quad \text{and} \quad x \sim c \quad [BD00]$$

• Decidability: - for two clocks, decidable using the abstraction



- for four clocks (or more), undecidable!

• Expressiveness: more expressive! (even using two clocks)

$$x + y = 1, a, x := 0$$

 $\{(a^n, t_1 \dots t_n) \mid n \ge 1 \text{ and } t_i = 1 - \frac{1}{2^i}\}$

[BD00] Bérard, Dufourd. Timed automata and additive clock constraints (Information Processing Letters).

Undecidability proof



 \sim simulation of \bullet decrementation of a counter \bullet incrementation of a counter

We will use 4 clocks:

- *u*, "tic" clock (each time unit)
- x_0 , x_1 , x_2 : reference clocks for the two counters

" x_i reference for c" \equiv "the last time x_i has been reset is the last time action c has been performed"

Undecidability proof (cont.)



ref for c is x_0

ref for c is x_2

• Decrementation of counter c:



Adding constraints of the form $x + y \sim c$

• Two clocks: decidable using the abstraction



• Four clocks (or more): undecidable!

Adding constraints of the form $x + y \sim c$

• Two clocks: decidable using the abstraction





• Four clocks (or more): undecidable!

Adding new operations on clocks

Several types of updates: x := y + c, x :< c, x :> c, etc...

Adding new operations on clocks

Several types of updates: x := y + c, x :< c, x :> c, etc...

• The general model is undecidable. (simulation of a two-counter machine)

Adding new operations on clocks

Several types of updates: x := y + c, x :< c, x :> c, etc...

- The general model is undecidable. (simulation of a two-counter machine)
- Only decrementation also leads to undecidability



Decidability



The classical region automaton construction is not correct.

Decidability (cont.)

- $\mathcal{A} \quad \rightsquigarrow \quad \mathsf{Diophantine\ linear\ inequations\ system}$
 - \rightsquigarrow $\;$ is there a solution?
 - \rightsquigarrow $\;$ if yes, belongs to a decidable class

Examples:

٩	constraint $x \sim c$	$c \leq \max_x$
٩	constraint $x - y \sim c$	$c \leq \max_{x,y}$
٩	update $x : \sim y + c$	$\max_x \leq \max_y + c$
	and for each clock z,	$\max_{x,z} \geq \max_{y,z} + c, \max_{z,x} \geq \max_{z,y} - c$
٩	update x :< c	$c \leq \max_x$
		and for each clock z , $\max_{z} \ge c + \max_{z,x} c$

The constants (max_x) and $(max_{x,y})$ define a set of regions.

How far can we extend the model and preserve decidability?

Decidability (cont.)



The bisimulation property is met.



Decrementation x := x - 1



Decrementation x := x - 1



Decrementation x := x - 1



Decrementation x := x - 1



Decrementation x := x - 1



Decrementation x := x - 1



Decrementation x := x - 1



Decidability (cont.)

	Diagonal-free constraints	General constraints
x := c, x := y		PSPACE-complete
x := x + 1	PSPACE-complete	
x := y + c		Undecidable
x := x - 1	Undecidable	
x :< c		PSPACE-complete
x :> c	PSPACE_complete	
$x :\sim y + c$	1 SI ACE-complete	Undecidable
y + c <: x :< y + d		Ondeeldable
y + c <: x :< z + d	Undecidable	

[BDFP00]

Outline

- 1. Introduction
- 2. The timed automaton model
- 3. Timed automata, decidability issues
- 4. How far can we extend the model and preserve decidability? Hybrid systems Smaller extensions of timed automata An alternative way of proving decidability
- 5. Timed automata in practice
- 6. Conclusion

How far can we extend the model and preserve decidability?

The example of alternating timed automata

Alternating timed automata \equiv ATA

[LW05,OW05]

[LW05] Lasota, Walukiewicz. Alternating timed automata (FoSSaCS'05). [OW05] Ouaknine, Worrell. On the decidability of Metric Temporal Logic (LICS'05).

The example of alternating timed automata Alternating timed automata = ATA



Example

"No two a's are separated by 1 unit of time"

$$\left\{ \begin{array}{cccc} \ell_0, a, true & \mapsto & \ell_0 \land (x := 0, \ell_1) \\ \ell_1, a, x \neq 1 & \mapsto & \ell_1 \\ \ell_1, a, x = 1 & \mapsto & \ell_2 \\ \ell_2, a, true & \mapsto & \ell_2 \end{array} \right\} \left\{ \begin{array}{c} \ell_0 \text{ initial state} \\ \ell_0, \ell_1 \text{ final states} \\ \ell_2 \text{ losing state} \end{array} \right\}$$

[LW05] Lasota, Walukiewicz. Alternating timed automata (FoSSaCS'05). [OW05] Ouaknine, Worrell. On the decidability of Metric Temporal Logic (LICS'05).

The example of alternating timed automata Alternating timed automata = ATA



Example

"No two a's are separated by 1 unit of time"





[LW05] Lasota, Walukiewicz. Alternating timed automata (FoSSaCS'05). [OW05] Ouaknine, Worrell. On the decidability of Metric Temporal Logic (LICS'05).

 \rightsquigarrow universality is as difficult as reachability

 \rightsquigarrow universality is as difficult as reachability

• more expressive than timed automata

 \rightsquigarrow universality is as difficult as reachability

• more expressive than timed automata

Theorem

- Emptiness of ATA is undecidable.
- Emptiness of one-clock ATA is decidable, but non-primitive recursive.
- Emptiness for Büchi properties of one-clock ATA is undecidable.
- Emptiness of one-clock ATA with ε -transitions is undecidable.

 \rightsquigarrow universality is as difficult as reachability

• more expressive than timed automata

Theorem

- Emptiness of ATA is undecidable.
- Emptiness of one-clock ATA is decidable, but non-primitive recursive.
- Emptiness for Büchi properties of one-clock ATA is undecidable.
- Emptiness of one-clock ATA with ε -transitions is undecidable.

Lower bound: simulation of a lossy channel system... [Sch02]







Execution over timed word (a, .3)(a, .8)(a, 1.4)(a, 1.8)(a, 2)

 $\{ (\ell_0, 0) \}$




















A configuration = a finite set of pairs (ℓ, x)

 $(\ell, 0)$ $(\ell, 0.3)$ $(\ell, 1.2)$ $(\ell, 2.3)$ $(\ell', 0.4)$ $(\ell', 1)$ $(\ell', 0.8)$













A configuration = a finite set of pairs (ℓ, x)



Abstracted into:

 $\{(\ell,0),(\ell',1)\} \cdot \{(\ell,1)\} \cdot \{(\ell,0),(\ell,2)\} \cdot \{(\ell',0)\} \cdot \{(\ell',0)\}$

 $\{(\ell,0),(\ell',1)\} \cdot \{(\ell,1)\} \cdot \{(\ell,0),(\ell,2)\} \cdot \{(\ell',0)\} \cdot \{(\ell',0)\}$



 $\{(\ell,0),(\ell',1)\} \cdot \{(\ell,1)\} \cdot \{(\ell,0),(\ell,2)\} \cdot \{(\ell',0)\} \cdot \{(\ell',0)\}$



 $\{(\ell,0),(\ell',1)\} \cdot \{(\ell,1)\} \cdot \{(\ell,0),(\ell,2)\} \cdot \{(\ell',0)\} \cdot \{(\ell',0)\}$

 $\{(\ell',1)\} \cdot \{(\ell,0),(\ell',1)\} \cdot \{(\ell,1)\} \cdot \{(\ell,0),(\ell,2)\} \cdot \{(\ell',0)\}$ $\{(\ell',1)\} \cdot \{(\ell',1)\} \cdot \{(\ell,0),(\ell',1)\} \cdot \{(\ell,1)\} \cdot \{(\ell,0),(\ell,2)\}$

 $\{(\ell,0),(\ell',1)\} \cdot \{(\ell,1)\} \cdot \{(\ell,0),(\ell,2)\} \cdot \{(\ell',0)\} \cdot \{(\ell',0)\}$

 $\{(\ell',1)\} \cdot \{(\ell,0),(\ell',1)\} \cdot \{(\ell,1)\} \cdot \{(\ell,0),(\ell,2)\} \cdot \{(\ell',0)\}$ $\{(\ell',1)\} \cdot \{(\ell',1)\} \cdot \{(\ell,0),(\ell',1)\} \cdot \{(\ell,1)\} \cdot \{(\ell,0),(\ell,2)\}$ $\{(\ell,1),(\ell,3)\} \cdot \{(\ell',1)\} \cdot \{(\ell',1)\} \cdot \{(\ell,0),(\ell',1)\} \cdot \{(\ell,1)\}$

 $\{(\ell,0),(\ell',1)\} \cdot \{(\ell,1)\} \cdot \{(\ell,0),(\ell,2)\} \cdot \{(\ell',0)\} \cdot \{(\ell',0)\}$



 $\{(\ell,0),(\ell',1)\} \cdot \{(\ell,1)\} \cdot \{(\ell,0),(\ell,2)\} \cdot \{(\ell',0)\} \cdot \{(\ell',0)\}$



 $\{(\ell,0),(\ell',1)\} \cdot \{(\ell,1)\} \cdot \{(\ell,0),(\ell,2)\} \cdot \{(\ell',0)\} \cdot \{(\ell',0)\}$



 $\{(\ell,0),(\ell',1)\} \quad \{(\ell,1)\} \quad \{(\ell,0),(\ell,2)\} \quad \{(\ell',0)\} \quad \{(\ell',0)\}$

Time successors:



 $\{(\ell'',0)\} \cdot \{(\ell,1),(\ell',2)\} \cdot \{(\ell,1)\} \cdot \{(\ell',1)\} \cdot \{(\ell',1)\}$

How far can we extend the model and preserve decidability?

What can we do with that abstract transition system? Correctness?

The previous abstraction is (almost) a time-abstract bisimulation.

The previous abstraction is (almost) a time-abstract bisimulation.

Termination?

The previous abstraction is (almost) a time-abstract bisimulation.

Termination?

© possibly infinitely many abstract configurations

The previous abstraction is (almost) a time-abstract bisimulation.

Termination?

- © possibly infinitely many abstract configurations
- ☺ there is a well-quasi ordering on the set of abstract configurations! (subword relation ⊆)

The previous abstraction is (almost) a time-abstract bisimulation.

Termination?

- © possibly infinitely many abstract configurations
- ☺ there is a well-quasi ordering on the set of abstract configurations! (subword relation ⊑)

+ downward compatibility:

 $(\gamma_1 \sqsubseteq \gamma'_1 \text{ and } \gamma'_1 \rightsquigarrow \gamma'_2) \Rightarrow (\gamma_1 \rightsquigarrow^* \gamma_2 \text{ and } \gamma_2 \sqsubseteq \gamma'_2)$

The previous abstraction is (almost) a time-abstract bisimulation.

Termination?

- © possibly infinitely many abstract configurations
- ☺ there is a well-quasi ordering on the set of abstract configurations! (subword relation ⊆)
 - + downward compatibility:

$$(\gamma_1 \sqsubseteq \gamma'_1 \text{ and } \gamma'_1 \rightsquigarrow \gamma'_2) \Rightarrow (\gamma_1 \rightsquigarrow^* \gamma_2 \text{ and } \gamma_2 \sqsubseteq \gamma'_2)$$

+ downward-closed objective (all states are accepting)

The previous abstraction is (almost) a time-abstract bisimulation.

Termination?

- c possibly infinitely many abstract configurations
- ☺ there is a well-quasi ordering on the set of abstract configurations! (subword relation ⊆)
 - + downward compatibility:

$$(\gamma_1 \sqsubseteq \gamma'_1 \text{ and } \gamma'_1 \rightsquigarrow \gamma'_2) \Rightarrow (\gamma_1 \rightsquigarrow^* \gamma_2 \text{ and } \gamma_2 \sqsubseteq \gamma'_2)$$

+ downward-closed objective (all states are accepting)

A recipe:

The previous abstraction is (almost) a time-abstract bisimulation.

Termination?

- © possibly infinitely many abstract configurations
- ☺ there is a well-quasi ordering on the set of abstract configurations! (subword relation ⊆)
 - + downward compatibility:

 $(\gamma_1 \sqsubseteq \gamma'_1 \text{ and } \gamma'_1 \rightsquigarrow \gamma'_2) \Rightarrow (\gamma_1 \rightsquigarrow^* \gamma_2 \text{ and } \gamma_2 \sqsubseteq \gamma'_2)$

+ downward-closed objective (all states are accepting)

A recipe:

```
(\mathsf{Higman's}\;\mathsf{lemma}\;+\;\mathsf{Koenig's}\;\mathsf{lemma}) \Rightarrow \mathsf{termination}
```

The previous abstraction is (almost) a time-abstract bisimulation.

Termination?

- © possibly infinitely many abstract configurations
- ☺ there is a well-quasi ordering on the set of abstract configurations! (subword relation ⊆)
 - + downward compatibility:

 $(\gamma_1 \sqsubseteq \gamma'_1 \text{ and } \gamma'_1 \rightsquigarrow \gamma'_2) \Rightarrow (\gamma_1 \rightsquigarrow^* \gamma_2 \text{ and } \gamma_2 \sqsubseteq \gamma'_2)$

+ downward-closed objective (all states are accepting)

A recipe:

```
(Higman's lemma + Koenig's lemma) \Rightarrow termination
```

Alternative

The abstract transition system can be simulated by a kind of FIFO channel machine.





$$x, y \in r_0, \{y\} < \{x\}$$





$$x \in r_1, y \in r_0, \{x\} < \{y\}$$





$$x, y \in r_1, \{y\} < \{x\}$$





The classical region automaton can be simulated by a channel machine (with a single bounded channel).

Similar technics apply to:

• networks of single-clock timed automata

[Abdulla, Jonsson 1998]

Similar technics apply to:

- networks of single-clock timed automata
- timed Petri nets

[Abdulla, Jonsson 1998]

[Abdulla,Nylén 2001]

Similar technics apply to:

- networks of single-clock timed automata
- timed Petri nets
- MTL model checking

[Abdulla, Jonsson 1998]

[Abdulla,Nylén 2001]

[Ouaknine,Worrell 2005,2007]

Similar technics apply to:

- networks of single-clock timed automata
- timed Petri nets
- MTL model checking

[Abdulla, Jonsson 1998]

[Abdulla,Nylén 2001]

[Ouaknine,Worrell 2005,2007]

• coFlatMTL model checking [Bouyer,Markey,Ouaknine,Worrell 2007] (using channel machines with a bounded number of cycles)
Partial conclusion

Similar technics apply to:

- networks of single-clock timed automata
- timed Petri nets
- MTL model checking

[Abdulla, Jonsson 1998]

[Abdulla,Nylén 2001]

[Ouaknine,Worrell 2005,2007]

- coFlatMTL model checking [Bouyer,Markey,Ouaknine,Worrell 2007] (using channel machines with a bounded number of cycles)
- single-clock automata inclusion checking [Ouaknine,Worrell 2004]

Partial conclusion

Similar technics apply to:

- networks of single-clock timed automata
- timed Petri nets
- MTL model checking

[Abdulla, Jonsson 1998]

[Abdulla,Nylén 2001]

[Ouaknine,Worrell 2005,2007]

- coFlatMTL model checking [Bouyer,Markey,Ouaknine,Worrell 2007] (using channel machines with a bounded number of cycles)
- single-clock automata inclusion checking [Ouaknine,Worrell 2004]

o ...

Outline

- 1. Introduction
- 2. The timed automaton model
- 3. Timed automata, decidability issues
- 4. How far can we extend the model and preserve decidability? Hybrid systems Smaller extensions of timed automata An alternative way of proving decidability
- 5. Timed automata in practice
- 6. Conclusion

- the region automaton is never computed
- instead, symbolic computations are performed

What do we need?

• Need of a symbolic representation:

- the region automaton is never computed
- instead, symbolic computations are performed

What do we need?

• Need of a symbolic representation:

Finite representation of infinite sets of configurations

 in the plane, a line represented by two points.

- the region automaton is never computed
- instead, symbolic computations are performed

What do we need?

• Need of a symbolic representation:

- in the plane, a line represented by two points.
- set of words aa, aaaa, aaaaaa...
 represented by a rational expression aa(aa)*

- the region automaton is never computed
- instead, symbolic computations are performed

What do we need?

• Need of a symbolic representation:

- in the plane, a line represented by two points.
- set of words aa, aaaa, aaaaaaa...
 represented by a rational expression aa(aa)*
- set of integers, represented using semi-linear sets

- the region automaton is never computed
- instead, symbolic computations are performed

What do we need?

• Need of a symbolic representation:

- in the plane, a line represented by two points.
- set of words aa, aaaa, aaaaaa...
 represented by a rational expression aa(aa)*
- set of integers, represented using semi-linear sets
- sets of constraints, polyhedra, zones, regions

- the region automaton is never computed
- instead, symbolic computations are performed

What do we need?

• Need of a symbolic representation:

- in the plane, a line represented by two points.
- set of words aa, aaaa, aaaaaa...
 represented by a rational expression aa(aa)*
- set of integers, represented using semi-linear sets
- sets of constraints, polyhedra, zones, regions
- BDDs, DBMs (see later), CDDs, etc...

- the region automaton is never computed
- instead, symbolic computations are performed

What do we need?

• Need of a symbolic representation:

- in the plane, a line represented by two points.
- set of words aa, aaaa, aaaaaaa...
 represented by a rational expression aa(aa)*
- set of integers, represented using semi-linear sets
- sets of constraints, polyhedra, zones, regions
- BDDs, DBMs (see later), CDDs, etc...
- Need of abstractions, heuristics, etc...

An example of computation with HyTech

```
command: /usr/local/bin/hytech gas_burner
                    _____
HvTech: symbolic model checker for embedded systems
Version 1.04f (last modified 1/24/02) from v1.04a of 12/6/96
For more info:
   email: hvtech@eecs.berkelev.edu
   http://www.eecs.berkeley.edu/~tah/HyTech
Warning: Input has changed from version 1.00(a). Use -i for more info
Backward computation
Number of iterations required for reachability: 6
System satisfies non-leaking duration property
Location: not_leaking
x >= 0 & t >= 3 & v <= 20t & v >= 0
| x + 20t >= y + 11 & y <= 20t + 19 & t >= 2 & x >= 0 & y >= 0
| y >= 0 & t >= 1 & x + 20t >= y + 22 & y <= 20t + 8 & x >= 0
| v >= 0 \& x + 20t >= v + 33 \& 20t >= v + 3 \& x >= 0
Location: leaking
19x + y \le 20t + 19 \& y \ge x + 59 \& x \le 1 \& x \ge 0
| t >= x + 2 & x <= 1 & y >= 0 & 19x + y <= 20t + 19 & x >= 0
| t >= x + 1 & x <= 1 & y >= 0 & 19x + y <= 20t + 8 & x >= 0
| 20t >= 19x + y + 3 & y >= 0 & x <= 1 & x >= 0
______
                                          _____
Max memory used = 0 pages = 0 bytes = 0.00 MB
Time spent = 0.02u + 0.00s = 0.02 sec total
```

Zones: A symbolic representation for timed systems Example of a zone and its DBM representation $Z = (x_1 > 3) \land (x_2 < 5) \land (x_1 - x_2 < 4)$ x_2 5 $\begin{array}{c} x_0 \\ x_1 \\ x_2 \end{array} \left(\begin{array}{ccc} \infty & -3 & \infty \\ \infty & \infty & 4 \\ 5 & \infty & \infty \end{array} \right)$ $x_1 - x_2 = 4$ 3 X_1

DBM: Difference Bound Matrice [BM83,Dill89]

[BM83] Berthomieu, Menasche. An enumerative approach for analyzing time Petri nets World Comupter Congress. [Dill89] Dill. Timing assumptions and verification of finite-state concurrent systems (Automatic Verification Methods for Finite State Systems).

Zones: A symbolic representation for timed systems Example of a zone and its DBM representation $Z = (x_1 > 3) \land (x_2 < 5) \land (x_1 - x_2 < 4)$ x_2 5 $\begin{array}{c} x_0 \\ x_1 \\ x_2 \end{array} \begin{pmatrix} 0 & -3 & 0 \\ 9 & 0 & 4 \\ 5 & 2 & 0 \end{array} \right)$ 2 $x_1 - x_2 = 4$ "normal form" 3 X_1 4

DBM: Difference Bound Matrice [BM83,Dill89]

[BM83] Berthomieu, Menasche. An enumerative approach for analyzing time Petri nets World Comupter Congress. [Dill89] Dill. Timing assumptions and verification of finite-state concurrent systems (Automatic Verification Methods for Finite State Systems).

Timed automata in practice

Backward computation





77/94





























 $\textcircled{\mbox{$\odot$}}$ the backward computation always terminates! $\textcircled{\mbox{$\odot$}}$... and it is correct!!!

Note on the backward analysis (cont.)

If \mathcal{A} is a timed automaton, we construct its corresponding set of regions.

Because of the bisimulation property, we get that:

"Every set of valuations which is computed along the backward computation is a finite union of regions"

Note on the backward analysis (cont.)

If \mathcal{A} is a timed automaton, we construct its corresponding set of regions.

Because of the bisimulation property, we get that:

"Every set of valuations which is computed along the backward computation is a finite union of regions"

Let R be a region. Assume:

•
$$v \in \overleftarrow{R}$$
 (for ex. $v + t \in R$)

•
$$v' \equiv_{reg.} v$$

There exists t' s.t. $v' + t' \equiv_{reg.} v + t$, which implies that $v' + t' \in R$ and thus $v' \in R$.

Note on the backward analysis (cont.)

If \mathcal{A} is a timed automaton, we construct its corresponding set of regions.

Because of the bisimulation property, we get that:

"Every set of valuations which is computed along the backward computation is a finite union of regions"

But, the backward computation is not so nice, when also dealing with integer variables...

 $i := j.k + \ell.m$









Timed automata in practice





Timed automata in practice














Forward analysis of timed automata



Forward analysis of timed automata



🙁 the forward computation may not terminate...



















 \rightsquigarrow an infinite number of steps...

"Solutions" to this problem

(f.ex. in [Larsen, Pettersson, Yi 1997] or in [Daws, Tripakis 1998])

• inclusion checking: if $Z \subseteq Z'$ and Z' already considered, then we don't need to consider Z

 \sim correct w.r.t. reachability

. . .

"Solutions" to this problem

(f.ex. in [Larsen, Pettersson, Yi 1997] or in [Daws, Tripakis 1998])

• inclusion checking: if $Z \subseteq Z'$ and Z' already considered, then we don't need to consider Z

 \sim correct w.r.t. reachability

● activity: eliminate redundant clocks [Daws,Yovine 1996] → correct w.r.t. reachability

 $q \xrightarrow{g,a,C:=0} q'$ implies $Act(q) = clocks(g) \cup (Act(q') \setminus C)$

. . .

"Solutions" to this problem (cont.)

convex-hull approximation: if Z and Z' are computed then we overapproximate using "Z ⊔ Z'".

→ "semi-correct" w.r.t. reachability



"Solutions" to this problem (cont.)

convex-hull approximation: if Z and Z' are computed then we overapproximate using "Z ⊔ Z'".

→ "semi-correct" w.r.t. reachability



• extrapolation, an abstraction operator on zones

An abstraction: the extrapolation operator

Approx₂(Z): "the smallest zone containing Z that is defined only with constants no more than 2"



 \rightsquigarrow The extrapolation operator ensures termination of the computation!

An abstraction: the extrapolation operator

Approx₂(Z): "the smallest zone containing Z that is defined only with constants no more than 2"



 \rightsquigarrow The extrapolation operator ensures termination of the computation!

Challenge

Choose a **good** constant for the extrapolation so that the forward computation is correct. Classical algorithm, the choice goes to the maximal constant.

Challenge

Choose a **good** constant for the extrapolation so that the forward computation is correct. Classical algorithm, the choice goes to the maximal constant.

- Implemented in tools like Uppaal, Kronos, RT-Spin...
- Successfully used on many real-life examples

Challenge

Choose a **good** constant for the extrapolation so that the forward computation is correct. Classical algorithm, the choice goes to the maximal constant.

- Implemented in tools like Uppaal, Kronos, RT-Spin...
- Successfully used on many real-life examples

Theorem

The classical algorithm is correct for diagonal-free timed automata.

Challenge

Choose a **good** constant for the extrapolation so that the forward computation is correct. Classical algorithm, the choice goes to the maximal constant.

- Implemented in tools like Uppaal, Kronos, RT-Spin...
- Successfully used on many real-life examples

Theorem

The classical algorithm is correct for diagonal-free timed automata.

This theorem does not extend to timed automata using diagonal clock constraints... [Bou03,Bou04]

A problematic automaton



A problematic automaton



 $\begin{cases} v(x_1) = 0 \\ v(x_2) = d \\ v(x_3) = 2\alpha + 5 \\ v(x_4) = 2\alpha + 5 + d \end{cases}$

A problematic automaton



The problematic zone





The problematic zone



If α is sufficiently large, after extrapolation:



does not imply $x_1 - x_2 = x_3 - x_4$.

The problematic zone



If α is sufficiently large, after extrapolation:



Hence, any choice of constant is erroneous!

Criteria for a good abstraction operator Abs:

Criteria for a good abstraction operator Abs :

easy computation

Abs(Z) is a zone if Z is a zone

[Effectiveness]

Criteria for a good abstraction operator Abs :

- easy computation Abs(Z) is a zone if Z is a zone
- finiteness of the abstraction {Abs(Z) | Z zone} is finite

[Effectiveness]

[Termination]

Criteria for a good abstraction operator Abs :

- easy computation Abs(Z) is a zone if Z is a zone
- finiteness of the abstraction {Abs(Z) | Z zone} is finite
- completeness of the abstraction
 Z ⊆ Abs(Z)

[Effectiveness]

[Termination]

[Completeness]

Criteria for a good abstraction operator $\operatorname{Abs:}$

- easy computation
 Abs(Z) is a zone if Z is a zone
- finiteness of the abstraction {Abs(Z) | Z zone} is finite
- completeness of the abstraction
 Z ⊆ Abs(Z)

• soundness of the abstraction the computation of $(Abs \circ Post)^*$ is correct w.r.t. reachability

[Effectiveness]

[Termination]

[Completeness]

[Soundness]

Criteria for a good abstraction operator Abs :

- easy computation
 Abs(Z) is a zone if Z is a zone
- finiteness of the abstraction {Abs(Z) | Z zone} is finite
- completeness of the abstraction $Z \subseteq Abs(Z)$

• soundness of the abstraction [S the computation of (Abs o Post)* is correct w.r.t. reachability

For the previous automaton,

no abstraction operator can satisfy all these criteria!

[Effectiveness]

[Termination]

[Completeness]

[Soundness]

Why that?

Assume there is a "nice" operator Abs.

The set {*M* DBM representing a zone Abs(Z)} is finite.

 $\sim k$ the max. constant defining one of the previous DBMs

We get that, for every zone Z,

 $Z \subseteq \operatorname{Extra}_k(Z) \subseteq \operatorname{Abs}(Z)$

Timed automata in practice

Open questions:	 which conditions can be made weaker? find a clever termination criterium?
	- use an other data structure than $zones/DBMs?$

- the extrapolation operator can be made coarser:
 - local extrapolation constants [BBFL03];
 - distinguish between lower- and upper-bounded contraints

[BBLP03,BBLP06]

[BBFL03] Behrmann, Bouyer, Fleury, Larsen. Static Guard Analysis in Timed Automata Verification (TACAS'03).

- [BBLP04] Behrmann, Bouyer, Larsen, Pelánek. Lower and Upper Bounds in Zone Based Abstractions of Timed Automata (TACAS'04).
- BBLP06 Behrmann, Bouyer, Larsen, Pelánek. Lower and Upper Bounds in Zone-Based Abstractions of Timed Automata (International Journal on Software Tools for Technology Transfer).
- [HBL+03] Hendriks, Behrmann, Larsen, Niebert, Vaandrager. Adding symmetry reduction to Uppaal (FORMATS'03).
- [DHLP06] David, Håkansson, Larsen, Pettersson. Model checking timed automata with priorities using DBM subtraction (FORMATS'06).

- the extrapolation operator can be made coarser:
 - local extrapolation constants [BBFL03];
 - distinguish between lower- and upper-bounded contraints

[BBLP03,BBLP06]

- heuristics can be added
 - order for exploration
 - symmetry reduction [HBL+03]

[BBFL03] Behrmann, Bouyer, Fleury, Larsen. Static Guard Analysis in Timed Automata Verification (TACAS'03).

- [BBLP04] Behrmann, Bouyer, Larsen, Pelánek. Lower and Upper Bounds in Zone Based Abstractions of Timed Automata (TACAS'04).
- [BBLP06] Behrmann, Bouyer, Larsen, Pelánek. Lower and Upper Bounds in Zone-Based Abstractions of Timed Automata (International Journal on Software Tools for Technology Transfer).
- [HBL+03] Hendriks, Behrmann, Larsen, Niebert, Vaandrager. Adding symmetry reduction to Uppaal (FORMATS'03).
- [DHLP06] David, Håkansson, Larsen, Pettersson. Model checking timed automata with priorities using DBM subtraction (FORMATS'06).

- the extrapolation operator can be made coarser:
 - local extrapolation constants [BBFL03];
 - distinguish between lower- and upper-bounded contraints

[BBLP03,BBLP06]

- heuristics can be added
 - order for exploration
 - symmetry reduction [HBL+03]
- the representation of zones can be improved [DHLP06]

[BBFL03] Behrmann, Bouyer, Fleury, Larsen. Static Guard Analysis in Timed Automata Verification (TACAS'03).

- [BBLP04] Behrmann, Bouyer, Larsen, Pelánek. Lower and Upper Bounds in Zone Based Abstractions of Timed Automata (TACAS'04).
- [BBLP06] Behrmann, Bouyer, Larsen, Pelánek. Lower and Upper Bounds in Zone-Based Abstractions of Timed Automata (International Journal on Software Tools for Technology Transfer).

[HBL+03] Hendriks, Behrmann, Larsen, Niebert, Vaandrager. Adding symmetry reduction to Uppaal (FORMATS'03).

[DHLP06] David, Håkansson, Larsen, Pettersson. Model checking timed automata with priorities using DBM subtraction (FORMATS'06).

- the extrapolation operator can be made coarser:
 - local extrapolation constants [BBFL03];
 - distinguish between lower- and upper-bounded contraints

[BBLP03,BBLP06]

- heuristics can be added
 - order for exploration
 - symmetry reduction [HBL+03]
- the representation of zones can be improved [DHLP06]

 \rightsquigarrow the tool Uppaal is under development since 1995...

- [BBLP04] Behrmann, Bouyer, Larsen, Pelánek. Lower and Upper Bounds in Zone Based Abstractions of Timed Automata (TACAS'04).
- [BBLP06] Behrmann, Bouyer, Larsen, Pelánek. Lower and Upper Bounds in Zone-Based Abstractions of Timed Automata (International Journal on Software Tools for Technology Transfer).
- [HBL+03] Hendriks, Behrmann, Larsen, Niebert, Vaandrager. Adding symmetry reduction to Uppaal (FORMATS'03).
- [DHLP06] David, Håkansson, Larsen, Pettersson. Model checking timed automata with priorities using DBM subtraction (FORMATS'06).

[[]BBFL03] Behrmann, Bouyer, Fleury, Larsen. Static Guard Analysis in Timed Automata Verification (TACAS'03).

Outline

- 1. Introduction
- 2. The timed automaton model
- 3. Timed automata, decidability issues
- 4. How far can we extend the model and preserve decidability? Hybrid systems Smaller extensions of timed automata An alternative way of proving decidability
- 5. Timed automata in practice
- 6. Conclusion
Conclusion

- Justification of the dense-time paradigm
- Several technics for proving decidability of real-time systems
 - finite time-abstract bisimulation
 - well-quasi-order on the time-abstract transition system
- Timed automata are implemented in several model checking tools
 - Other timed models have been developed and have concurrent tools: for instance Romeo and Tina for time Petri nets

Conclusion

- Justification of the dense-time paradigm
- Several technics for proving decidability of real-time systems
 - finite time-abstract bisimulation
 - well-quasi-order on the time-abstract transition system
- Timed automata are implemented in several model checking tools
 - Other timed models have been developed and have concurrent tools: for instance Romeo and Tina for time Petri nets

Some current streams of research in timed systems:

- quantitative model-checking,
- real-time logics,
- robustness, implementability issues,
- timed games,
- modelling of resources,

• ...

Conclusion

- Justification of the dense-time paradigm
- Several technics for proving decidability of real-time systems
 - finite time-abstract bisimulation
 - well-quasi-order on the time-abstract transition system
- Timed automata are implemented in several model checking tools
 - Other timed models have been developed and have concurrent tools: for instance Romeo and Tina for time Petri nets

Some current streams of research in timed systems:

- quantitative model-checking,
- real-time logics,
- robustness, implementability issues,
- timed games,
- modelling of resources,

• ...

Modelling and analyzing resources in timed systems

Patricia Bouyer-Decitre

LSV, CNRS & ENS Cachan, France

Outline

1. Introduction

- 2. Modelling and optimizing resources in timed systems
- 3. Managing resources
- 4. Conclusion

A starting example



Natural questions

• Can I reach Pontivy from Oxford?

- What is the minimal time to reach Pontivy from Oxford?
- What is the minimal fuel consumption to reach Pontivy from Oxford?
- What if there is an unexpected event?
- Can I use my computer all the way?

A first model of the system



Can I reach Pontivy from Oxford?



This is a reachability question in a finite graph: Yes, I can!

A second model of the system



How long will that take?



It is a reachability (and optimization) question in a timed automaton: at least 350mn = 5h50mn!







- X 0
- y 0



	safe	$\xrightarrow{23}$	safe
х	0		23
у	0		23



	safe	$\xrightarrow{23}$	safe	$\xrightarrow{\text{problem}}$	alarm
х	0		23		0
у	0		23		23









failsafe

... 15.6

0



	safe	$\xrightarrow{23}$	safe	$\xrightarrow{\text{problem}}$	alarm	$\xrightarrow{15.6}$	alarm	$\xrightarrow{\text{delayed}}$	failsafe	
х	0		23		0		15.6		15.6	
у	0		23		23		38.6		0	

failsafe	$\xrightarrow{2.3}$	failsafe
 15.6		17.9
0		2.3



	safe	$\xrightarrow{23}$	safe	$\xrightarrow{\text{problem}}$	alarm	$\xrightarrow{15.6}$	alarm	$\xrightarrow{\text{delayed}}$	failsafe	
х	0		23		0		15.6		15.6	
у	0		23		23		38.6		0	

	failsafe	$\xrightarrow{2.3}$	failsafe	$\xrightarrow{\text{repair}}$	repairing
•••	15.6		17.9		17.9
	0		2.3		0







	safe	$\xrightarrow{23}$	safe	problem	^m → a	larm	$\xrightarrow{15.6}$	alarm	$\xrightarrow{\text{delayed}}$	failsafe	
х	0		23			0		15.6		15.6	
у	0		23			23		38.6		0	
								00.1			
	failsafe	2.3	≻ fa	ilsafe	repair	\rightarrow	repairing	$\xrightarrow{22.1}$	repairing	$\xrightarrow{\text{done}}$	safe
	15.6			17.9			17.9		40		40
	0			2.3			0		22.1		22.1

Timed automata

Theorem [AD90,CY92]

The (time-optimal) reachability problem is decidable (and PSPACE-complete) for timed automata.

[AD90] Alur, Dill. Automata for modeling real-time systems (ICALP'90). [CY92] Courcoubetis, Yannakakis. Minimum and maximum delay problems in real-time systems (Formal Methods in System Design).

Timed automata

Theorem [AD90,CY92]

The (time-optimal) reachability problem is decidable (and PSPACE-complete) for timed automata.







• "compatibility" between regions and constraints



- "compatibility" between regions and constraints
- "compatibility" between regions and time elapsing



- "compatibility" between regions and constraints
- "compatibility" between regions and time elapsing



- "compatibility" between regions and constraints
- "compatibility" between regions and time elapsing

→ an equivalence of finite index a time-abstract bisimulation



1. Introduction

- 2. Modelling and optimizing resources in timed systems
- 3. Managing resources
- 4. Conclusion

• System resources might be relevant and even crucial information

- System resources might be relevant and even crucial information
 - energy consumption,
 - memory usage,
 - price to pay,
 - bandwidth,
 - ...

- System resources might be relevant and even crucial information
 - energy consumption,
 - memory usage,
 - price to pay,
 - bandwidth,
 - ...

 \rightsquigarrow timed automata are not powerful enough!

- System resources might be relevant and even crucial information
 - energy consumption,
 - memory usage,
 - price to pay,
 - bandwidth,
 - ...

 \rightsquigarrow timed automata are not powerful enough!

• A possible solution: use hybrid automata

- System resources might be relevant and even crucial information
 - energy consumption,
 - memory usage,
 - price to pay,
 - bandwidth,
 - ...

 \rightsquigarrow timed automata are not powerful enough!

• A possible solution: use hybrid automata

The thermostat example



- System resources might be relevant and even crucial information
 - energy consumption,
 - memory usage,
 - price to pay,
 - bandwidth,
 - ...

 \rightsquigarrow timed automata are not powerful enough!

• A possible solution: use hybrid automata

The thermostat example $T \leq 19$ 22 Off On 21 $\dot{T} = -0.5T$ $\dot{T} = 2.25 - 0.5T$ 19 $T \ge 18)$ (T < 22)18 $T \ge 21$ 2 8 10 time Δ
Modelling resources in timed systems

- System resources might be relevant and even crucial information
 - energy consumption,
 - memory usage,
 - price to pay,
 - bandwidth,
 - ...

 \rightsquigarrow timed automata are not powerful enough!

• A possible solution: use hybrid automata

Theorem [HKPV95]

The reachability problem is undecidable in hybrid automata.

Modelling resources in timed systems

- System resources might be relevant and even crucial information
 - energy consumption,
 - memory usage,
 - price to pay,
 - bandwidth,
 - ...
- \rightsquigarrow timed automata are not powerful enough!
- A possible solution: use hybrid automata

Theorem [HKPV95]

The reachability problem is undecidable in hybrid automata.

 An alternative: weighted/priced timed automata [ALP01,BFH+01]

 hybrid variables do not constrain the system hybrid variables are observer variables

A third model of the system



How much fuel will I use?



It is a <u>quantitative</u> (optimization) problem in a priced timed automaton: at least 68 anti-planet units!













[ALP01] Alur, La Torre, Pappas. Optimal paths in weighted timed automata (HSCC'01).



[ALP01] Alur, La Torre, Pappas. Optimal paths in weighted timed automata (HSCC'01).



[ALP01] Alur, La Torre, Pappas. Optimal paths in weighted timed automata (HSCC'01).



[ALP01] Alur, La Torre, Pappas. Optimal paths in weighted timed automata (HSCC'01).



Question: what is the optimal cost for reaching \bigcirc ?



Question: what is the optimal cost for reaching \bigcirc ?

5t + 10(2 - t) + 1



Question: what is the optimal cost for reaching \bigcirc ?

5t + 10(2 - t) + 1, 5t + (2 - t) + 7



Question: what is the optimal cost for reaching \bigcirc ?

min (5t + 10(2 - t) + 1, 5t + (2 - t) + 7)



Question: what is the optimal cost for reaching \bigcirc ?

$$\inf_{0 \le t \le 2} \min \left(5t + 10(2-t) + 1, 5t + (2-t) + 7 \right) = 9$$



Question: what is the optimal cost for reaching \bigcirc ?

$$\inf_{0 \le t \le 2} \min \left(5t + 10(2-t) + 1, 5t + (2-t) + 7 \right) = 9$$

 \sim strategy: leave immediately ℓ_0 , go to ℓ_3 , and wait there 2 t.u.

[ALP01] Alur, La Torre, Pappas. Optimal paths in weighted timed automata (HSCC'01).

The region abstraction is not fine enough



The corner-point abstraction



The corner-point abstraction



We can somehow discretize the behaviours...

$$\circ \xrightarrow{t_1} \circ \xrightarrow{t_2} \circ \xrightarrow{t_3} \circ \xrightarrow{t_4} \circ \xrightarrow{t_5} \circ \cdots$$

$$\circ \xrightarrow{t_1} \circ \xrightarrow{t_2} \circ \xrightarrow{t_3} \circ \xrightarrow{t_4} \circ \xrightarrow{t_5} \circ \cdots \qquad \begin{cases} t_1 + t_2 \le 2 \\ \end{array}$$

$$\circ \xrightarrow{t_1} \circ \xrightarrow{t_2} \circ \xrightarrow{t_3} \circ \xrightarrow{t_4} \circ \xrightarrow{t_5} \circ \cdots \qquad \begin{cases} t_1 + t_2 \le 2 \\ t_2 + t_3 + t_4 \ge 5 \end{cases}$$

Optimal reachability as a linear programming problem

$$\circ \xrightarrow{t_1} \circ \xrightarrow{t_2} \circ \xrightarrow{t_3} \circ \xrightarrow{t_4} \circ \xrightarrow{t_5} \circ \xrightarrow{t_5} \circ \cdots \qquad \begin{cases} t_1 + t_2 \le 2 \\ t_2 + t_3 + t_4 \ge 5 \end{cases}$$

Lemma

Let Z be a bounded zone and f be a function

$$f:(t_1,...,t_n)\mapsto \sum_{i=1}^n c_it_i+c$$

well-defined on \overline{Z} . Then $inf_Z f$ is obtained on the border of \overline{Z} with integer coordinates.

Optimal reachability as a linear programming problem

$$\circ \xrightarrow{t_1} \circ \xrightarrow{t_2} \circ \xrightarrow{t_3} \circ \xrightarrow{t_4} \circ \xrightarrow{t_5} \circ \xrightarrow{t_5} \circ \cdots \qquad \begin{cases} t_1 + t_2 \leq 2 \\ t_2 + t_3 + t_4 \geq 5 \end{cases}$$

Lemma

Let Z be a bounded zone and f be a function

$$f:(t_1,...,t_n)\mapsto \sum_{i=1}^n c_it_i+c$$

well-defined on \overline{Z} . Then $inf_Z f$ is obtained on the border of \overline{Z} with integer coordinates.

 \rightsquigarrow for every finite path π in $\mathcal A,$ there exists a path Π in $\mathcal A_{\sf cp}$ such that

 $cost(\Pi) \leq cost(\pi)$

[Π is a "corner-point projection" of π]

Approximation of abstract paths:



For any path Π of $\mathcal{A}_{\mathsf{cp}}$,

Approximation of abstract paths:



For any path Π of $\mathcal{A}_{\sf cp}$, for any $\varepsilon > 0,$

Approximation of abstract paths:



For any path Π of A_{cp} , for any $\varepsilon > 0$, there exists a path π_{ε} of A s.t.

 $\|\Pi - \pi_{\varepsilon}\|_{\infty} < \varepsilon$

Approximation of abstract paths:



For any path Π of A_{cp} , for any $\varepsilon > 0$, there exists a path π_{ε} of A s.t.

 $\|\Pi - \pi_{\varepsilon}\|_{\infty} < \varepsilon$

For every $\eta > 0$, there exists $\varepsilon > 0$ s.t.

$$\|\Pi - \pi_{\varepsilon}\|_{\infty} < \varepsilon \Rightarrow |\mathsf{cost}(\Pi) - \mathsf{cost}(\pi_{\varepsilon})| < \eta$$

Optimal-cost reachability

Theorem [ALP01,BFH+01,BBBR07]

The optimal-cost reachability problem is decidable (and PSPACE-complete) in (priced) timed automata.

[ALP01] Alur, La Torre, Pappas. Optimal paths in weighted timed automata (HSCC'01). [BFH+01] Behrmann, Fehnker, Hune, Larsen, Pettersson, Romijn, Vaandrager. Minimum-cost reachability in priced timed automata (HSCC'01). [BBBR07] Bouyer, Brihaye, Bruyère, Raskin. On the optimal reachability problem (Formal Methods in System Design).

Going further 1: mean-cost optimization



[BBL08] Bouyer, Brinksma, Larsen. Optimal infinite scheduling for multi-priced timed automata (Formal Methods in System Designs).

Going further 1: mean-cost optimization



 \rightsquigarrow compute optimal infinite schedules that minimize

$$\operatorname{mean-cost}(\pi) = \limsup_{n \to +\infty} \frac{\operatorname{cost}(\pi_n)}{\operatorname{reward}(\pi_n)}$$

[BBL08] Bouyer, Brinksma, Larsen. Optimal infinite scheduling for multi-priced timed automata (Formal Methods in System Designs).

Going further 1: mean-cost optimization



 \rightsquigarrow compute optimal infinite schedules that minimize



[BBL08] Bouyer, Brinksma, Larsen. Optimal infinite scheduling for multi-priced timed automata (Formal Methods in System Designs).
Going further 1: mean-cost optimization



 \rightsquigarrow compute optimal infinite schedules that minimize

$$\mathsf{mean-cost}(\pi) = \limsup_{n \to +\infty} \frac{\mathsf{cost}(\pi_n)}{\mathsf{reward}(\pi_n)}$$

Theorem [BBL08]

The mean-cost optimization problem is decidable (and PSPACE-complete) for priced timed automata.

 \rightsquigarrow the corner-point abstraction can be used

[BBL08] Bouyer, Brinksma, Larsen. Optimal infinite scheduling for multi-priced timed automata (Formal Methods in System Designs).

• Finite behaviours: based on the following property

Lemma

Let Z be a bounded zone and f be a function

$$f:(t_1,...,t_n)\mapsto \frac{\sum_{i=1}^n c_i t_i + c}{\sum_{i=1}^n r_i t_i + r}$$

well-defined on \overline{Z} . Then $inf_Z f$ is obtained on the border of \overline{Z} with integer coordinates.

• Finite behaviours: based on the following property

Lemma

Let Z be a bounded zone and f be a function

$$f:(t_1,...,t_n)\mapsto \frac{\sum_{i=1}^n c_i t_i + c}{\sum_{i=1}^n r_i t_i + r}$$

well-defined on \overline{Z} . Then $inf_Z f$ is obtained on the border of \overline{Z} with integer coordinates.

 \sim for every finite path π in A, there exists a path Π in A_{cp} s.t. mean-cost(Π) ≤ mean-cost(π)

• Finite behaviours: based on the following property

Lemma

Let Z be a bounded zone and f be a function

$$f:(t_1,...,t_n)\mapsto \frac{\sum_{i=1}^n c_i t_i + c}{\sum_{i=1}^n r_i t_i + r}$$

well-defined on \overline{Z} . Then $inf_Z f$ is obtained on the border of \overline{Z} with integer coordinates.

 \sim for every finite path π in \mathcal{A} , there exists a path Π in \mathcal{A}_{cp} s.t. mean-cost(Π) \leq mean-cost(π)

• Infinite behaviours: decompose each sufficiently long projection into cycles:

The (acyclic) linear part will be negligible!

• Finite behaviours: based on the following property

Lemma

Let Z be a bounded zone and f be a function

$$f:(t_1,...,t_n)\mapsto \frac{\sum_{i=1}^n c_i t_i + c}{\sum_{i=1}^n r_i t_i + r}$$

well-defined on \overline{Z} . Then $inf_Z f$ is obtained on the border of \overline{Z} with integer coordinates.

 \sim for every finite path π in \mathcal{A} , there exists a path Π in \mathcal{A}_{cp} s.t. mean-cost(Π) \leq mean-cost(π)

• Infinite behaviours: decompose each sufficiently long projection into cycles:

The (acyclic) linear part will be negligible!

 \rightsquigarrow the optimal cycle of $\mathcal{A}_{\mathsf{cp}}$ is better than any infinite path of $\mathcal{A}!$

Approximation of abstract paths:



For any path Π of $\mathcal{A}_{\mathsf{cp}}$,

Approximation of abstract paths:



For any path Π of $\mathcal{A}_{\mathsf{cp}}$, for any $\varepsilon > \mathsf{0},$

Approximation of abstract paths:



For any path Π of \mathcal{A}_{cp} , for any $\varepsilon > 0$, there exists a path π_{ε} of \mathcal{A} s.t.

 $\|\Pi - \pi_{\varepsilon}\|_{\infty} < \varepsilon$

Approximation of abstract paths:



For any path Π of \mathcal{A}_{cp} , for any $\varepsilon > 0$, there exists a path π_{ε} of \mathcal{A} s.t.

 $\|\Pi - \pi_{\varepsilon}\|_{\infty} < \varepsilon$

For every $\eta > 0$, there exists $\varepsilon > 0$ s.t.

$$\|\Pi - \pi_{\varepsilon}\|_{\infty} < \varepsilon \Rightarrow |\mathsf{mean-cost}(\Pi) - \mathsf{mean-cost}(\pi_{\varepsilon})| < \eta$$

Going further 2: concavely-priced cost functions

 \rightsquigarrow A general abstract framework for quantitative timed systems

Theorem [JT08]

Optimal cost in concavely-priced timed automata is computable, if we restrict to quasi-concave price functions. For the following cost functions, the (decision) problem is even PSPACE-complete:

- optimal-time and optimal-cost reachability;
- optimal discrete discounted cost;
- optimal average-time and average-cost;
- optimal mean-cost.

 \rightsquigarrow a slight extension of corner-point abstraction can be used





 \rightsquigarrow compute optimal infinite schedules that minimize discounted cost over time

[FL08] Fahrenberg, Larsen. Discount-optimal infinite runs in priced timed automata (INFINITY'08).



 \rightsquigarrow compute optimal infinite schedules that minimize

discounted-cost_{$$\lambda$$}(π) = $\sum_{n\geq 0} \lambda^{T_n} \int_{t=0}^{t_{n+1}} \lambda^t \operatorname{cost}(\ell_n) dt + \lambda^{T_{n+1}} \operatorname{cost}(\ell_n \xrightarrow{a_{n+1}} \ell_{n+1})$

if
$$\pi = (\ell_0, \nu_0) \xrightarrow{\tau_1, a_1} (\ell_1, \nu_1) \xrightarrow{\tau_2, a_2} \cdots$$
 and $T_n = \sum_{i \le n} \tau_i$

[FL08] Fahrenberg, Larsen. Discount-optimal infinite runs in priced timed automata (INFINITY'08).



 \rightsquigarrow compute optimal infinite schedules that minimize discounted cost over time

[FL08] Fahrenberg, Larsen. Discount-optimal infinite runs in priced timed automata (INFINITY'08).



 \rightsquigarrow compute optimal infinite schedules that minimize discounted cost over time



if $\lambda = e^{-1}$, the discounted cost of that infinite schedule is ≈ 2.16

[FL08] Fahrenberg, Larsen. Discount-optimal infinite runs in priced timed automata (INFINITY'08).



 \sim compute optimal infinite schedules that minimize discounted cost over time

Theorem [FL08]

The optimal discounted cost is computable in EXPTIME in priced timed automata.

 \rightsquigarrow the corner-point abstraction can be used

A fourth model of the system What if there is an unexpected event?



A fourth model of the system What if there is an unexpected event?



A fourth model of the system What if there is an unexpected event?



 \rightsquigarrow modelled as timed games

A simple example of timed game



A simple example of timed game



Another example



Theorem [AMPS98,HK99]

Safety and reachability control in timed automata are decidable and EXPTIME-complete.

Theorem [AMPS98,HK99]

Safety and reachability control in timed automata are decidable and EXPTIME-complete.

(the attractor is computable...)

Theorem [AMPS98,HK99]

Safety and reachability control in timed automata are decidable and EXPTIME-complete.

(the attractor is computable...)

 \sim classical regions are sufficient for solving such problems

Theorem [AMPS98,HK99]

Safety and reachability control in timed automata are decidable and EXPTIME-complete.

(the attractor is computable...)

 \rightsquigarrow classical regions are sufficient for solving such problems

Theorem [AM99,BHPR07,JT07]

Optimal-time reachability timed games are decidable and EXPTIME-complete.

[AM99] Asarin, Maler. As soon as possible: time optimal control for timed automata (*HSCC'99*). [BHPR07] Brihaye, Henzinger, Prabhu, Raskin. Minimum-time reachability in timed games (*ICALP'07*). [JT07] Jurdzinński, Trivedi. Reachability-time games on timed automata (*ICALP'07*).

Theorem [AMPS98,HK99]

Safety and reachability control in timed automata are decidable and EXPTIME-complete.

(the attractor is computable...)

 \rightsquigarrow classical regions are sufficient for solving such problems

Theorem [AM99,BHPR07,JT07]

Optimal-time reachability timed games are decidable and EXPTIME-complete.

\sim let's play with Uppaal Tiga! [BCD+07]







Question: what is the optimal cost we can ensure while reaching \bigcirc ?



Question: what is the optimal cost we can ensure while reaching \bigcirc ?

5t + 10(2 - t) + 1



Question: what is the optimal cost we can ensure while reaching \bigcirc ?

5t + 10(2 - t) + 1, 5t + (2 - t) + 7



Question: what is the optimal cost we can ensure while reaching \bigcirc ?

max (5t + 10(2 - t) + 1 , 5t + (2 - t) + 7)



Question: what is the optimal cost we can ensure while reaching \bigcirc ?

$$\inf_{0 \le t \le 2} \max \left(5t + 10(2-t) + 1 , 5t + (2-t) + 7 \right) = 14 + \frac{1}{3}$$



Question: what is the optimal cost we can ensure while reaching \bigcirc ?

$$\inf_{0 \le t \le 2} \max \left(5t + 10(2-t) + 1, 5t + (2-t) + 7 \right) = 14 + \frac{1}{3}$$

\$\sim strategy: wait in \$\ell_0\$, and when \$t = \frac{4}{2}\$, go to \$\ell_1\$}

Optimal reachability in priced timed games

This topic has been fairly hot these last couple of years...

e.g. [LMM02,ABM04,BCFL04]

[LMM02] La Torre, Mukhopadhyay, Murano. Optimal-reachability and control for acyclic weighted timed automata (TCS002). [ABM04] Alur, Bernardsky, Madhusudan. Optimal reachability in weighted timed games (ICALP'04). [BCFL04] Bouyer, Cassez, Fleury, Larsen. Optimal strategies in priced timed game automata (FSTTCS'04).
Optimal reachability in priced timed games

This topic has been fairly hot these last couple of years...

e.g. [LMM02,ABM04,BCFL04]

Theorem [BBR05,BBM06]

Optimal timed games are undecidable, as soon as automata have three clocks or more.

Optimal reachability in priced timed games

This topic has been fairly hot these last couple of years...

e.g. [LMM02,ABM04,BCFL04]

Theorem [BBR05,BBM06]

Optimal timed games are undecidable, as soon as automata have three clocks or more.

Theorem [BLMR06]

Turn-based optimal timed games are decidable in 3EXPTIME when automata have a single clock. They are PTIME-hard.

[BBR05] Brihaye, Bruyère, Raskin. On optimal timed strategies (FORMATS'05).
[BBM06] Bouyer, Brihaye, Markey. Improved undecidability results on weighted timed automata (Information Processing Letters).
[BLMR06] Bouyer, Larsen, Markey, Rasmussen. Almost-optimal strategies in one-clock priced timed automata (FSTTCS'06).

Theorem [BLMR06]

Turn-based optimal timed games are decidable in 3EXPTIME when automata have a single clock. They are PTIME-hard.

• Key: resetting the clock somehow resets the history...

Theorem [BLMR06]

Turn-based optimal timed games are decidable in 3EXPTIME when automata have a single clock. They are PTIME-hard.

- Key: resetting the clock somehow resets the history...
- Memoryless strategies can be non-optimal...



Theorem [BLMR06]

Turn-based optimal timed games are decidable in 3EXPTIME when automata have a single clock. They are PTIME-hard.

- Key: resetting the clock somehow resets the history...
- Memoryless strategies can be non-optimal...



• However, by unfolding and removing one by one the locations, we can synthesize memoryless almost-optimal winning strategies.

Theorem [BLMR06]

Turn-based optimal timed games are decidable in 3EXPTIME when automata have a single clock. They are PTIME-hard.

- Key: resetting the clock somehow resets the history...
- Memoryless strategies can be non-optimal...



- However, by unfolding and removing one by one the locations, we can synthesize memoryless almost-optimal winning strategies.
- Rather involved proof of correctness for a simple algorithm.







• In
$$\bigcirc$$
, cost = $2x_0 + (1 - y_0) + 2$



Given two clocks x and y, we can check whether y = 2x.



• In
$$\textcircled{\begin{subarray}{c} \mbox{...}}$$
, cost = $2x_0 + (1 - y_0) + 2$
In $\textcircled{\begin{subarray}{c} \mbox{...}}$, cost = $2(1 - x_0) + y_0 + 1$

• if $y_0 < 2x_0$, player 2 chooses the first branch: cost > 3

Given two clocks x and y, we can check whether y = 2x.



• In
$$\textcircled{i}$$
, cost = $2x_0 + (1 - y_0) + 2$
In \textcircled{i} , cost = $2(1 - x_0) + y_0 + 1$

• if $y_0 < 2x_0$, player 2 chooses the first branch: cost > 3 if $y_0 > 2x_0$, player 2 chooses the second branch: cost > 3

Given two clocks x and y, we can check whether y = 2x.



• In
$$\textcircled{\begin{subarray}{c} \mbox{.}}$$
, $\mbox{cost} = 2x_0 + (1 - y_0) + 2$
In $\textcircled{\begin{subarray}{c} \mbox{.}}$, $\mbox{cost} = 2(1 - x_0) + y_0 + 1$

• if $y_0 < 2x_0$, player 2 chooses the first branch: cost > 3 if $y_0 > 2x_0$, player 2 chooses the second branch: cost > 3 if $y_0 = 2x_0$, in both branches, cost = 3

Given two clocks x and y, we can check whether y = 2x.



• In
$$\textcircled{i}$$
, cost = $2x_0 + (1 - y_0) + 2$
In \textcircled{i} , cost = $2(1 - x_0) + y_0 + 1$

• if $y_0 < 2x_0$, player 2 chooses the first branch: cost > 3 if $y_0 > 2x_0$, player 2 chooses the second branch: cost > 3 if $y_0 = 2x_0$, in both branches, cost = 3

• Player 1 has a winning strategy with cost ≤ 3 iff $y_0 = 2x_0$

Player 1 will simulate a two-counter machine:

- each instruction is encoded as a module;
- the values c_1 and c_2 of the counters are encoded by the values of two clocks:

$$x = \frac{1}{2^{c_1}}$$
 and $y = \frac{1}{3^{c_2}}$

when entering the corresponding module.

Player 1 will simulate a two-counter machine:

- each instruction is encoded as a module;
- the values c_1 and c_2 of the counters are encoded by the values of two clocks:

$$x = \frac{1}{2^{c_1}}$$
 and $y = \frac{1}{3^{c_2}}$

when entering the corresponding module.

The two-counter machine has an halting computation iff player 1 has a winning strategy to ensure a cost no more than 3.

Player 1 will simulate a two-counter machine:

- each instruction is encoded as a module;
- the values c_1 and c_2 of the counters are encoded by the values of two clocks:

$$x = \frac{1}{2^{c_1}}$$
 and $y = \frac{1}{3^{c_2}}$

when entering the corresponding module.

The two-counter machine has an halting computation iff player 1 has a winning strategy to ensure a cost no more than 3.

Globally, $(x \le 1, y \le 1, u \le 1)$



Outline

1. Introduction

2. Modelling and optimizing resources in timed systems

- 3. Managing resources
- 4. Conclusion

A fifth model of the system



Can I work with my computer all the way?



Can I work with my computer all the way?



Can I work with my computer all the way?

Energy is not only consumed, but can be regained. \sim the aim is to continuously satisfy some energy constraints.









• Lower-bound problem: can we stay above 0?





• Lower-bound problem: can we stay above 0?





• Lower-bound problem: can we stay above 0?



• Lower-bound problem: can we stay above 0?

Globally $(x \le 1)$



• Lower-bound problem: can we stay above 0?



• Lower-bound problem: can we stay above 0?



- Lower-bound problem
- Lower-upper-bound problem: can we stay within bounds?



- Lower-bound problem
- Lower-upper-bound problem: can we stay within bounds?



- Lower-bound problem
- Lower-upper-bound problem: can we stay within bounds?



- Lower-bound problem
- Lower-upper-bound problem: can we stay within bounds?



- Lower-bound problem
- Lower-upper-bound problem: can we stay within bounds?



- Lower-bound problem
- Lower-upper-bound problem: can we stay within bounds?


- Lower-bound problem
- Lower-upper-bound problem: can we stay within bounds?



- Lower-bound problem
- Lower-upper-bound problem: can we stay within bounds?



- Lower-bound problem
- Lower-upper-bound problem: can we stay within bounds?



- Lower-bound problem
- Lower-upper-bound problem: can we stay within bounds?



- Lower-bound problem
- Lower-upper-bound problem: can we stay within bounds?



- Lower-bound problem
- Lower-upper-bound problem
- Lower-weak-upper-bound problem: can we "weakly" stay within bounds?

Globally $(x \le 1)$





L+W

- Lower-bound problem \rightsquigarrow L
- Lower-upper-bound problem \rightsquigarrow L+U
- Lower-weak-upper-bound problem ~~ \sim

36/45

Only partial results so far [BFLMS08]

0 clock!	exist. problem	univ. problem	games
L	∈ PTIME	∈ PTIME	$\in UP \cap co-UP$ $PTIME-hard$
L+W	∈ PTIME	∈ PTIME	$\in NP \cap co-NP$ $PTIME-hard$
L+U	$\in PSPACE$ NP-hard	∈ PTIME	EXPTIME-c.

[BFLMS08] Bouyer, Fahrenberg, Larsen, Markey, Srba. Infinite runs in weighted timed automata with energy constraints (FORMATS'08).

Only partial results so far [BFLMS08]

1 clock	exist. problem	univ. problem	games
L	∈ PTIME	∈ PTIME	?
L+W	∈ PTIME	∈ PTIME	?
L+U	?	?	undecidable

[BFLMS08] Bouyer, Fahrenberg, Larsen, Markey, Srba. Infinite runs in weighted timed automata with energy constraints (FORMATS'08).

Only partial results so far [BFLMS08]

n clocks	exist. problem	univ. problem	games
L	?	?	?
L+W	?	?	?
L+U	?	?	undecidable

[BFLMS08] Bouyer, Fahrenberg, Larsen, Markey, Srba. Infinite runs in weighted timed automata with energy constraints (FORMATS'08).

Definition

Mean-payoff games: in a weighted game graph, does there exists a strategy s.t. the mean-cost of any play is nonnegative?

Definition

Mean-payoff games: in a weighted game graph, does there exists a strategy s.t. the mean-cost of any play is nonnegative?

Lemma

 $\ensuremath{\mathsf{L}}\xspace$ and $\ensuremath{\mathsf{L}}\xspace+\ensuremath{\mathsf{W}}\xspace$ are determined, and memoryless strategies are sufficient to win.

Definition

Mean-payoff games: in a weighted game graph, does there exists a strategy s.t. the mean-cost of any play is nonnegative?

Lemma

 $\ensuremath{\mathsf{L}}\xspace$ and $\ensuremath{\mathsf{L}}\xspace+\ensuremath{\mathsf{W}}\xspace$ games and $\ensuremath{\mathsf{L}}\xspace+\ensuremath{\mathsf{W}}\xspace$ sufficient to win.

• from mean-payoff games to L-games or L+W-games: play in the same game graph G with initial credit $-M \ge 0$ (where M is the sum of negative costs in G).

Definition

Mean-payoff games: in a weighted game graph, does there exists a strategy s.t. the mean-cost of any play is nonnegative?

Lemma

 $\ensuremath{\mathsf{L}}\xspace$ and $\ensuremath{\mathsf{L}}\xspace+\ensuremath{\mathsf{W}}\xspace$ games and $\ensuremath{\mathsf{L}}\xspace+\ensuremath{\mathsf{W}}\xspace$ sufficient to win.

- from mean-payoff games to L-games or L+W-games: play in the same game graph G with initial credit $-M \ge 0$ (where M is the sum of negative costs in G).
- from L-games to mean-payoff games: transform the game as follows:



Theorem

The single-clock L+U-games are undecidable.

Theorem

The single-clock L+U-games are undecidable.

We encode the behaviour of a two-counter machine:

- each instruction is encoded as a module;
- the values c_1 and c_2 of the counters are encoded by the energy level

$$e = 5 - \frac{1}{2^{c_1} \cdot 3^{c_2}}$$

when entering the corresponding module.

Theorem

The single-clock L+U-games are undecidable.

We encode the behaviour of a two-counter machine:

- each instruction is encoded as a module;
- the values c_1 and c_2 of the counters are encoded by the energy level

$$e = 5 - \frac{1}{2^{c_1} \cdot 3^{c_2}}$$

when entering the corresponding module.

There is an infinite execution in the two-counter machine iff there is a strategy in the single-clock timed game under which the energy level remains between 0 and 5.

Theorem

The single-clock L+U-games are undecidable.

We encode the behaviour of a two-counter machine:

- each instruction is encoded as a module;
- the values c_1 and c_2 of the counters are encoded by the energy level

$$e = 5 - \frac{1}{2^{c_1} \cdot 3^{c_2}}$$

when entering the corresponding module.

There is an infinite execution in the two-counter machine iff there is a strategy in the single-clock timed game under which the energy level remains between 0 and 5.

 → We present a generic construction for incrementing/decrementing the counters.





























- $\alpha=3$: increment c_1
- $\alpha=2$: increment c_2
- $\alpha = 12$: decrement c_1
- $\alpha = 18$: decrement c_2

Outline

1. Introduction

2. Modelling and optimizing resources in timed systems

- 3. Managing resources
- 4. Conclusion

Some applications

Tools

- Uppaal (timed automata)
- Uppaal Cora (priced timed automata)
- Uppaal Tiga (timed games)

Case studies

- A lacquer production scheduling problem [BBHM05]
- Task graph scheduling problems [AKM03]
- An oil pump control problem [CJL+09]

Task graph scheduling problems

Compute $D \times (C \times (A+B)) + (A+B) + (C \times D)$ using two processors:



 P_2 (slow):







Task graph scheduling problems

Compute $D \times (C \times (A+B)) + (A+B) + (C \times D)$ using two processors:







C

 T_2

 T_A

 T_6

A B

Task graph scheduling problems

Compute $D \times (C \times (A+B)) + (A+B) + (C \times D)$ using two processors:



C

A B

Task graph scheduling problems

Compute $D \times (C \times (A+B)) + (A+B) + (C \times D)$ using two processors:



Processors







Tasks



Processors



Tasks



Modelling energy


Modelling the task graph scheduling problem

Processors



Modelling energy



Tasks



Modelling uncertainty



Conclusion

- Priced/weighted timed automata, a model for representing quantitative constraints on timed systems:
 - useful for modelling resources in timed systems
 - natural (optimization/management) questions have been posed...
 - ... and not all of them have been answered!

Conclusion

- Priced/weighted timed automata, a model for representing quantitative constraints on timed systems:
 - useful for modelling resources in timed systems
 - natural (optimization/management) questions have been posed...
 - ... and not all of them have been answered!

- Not mentioned here:
 - all works on model-checking issues (extensions of CTL, LTL)
 - models based on hybrid automata
 - weighted o-minimal hybrid games
 - weighted strong reset hybrid games
 - various tools have been developed:

Uppaal, Uppaal Cora, Uppaal Tiga

[BBC07] [BBJLR07]

Conclusion

- Priced/weighted timed automata, a model for representing quantitative constraints on timed systems:
 - useful for modelling resources in timed systems
 - natural (optimization/management) questions have been posed...
 - ... and not all of them have been answered!

- Not mentioned here:
 - all works on model-checking issues (extensions of CTL, LTL)
 - models based on hybrid automata
 - weighted o-minimal hybrid games
 - weighted strong reset hybrid games
 - various tools have been developed:

Uppaal, Uppaal Cora, Uppaal Tiga

- Current and further work:
 - further cost functions (e.g. exponential)
 - computation of approximate optimal values
 - further investigation of safe games + several cost variables?
 - discounted-time optimal games
 - Ink between discounted-time games and mean-cost games?
 - computation of equilibria

• ...

[BBC07] [BBJLR07]