

# Model-Checking Timed Temporal Logics

Patricia Bouyer

LSV – CNRS & ENS de Cachan – France

Based on joint works with Fabrice Chevalier, Nicolas Markey,  
Joël Ouaknine and James Worrell

# Outline

1. Introduction
2. Definition of the logics
3. The timed automaton model
4. The model-checking problem
5. Some interesting fragments
6. Conclusion

# Model-checking

system:

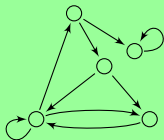


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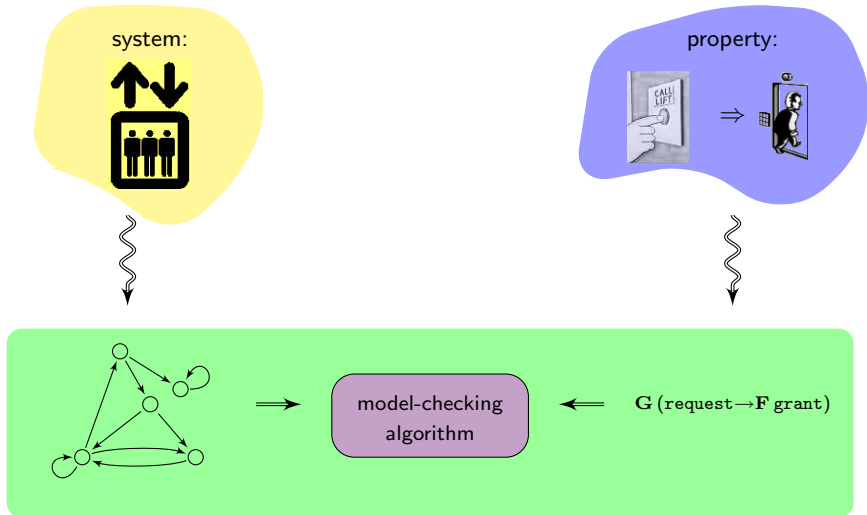


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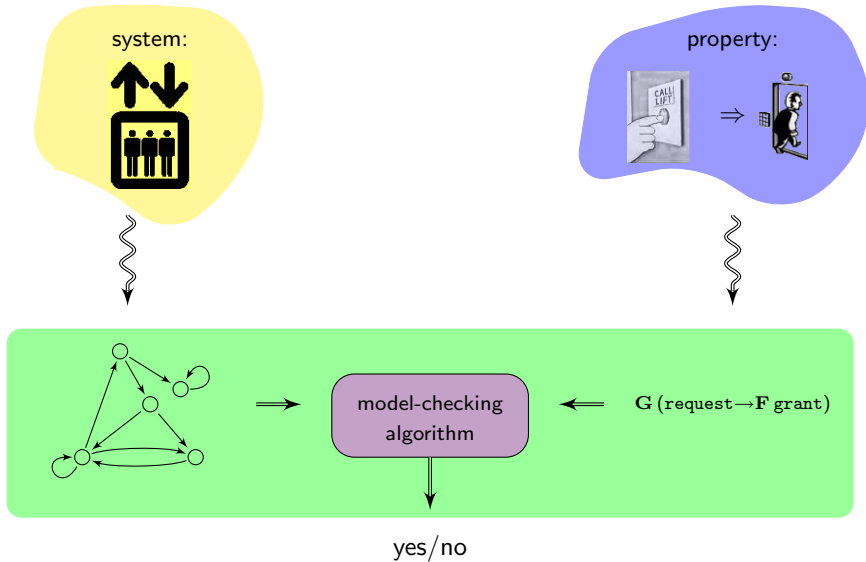


$G (\text{request} \rightarrow F \text{ grant})$

# Model-checking



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# The untimed (linear-time) framework

Linear-time temporal logic [Pnu77]

$\text{LTL} \ni \varphi ::= p \mid \varphi \wedge \varphi \mid \varphi \vee \varphi \mid \neg \varphi \mid \mathbf{X}\varphi \mid \varphi \mathbf{U}\varphi$

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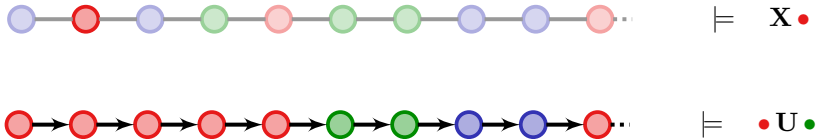
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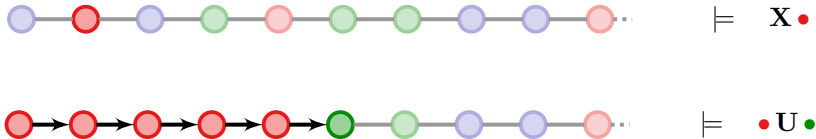
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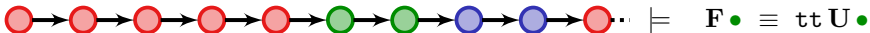
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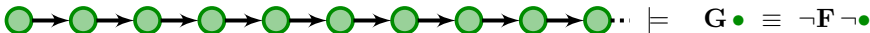
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- ▶ a more complex property:

$$(\bullet \wedge (\mathbf{F}\bullet \vee \mathbf{G}\bullet)) \mathbf{U}\bullet$$

# Adding timing requirements

- ▶ Need for **timed models**
  - ▶ the behaviour of most systems depends on time;
  - ▶ faithful modelling has to take time into account.
- ☞ **timed automata, time(d) Petri nets, timed process algebras...**

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- ▶ Need for **timed specification languages**
  - ▶ the behaviour of most systems depends on time;
  - ▶ untimed specifications are not sufficient  
(for instance, bounded response timed, etc...)

☞ **TCTL, MTL, TPTL, timed  $\mu$ -calculus...**

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# Metric Temporal Logic (MTL)

[Koy90]

$$\text{MTL } \exists \varphi ::= a \mid \neg \varphi \mid \varphi \vee \varphi \mid \varphi \wedge \varphi \mid \varphi \mathbf{U}_I \varphi$$

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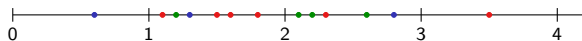
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- ▶ This is a timed extension of LTL
- ▶ Can be interpreted over **timed words**, or over **signals**
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- ▶ Can be interpreted over **finite** or **infinite** behaviours
  - ▶ this distinction is fundamental

# The pointwise semantics

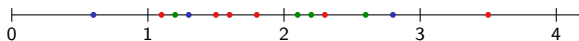
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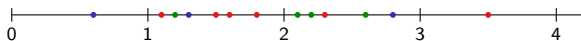


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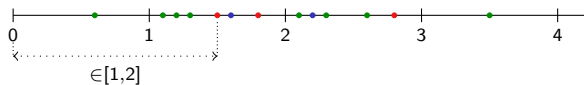
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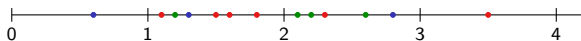
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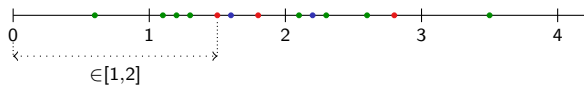
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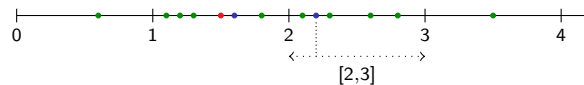


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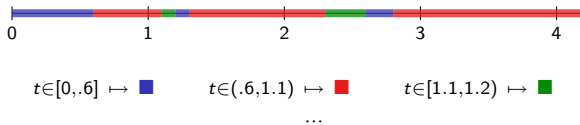
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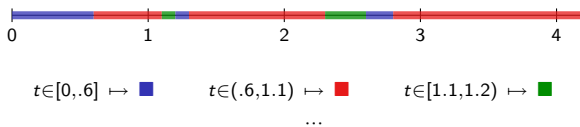
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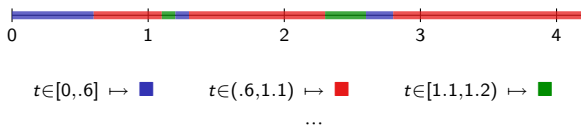
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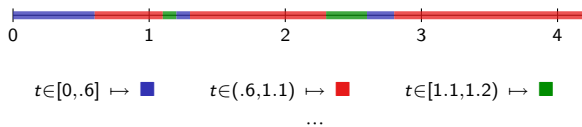
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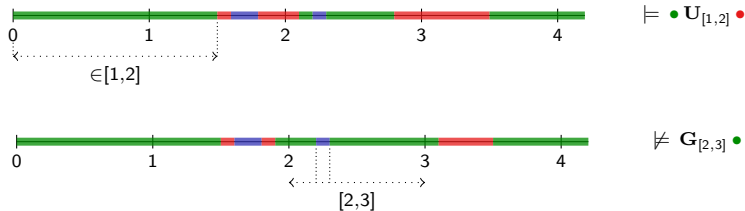


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## Some examples

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- ▶ “Each time there is a problem, it is either repaired within the next 15 time units, or an alarm rings during 3 time units 12 time units later”

$$G(\text{problem} \rightarrow (F_{\leq 15} \text{repair} \vee G_{[12,15)} \text{alarm}))$$

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LTL+Past is as expressive as LTL [Kam68,GPSS80].

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- ▶ This is wrong in the **continuous** semantics!

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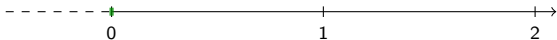
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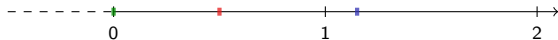


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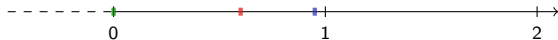
$$\mathbf{G} \bullet \rightarrow \left\{ \begin{array}{l} \mathbf{F}_{\leq 1} \bullet \wedge \mathbf{F}_{[1,2]} \bullet \end{array} \right.$$



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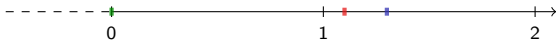


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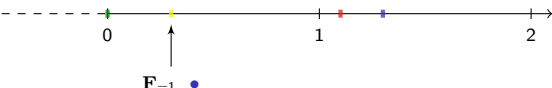


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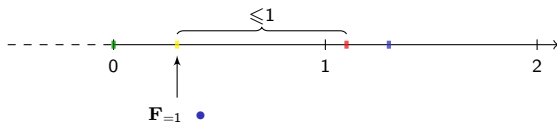
$\mathbf{F}_{=1}$  •

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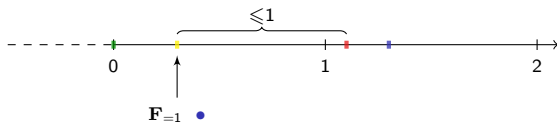


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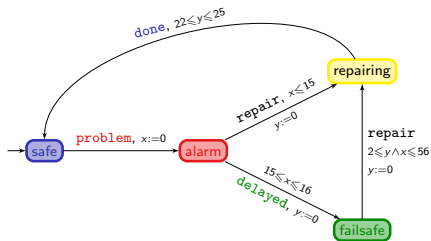


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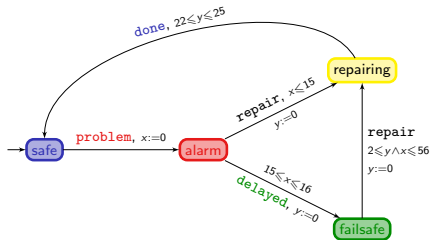
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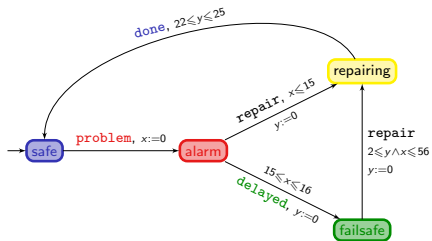
# Timed automata



safe  
 x 0  
 y 0

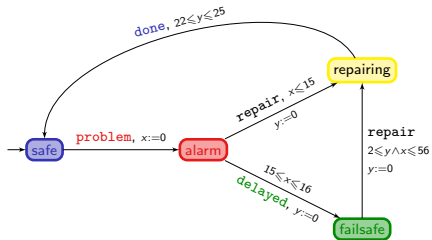


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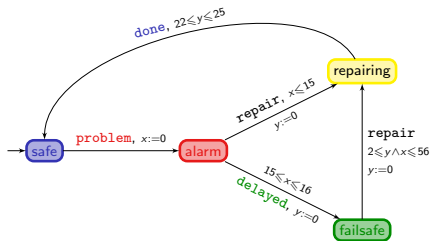
	<b>safe</b>	$\xrightarrow{23}$	<b>safe</b>
x	0		23
y	0		23

# Timed automata



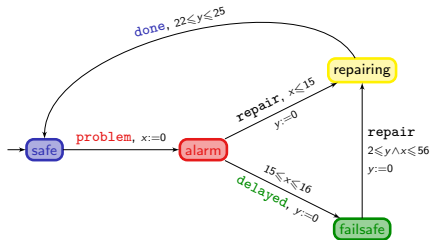
	safe	$\xrightarrow{23}$	safe	$\xrightarrow{\text{problem}}$	alarm
x	0		23		0
y	0		23		23

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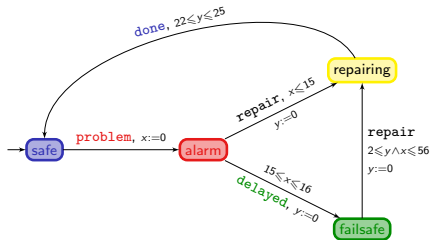
	safe	$\xrightarrow{23}$	safe	$\xrightarrow{\text{problem}}$	alarm	$\xrightarrow{15.6}$	alarm
x	0		23		0		15.6
y	0		23		23		38.6

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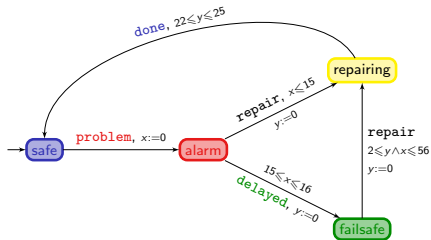
	safe	$\xrightarrow{23}$	safe	$\xrightarrow{\text{problem}}$	alarm	$\xrightarrow{15.6}$	alarm	$\xrightarrow{\text{delayed}}$	failsafe
x	0		23		0		15.6		15.6
y	0		23		23		38.6		0

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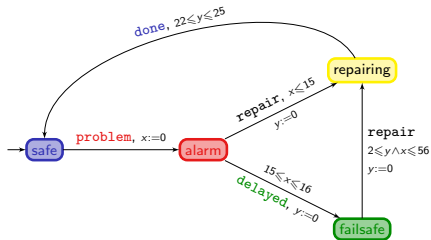
	safe	$\xrightarrow{23}$	safe	$\xrightarrow{\text{problem}}$	alarm	$\xrightarrow{15.6}$	alarm	$\xrightarrow{\text{delayed}}$	failsafe	$\xrightarrow{2.3}$	failsafe
x	0		23		0		15.6		15.6		17.9
y	0		23		23		38.6		0		2.3

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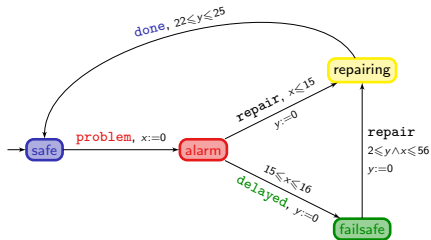
	safe	23	safe	problem	alarm	15.6	alarm	delayed	failsafe	2.3	failsafe	repair	reparation
x	0		23		0		15.6		15.6		17.9		17.9
y	0		23		23		38.6		0		2.3		0

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	safe	$\xrightarrow{23}$	safe	$\xrightarrow{\text{problem}}$	alarm	$\xrightarrow{15.6}$	alarm	$\xrightarrow{\text{delayed}}$	failsafe	$\xrightarrow{2.3}$	failsafe	$\xrightarrow{\text{repair}}$	reparation	$\xrightarrow{22.1}$	reparation
x	0		23		0		15.6		15.6		17.9		17.9		40
y	0		23		23		38.6		0		2.3		0		22.1

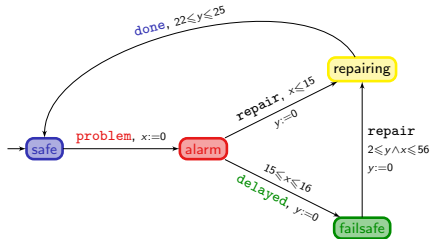
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x	0		23		0		15.6		15.6		17.9		17.9		40		40
y	0		23		23		38.6		0		2.3		0		22.1		22.1



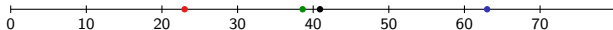
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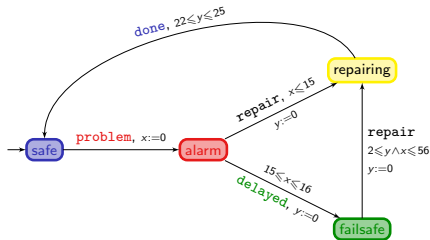
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x	0		23		0		15.6		15.6		17.9		17.9		40		40
y	0		23		23		38.6		0		2.3		0		22.1		22.1

Can be viewed:

- ▶ as the timed word  $(\text{problem}, 23)(\text{delayed}, 38.6)(\text{repair}, 40.9)(\text{done}, 63)$



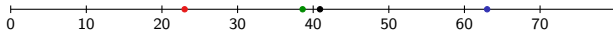
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- ▶ as the signal



# Basic result on timed automata

## Theorem

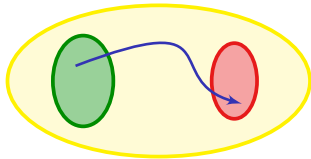
The reachability problem is decidable (and **PSPACE**-complete) for timed automata [AD94].

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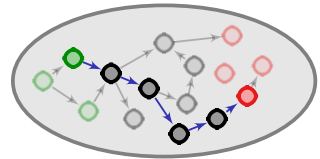
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timed automaton

finite bisimulation  
→



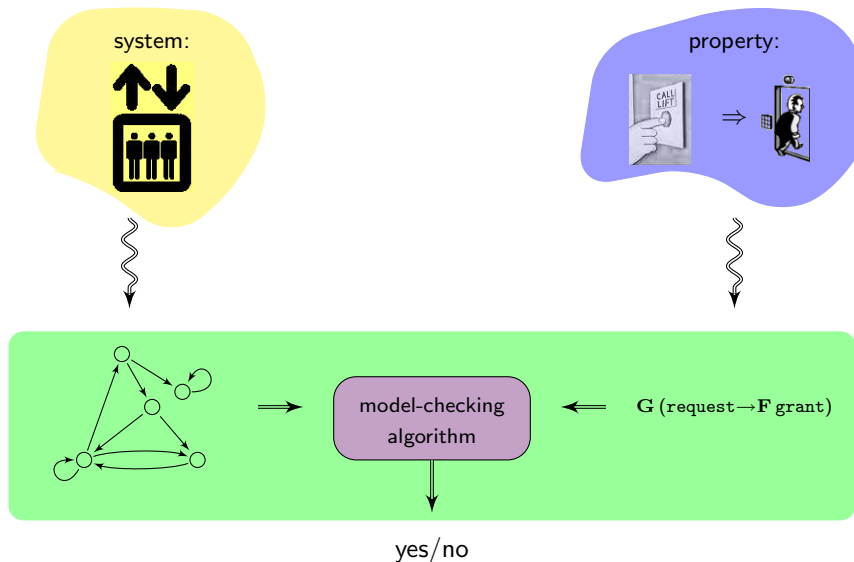
large (but finite) automaton  
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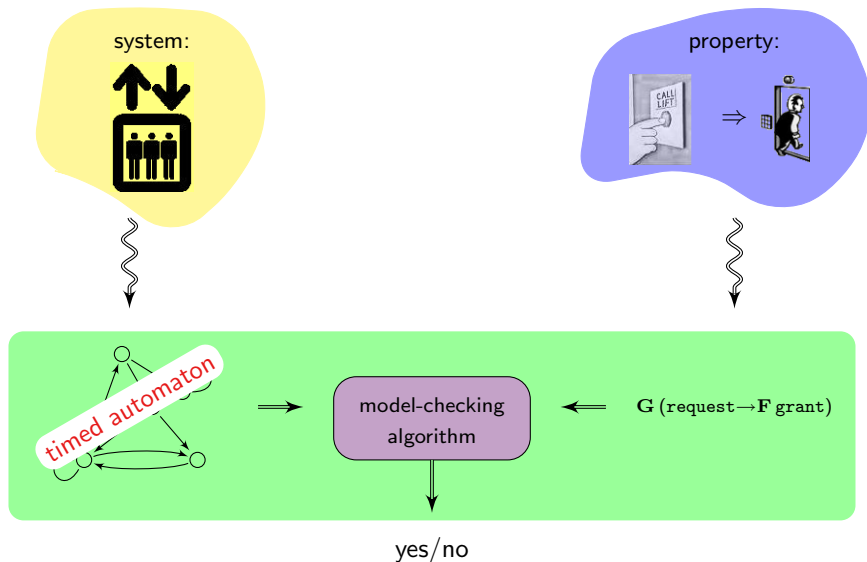
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3. The timed automaton model
- 4. The model-checking problem**
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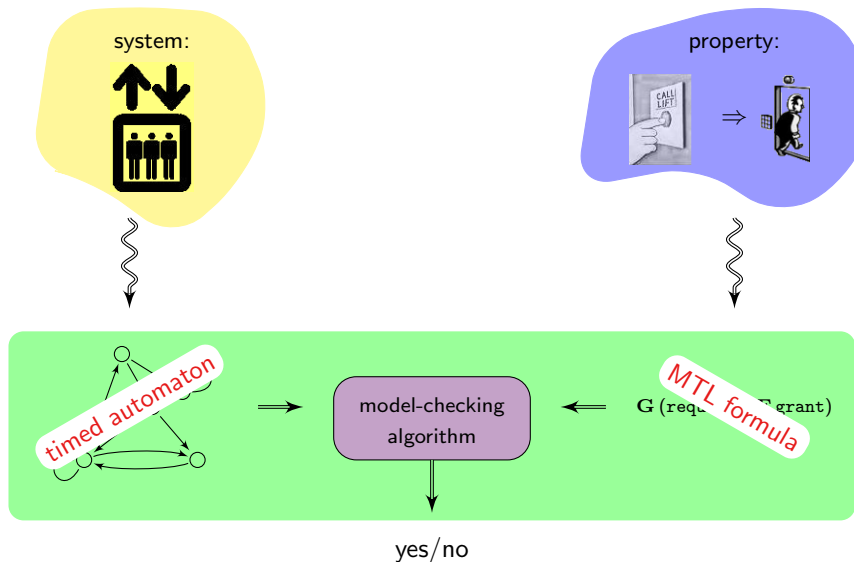
## Back to the model-checking problem



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# Results

## Theorem

Over finite runs, the model-checking problem is:

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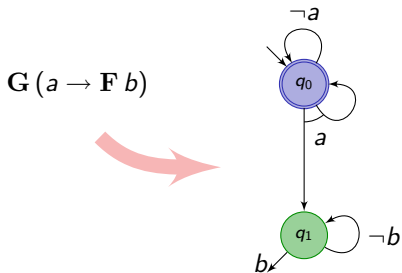
# From LTL to alternating automata

LTL formulas can be turned into linear alternating (Büchi) automata

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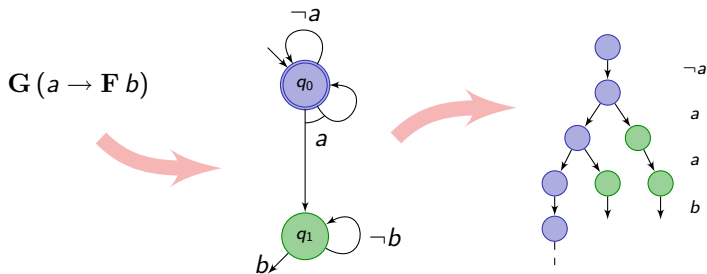
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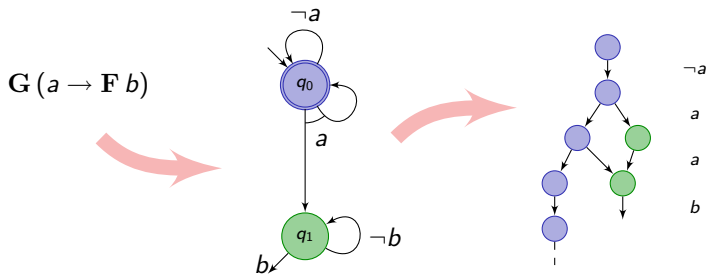
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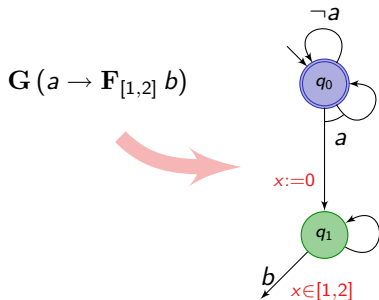
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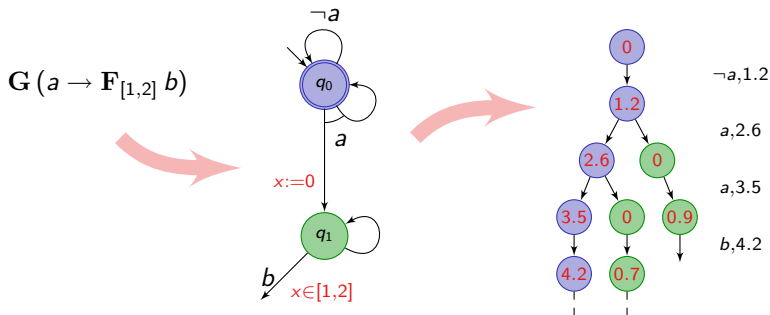
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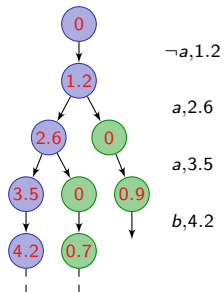


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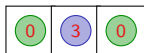
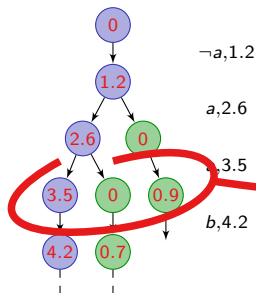
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# An abstract transition system

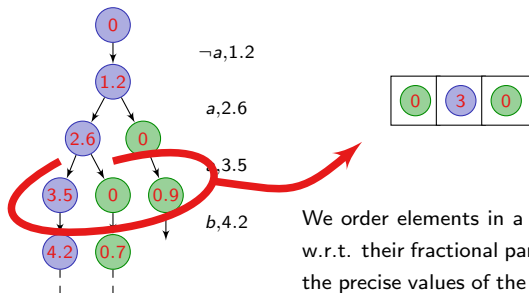


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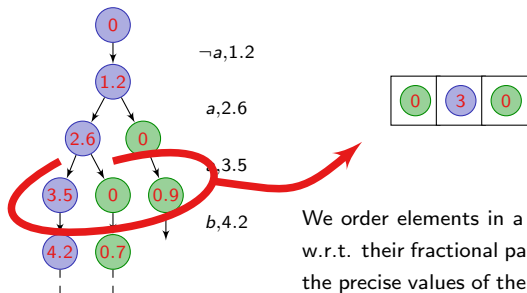
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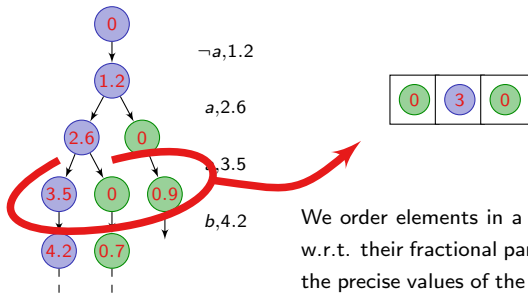
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- 👉 there is a well quasi-order on the set of abstract configurations (subword relation):

higman  $\sqsubseteq$  highmountain

# Summary

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[MH84] Miyano, Hayashi. Alternating finite automata on  $\omega$ -words (TCS, 1984).

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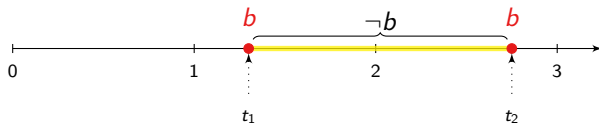
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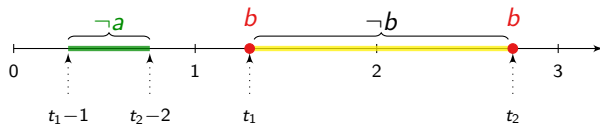
[HR04] Hirshfeld, Rabinovich. Logics for real time: decidability and complexity (Fundamenta Informaticae, 2004).

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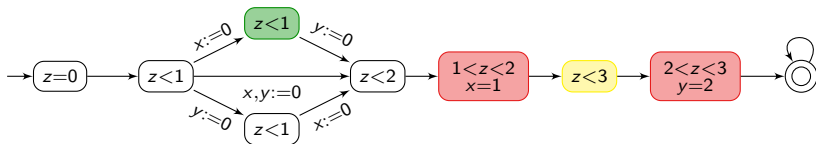
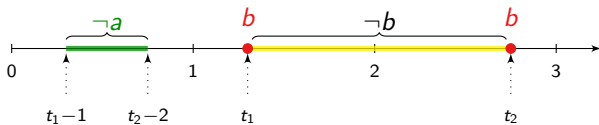
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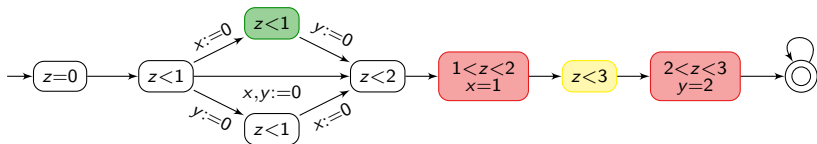
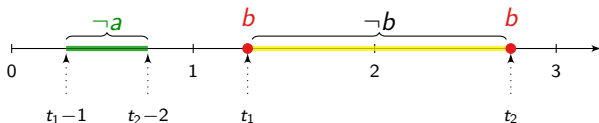
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☞ This idea can be extended to any formula in MITL

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  - ▶ **coFlat-MTL** contains **Bounded-MTL** (all modalities are time-bounded)

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- ▶ A Bounded-MTL formula may define a non timed-regular language:

$$\mathbf{G}_{\leq 1} (\bullet \rightarrow \mathbf{F}_{=1} \bullet) \wedge \mathbf{G}_{\leq 1} \bullet \wedge \mathbf{G}_{(1,2]} \bullet$$

defines the context-free language  $\{\bullet^n \bullet^m \mid n \leq m\}$ .

# Algorithm for Bounded-MTL

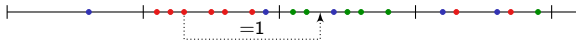
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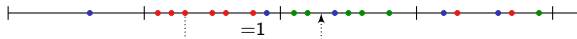
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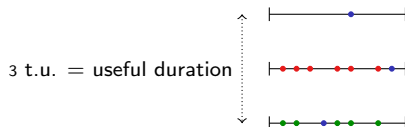
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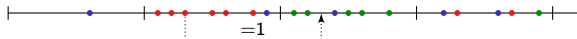
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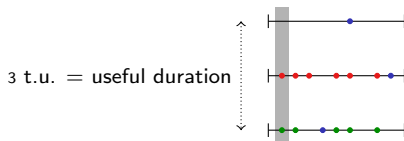
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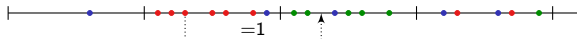




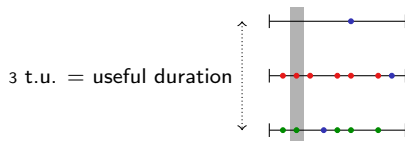
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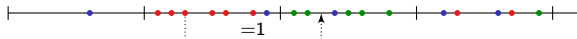
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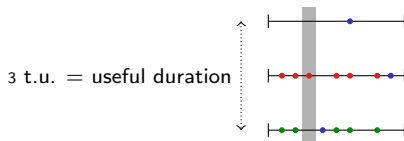
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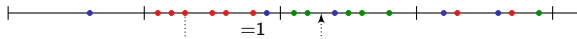
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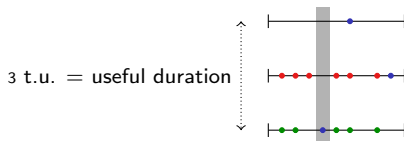
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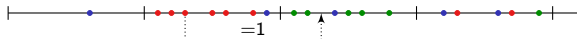
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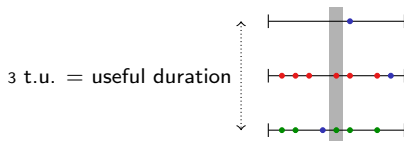
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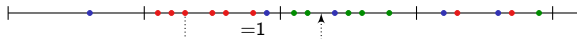
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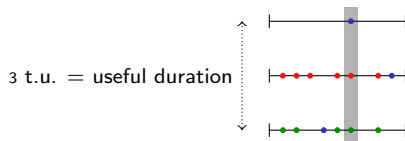
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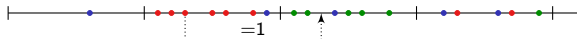
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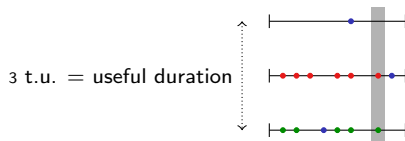
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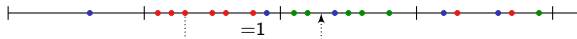
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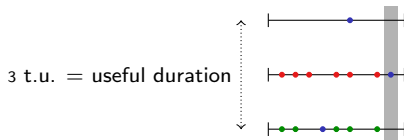
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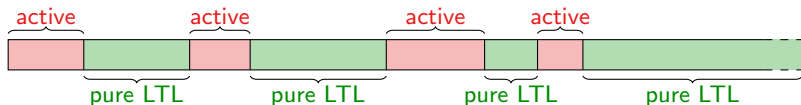
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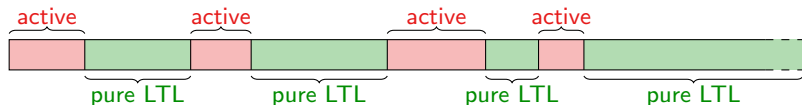
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- ▶ **active** fragment = cycle-bounded computation in a channel machine
- ▶ **pure LTL** part = finite automaton computation

# Outline

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2. Definition of the logics
3. The timed automaton model
4. The model-checking problem
5. Some interesting fragments
6. Conclusion

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