

When are timed automata determinizable?

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Outline

1. General framework
2. Timed automata
3. Towards a determinization procedure for timed automata...
 - Unfolding
 - Region equivalence
 - Symbolic determinization
 - Reducing the number of clocks
 - Reducing the number of locations
4. When can we apply the procedure?
5. Conclusion

Verification and formal languages

Real systems $Syst \rightsquigarrow \mathcal{A}_{Syst}$

Specification $Spec \rightsquigarrow \mathcal{A}_\varphi$

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The question $Syst \models Spec ? \rightsquigarrow \mathcal{L}(\mathcal{A}_{Syst}) \subseteq \mathcal{L}(\mathcal{A}_\varphi) ?$

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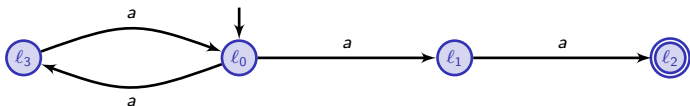
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Every finite automaton is **determinizable**
into an exponential size finite automaton.

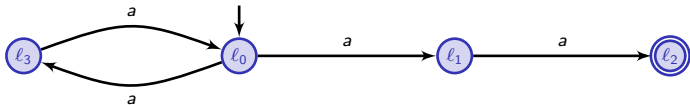
Determinizing finite automata (on finite words)

Example: $\mathcal{L}(\mathcal{A}) = (aa)^*aa$

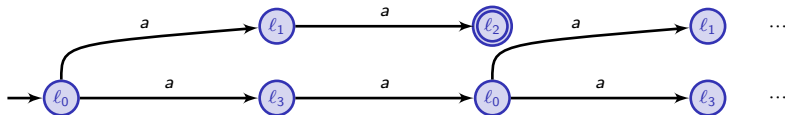


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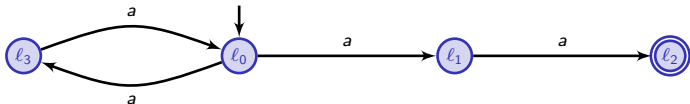


Unfolding \mathcal{A}

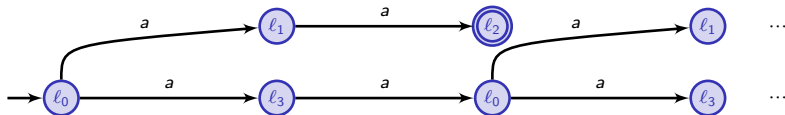


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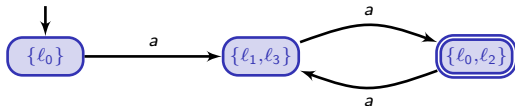
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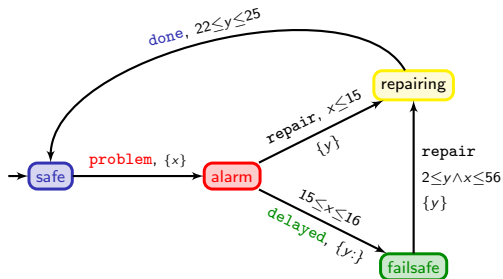
A deterministic version of \mathcal{A}



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What is a timed automaton?



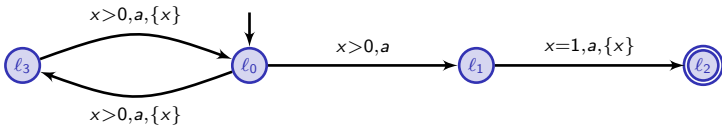
	safe	$\xrightarrow{23}$	safe	$\xrightarrow{\text{problem}}$	alarm	$\xrightarrow{15.6}$	alarm	$\xrightarrow{\text{delayed}}$	failsafe	
x	0		23		0		15.6		15.6	...
y	0		23		23		38.6		0	
	failsafe	$\xrightarrow{2.3}$	failsafe	$\xrightarrow{\text{repair}}$	repairing	$\xrightarrow{22.1}$	repairing	$\xrightarrow{\text{done}}$	safe	
...	15.6		17.9		17.9		40		40	
	0		2.3		0		22.1		22.1	

↪ It reads the timed word $(\text{problem}, 23)(\text{delayed}, 38.6)(\text{repair}, 40.9)(\text{done}, 63)$

Timed languages accepted by timed automata

Example

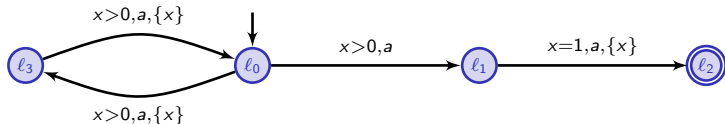
Let \mathcal{A} be the following timed automaton:



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Let \mathcal{A} be the following timed automaton:



$$\mathcal{L}(\mathcal{A}) = \{(a, t_1)(a, t_2) \cdots (a, t_{2n}) \mid$$

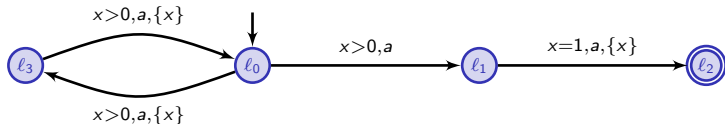
$$n \geq 1, 0 < t_1 < t_2 < \cdots < t_{2n-1}$$

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The timed word $w = (a, 0.2)(a, 0.5)(a, 1.2)(a, 1.5)$ is in $\mathcal{L}(\mathcal{A})$.

Results on timed automata [AD90,AD94]

Emptiness problem

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~> prevents using timed automata as a specification language

Timed automata and determinism

Deterministic timed automaton

A timed automaton \mathcal{A} is **deterministic** whenever for every timed word w , there is at most one initial run (starting from $(\ell_0, 0)$) which reads u .

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Checking universality (and language inclusion) is **PSPACE-complete** for deterministic timed automata.

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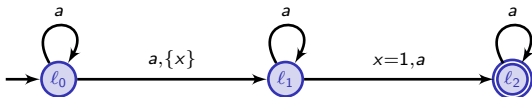
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There exist timed automata that are **not determinizable** [AD90]



$$\mathcal{L}(\mathcal{A}) = \{(a, t_1) \dots (a, t_n) \mid n \geq 2 \text{ and } \exists i < j \text{ s.t. } t_j - t_i = 1\}$$

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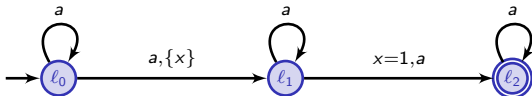
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Theorem [Tri03, Fin06]

We **cannot decide** whether a timed automaton can be determinized.

Event-clock timed automata [AFH94]

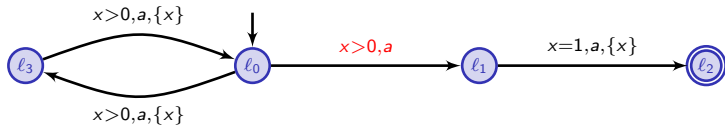
Event-clock timed automata

An **event-clock timed automaton** is a timed automaton that contains only event-recording clocks: for every letter $a \in \Sigma$, there is a clock x_a , which is reset at every occurrence of an a .

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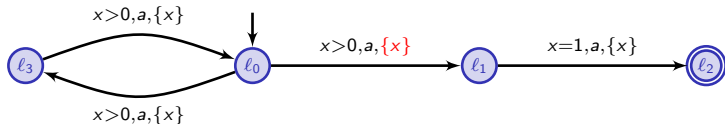


\mathcal{A} is not an event-clock timed automaton

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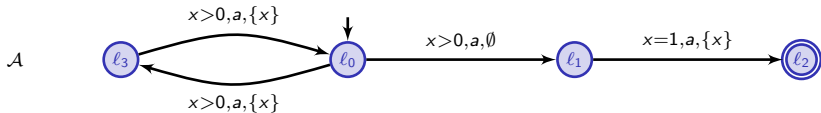
Theorem

- Event-clock timed automata are **determinizable**.
- Checking universality (and language inclusion) is **PSPACE-complete** for event-clock timed automata.

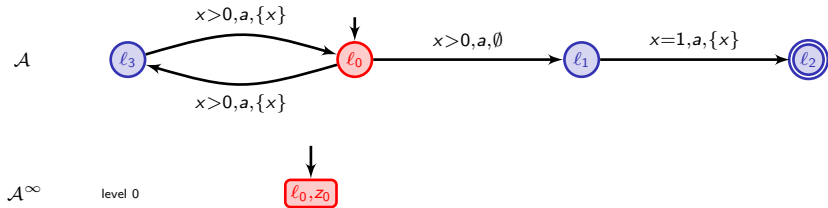
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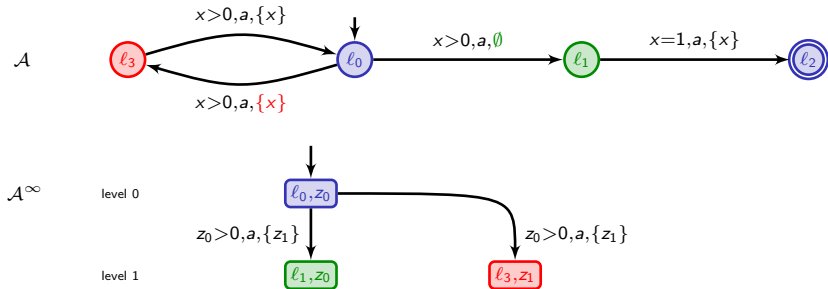
Unfolding



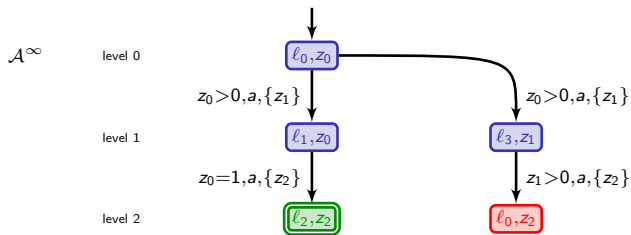
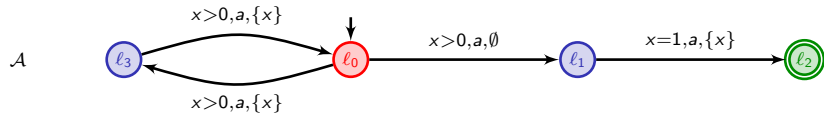
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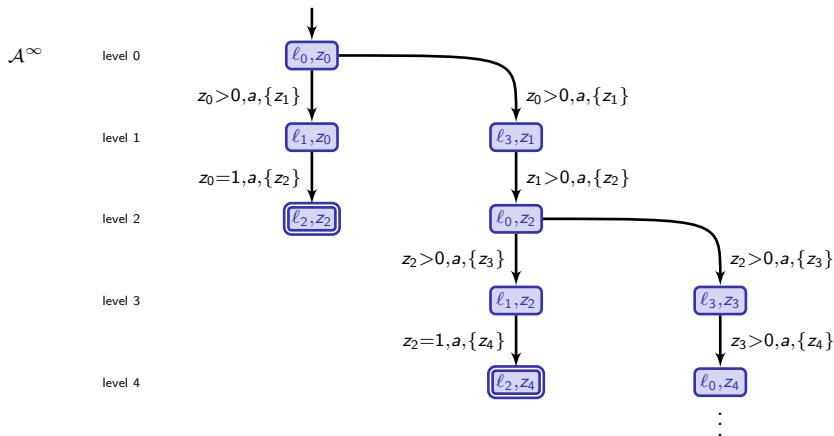
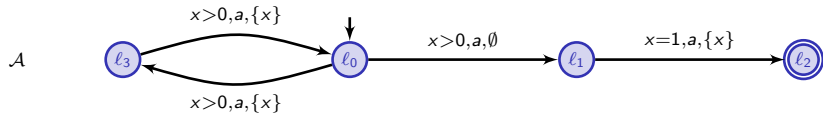
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Properties of the unfolding

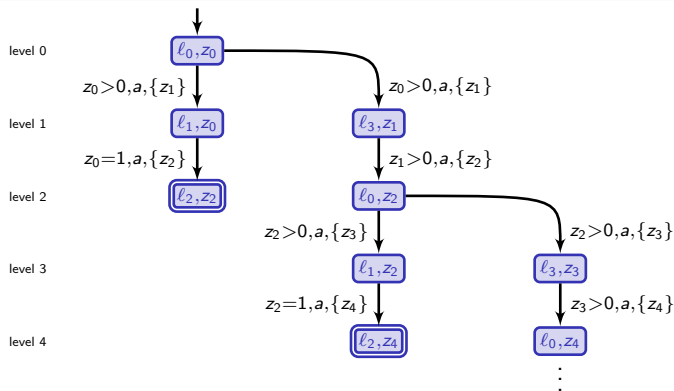
Advantage of the unfolding: “input-determinacy”

Given a finite timed word w , there is a unique valuation v_w such that every initial run reading w ends in a configuration (n, v_w) with $level(n) = |w|$.

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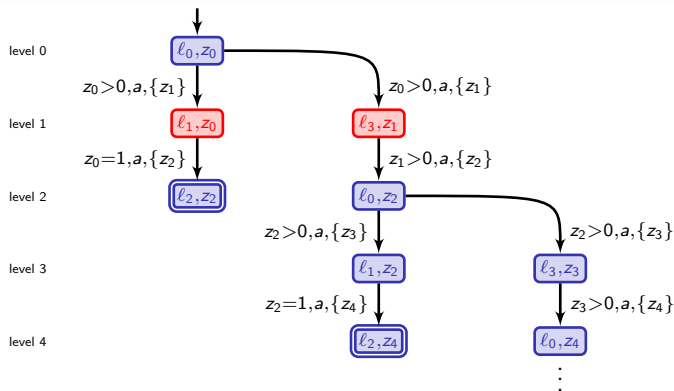
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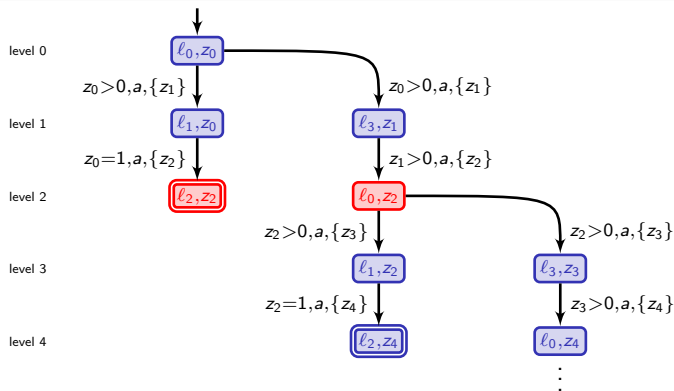
$$w = (a, 0.2)$$

$$\rightsquigarrow v_w = \begin{pmatrix} 0.2, 0 \\ z_0 \quad z_1 \end{pmatrix}$$

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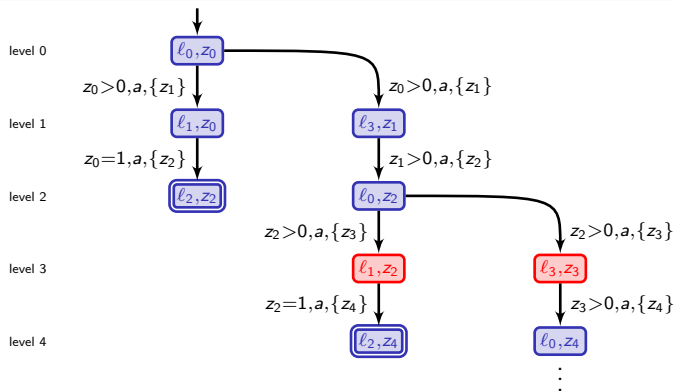
$$\rightsquigarrow v_w = (0.5, 0.3, 0)$$

$z_0 \quad z_1 \quad z_2$

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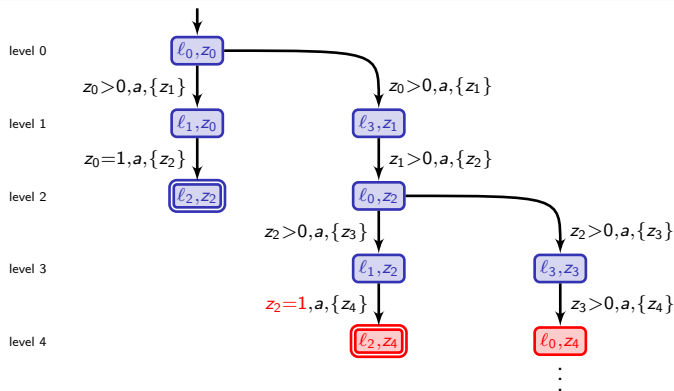
$$\rightsquigarrow v_w = (1.2, 1, 0.7, 0)$$

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$\begin{matrix} z_0 & z_1 & z_2 & z_3 & z_4 \end{matrix}$

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Drawbacks of the unfolding

- \mathcal{A}^∞ has **infinitely** many locations.
- \mathcal{A}^∞ has **infinitely** many clocks.
- \mathcal{A}^∞ is **not deterministic**.

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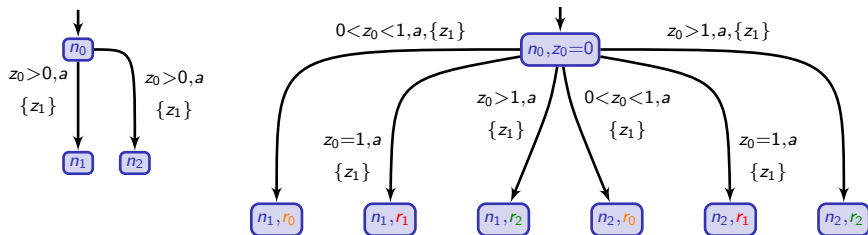
Lemma

\mathcal{A} and \mathcal{A}^∞ are strongly timed bisimilar.
In particular $\mathcal{L}(\mathcal{A}) = \mathcal{L}(\mathcal{A}^\infty)$.

Region equivalence on \mathcal{A}^∞

The standard region equivalence naturally extends to \mathcal{A}^∞ ,

at level i we only consider region over $\{z_1, \dots, z_i\}$.

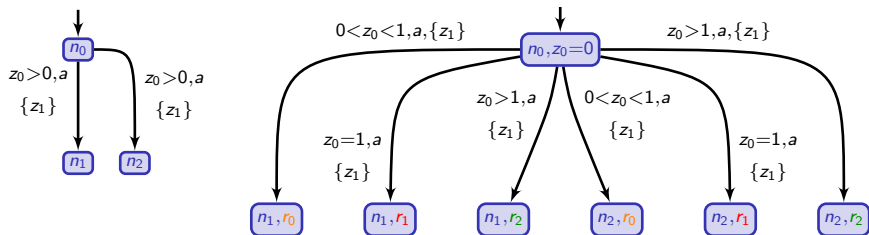


where $r_0 = (0 = z_1 < z_0 < 1)$, $r_1 = (0 = z_1 < z_0 = 1)$, $r_2 = (0 = z_1 < 1 < z_0)$.

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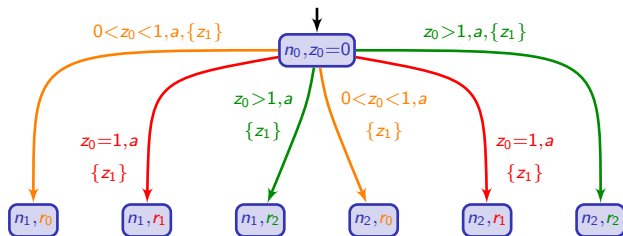


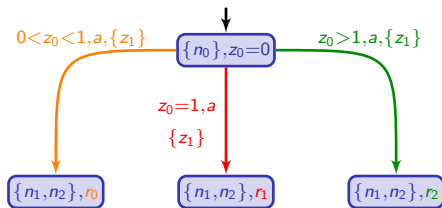
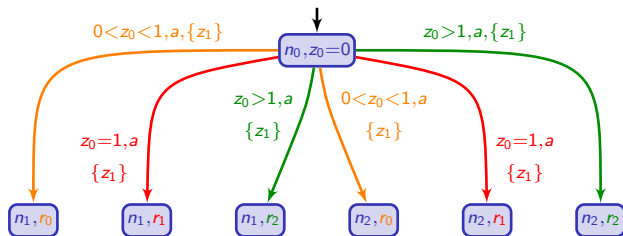
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Lemma

\mathcal{A}^∞ and $R(\mathcal{A}^\infty)$ are strongly timed bisimilar.

In particular, $\mathcal{L}(\mathcal{A}^\infty) = \mathcal{L}(R(\mathcal{A}^\infty))$.

Symbolic determinization of $R(\mathcal{A}^\infty)$ 

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Properties of the symbolic determinization

Advantage of $\text{SymbDet}(R(\mathcal{A}^\infty))$

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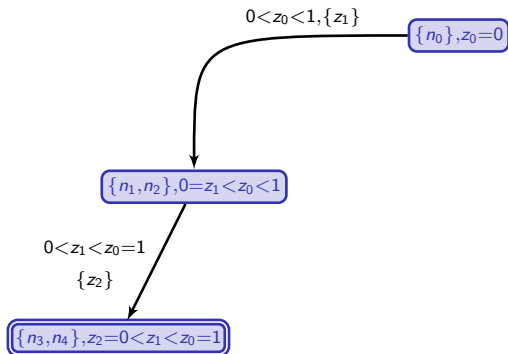
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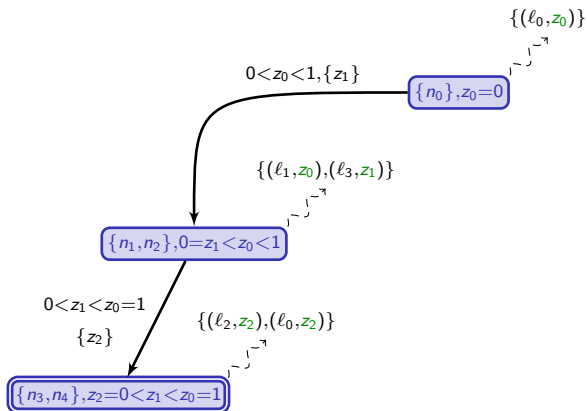
$$\mathcal{L}(\mathcal{A}) = \mathcal{L}(\text{SymbDet}(R(\mathcal{A}^\infty))).$$

Notion of active clocks

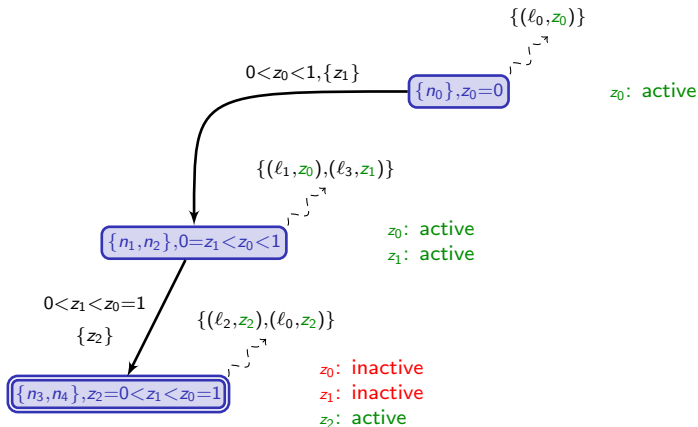


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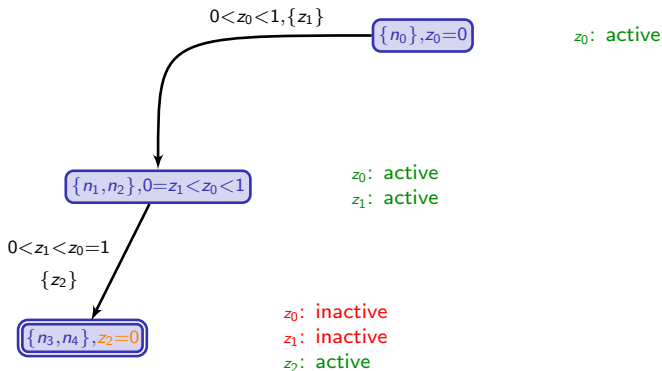
Remember where nodes come from!

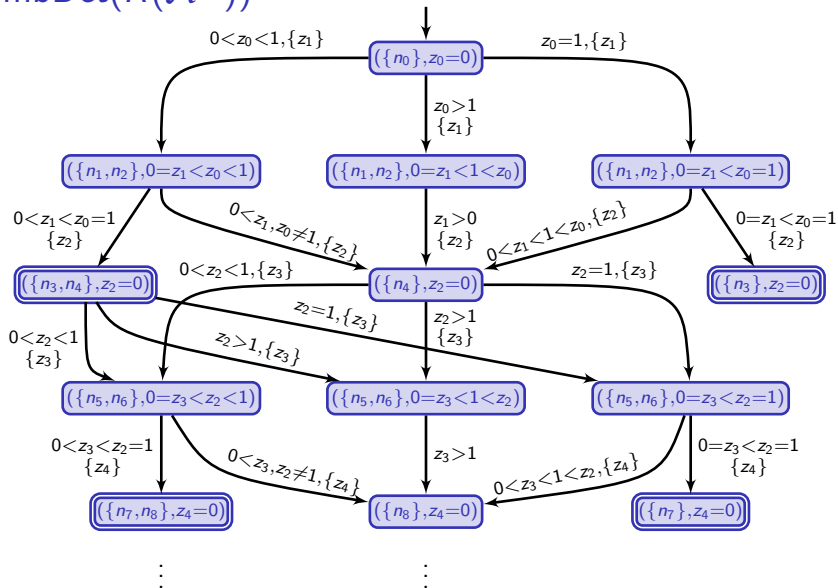


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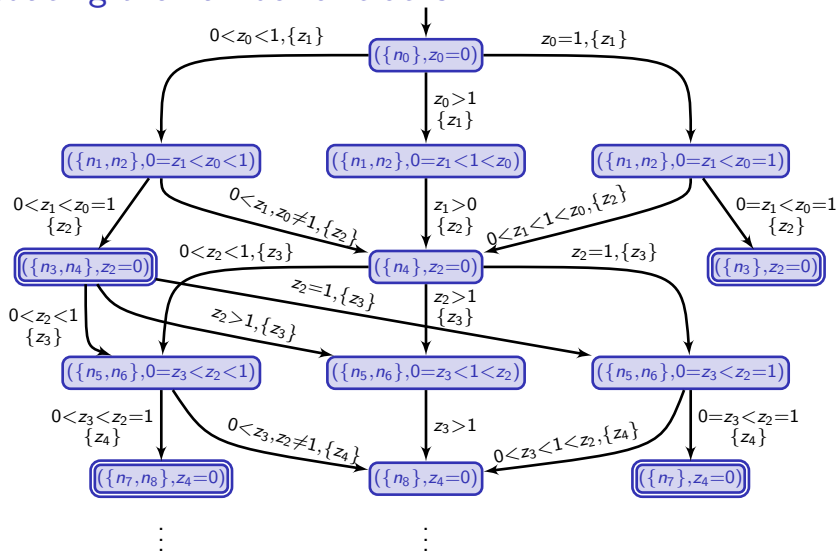


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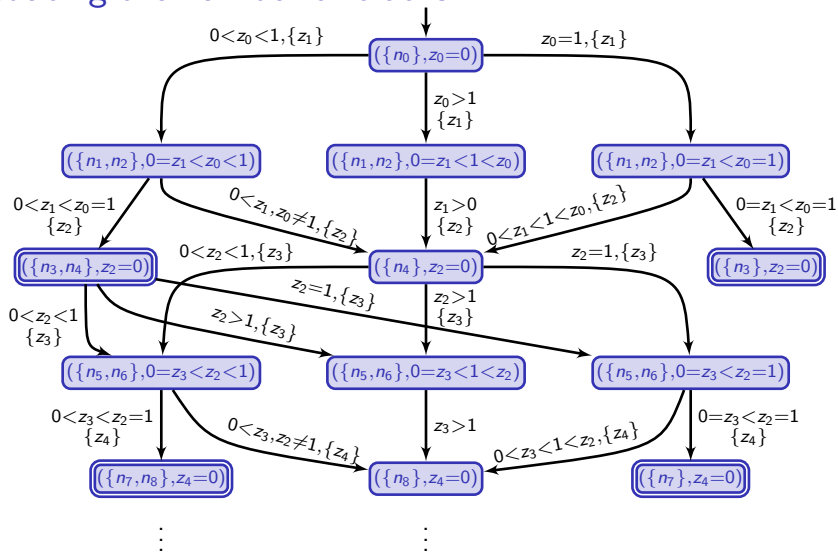


SymbDet($R(\mathcal{A}^\infty)$)

Reducing the number of clocks



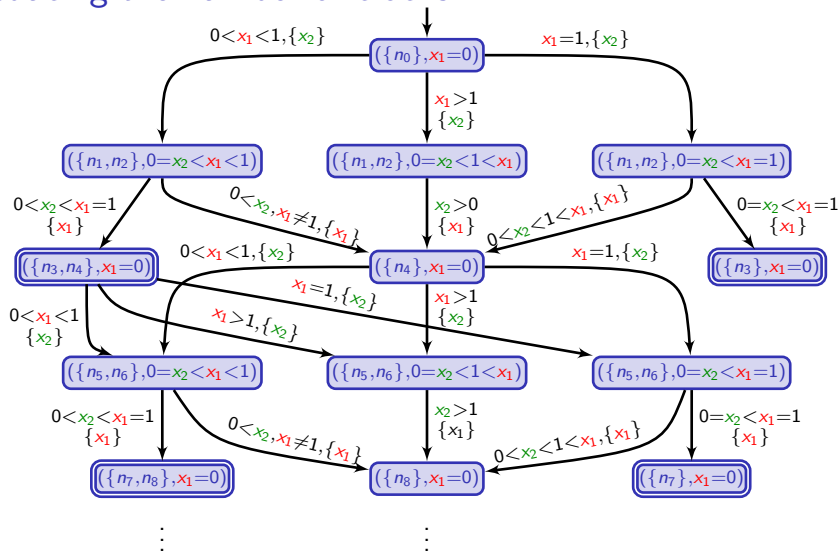
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Two clocks are sufficient to get full timing information!

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Properties of the clock reduction

Given $\gamma \in \mathbb{N}$, we say that $\text{SymbDet}(R(\mathcal{A}^\infty))$ is γ -clock-bounded if in every node, the number of active clocks is bounded by γ .

Properties of the clock reduction

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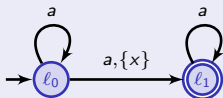
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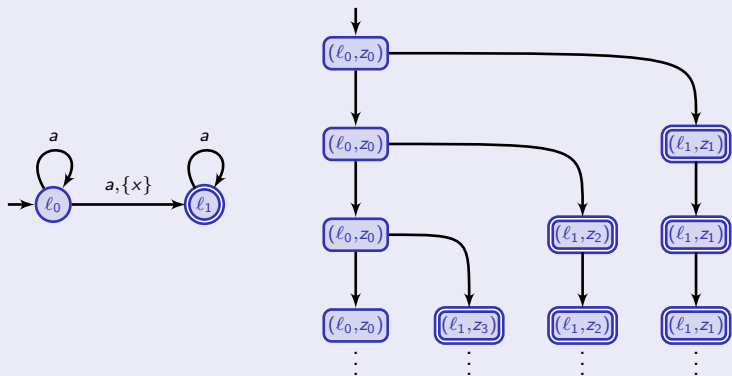


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Advantages of $\Gamma_\gamma(\text{SymbDet}(R(\mathcal{A}^\infty)))$

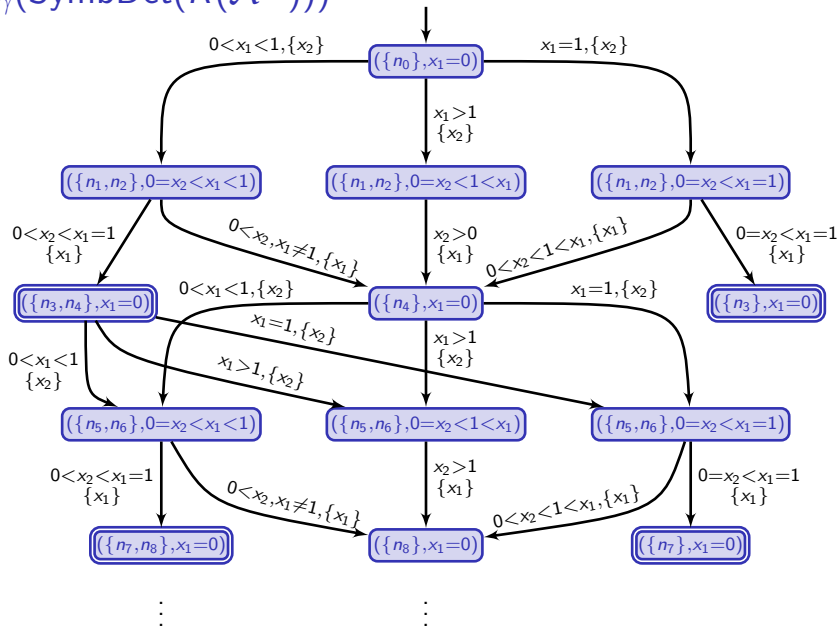
- $\Gamma_\gamma(\text{SymbDet}(R(\mathcal{A}^\infty)))$ is **deterministic**!
- $\Gamma_\gamma(\text{SymbDet}(R(\mathcal{A}^\infty)))$ has **finitely** many clocks.

Drawback of $\Gamma_\gamma(\text{SymbDet}(R(\mathcal{A}^\infty)))$

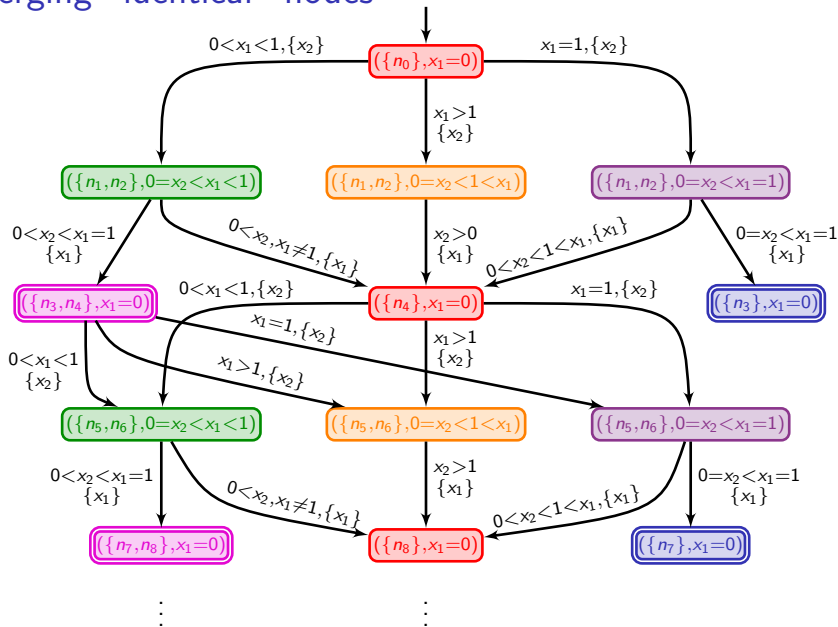
$\Gamma_\gamma(\text{SymbDet}(R(\mathcal{A}^\infty)))$ has **infinitely** many locations.

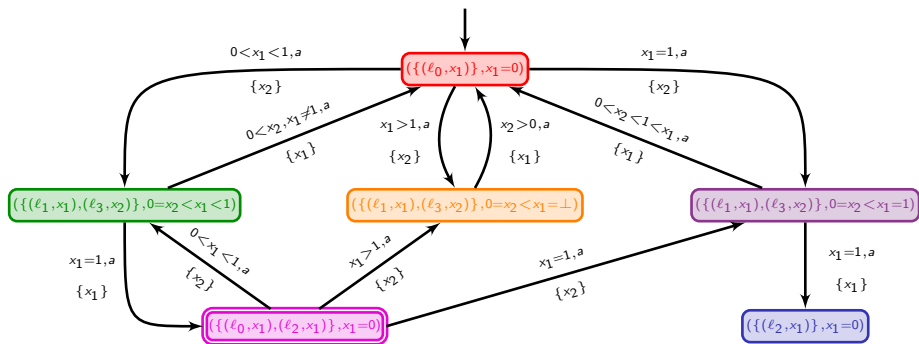
Lemma

$$\mathcal{L}(\mathcal{A}) = \mathcal{L}(\Gamma_\gamma(\text{SymbDet}(R(\mathcal{A}^\infty)))).$$

$$\Gamma_\gamma(\text{SymbDet}(R(\mathcal{A}^\infty)))$$


Merging "identical" nodes



A deterministic timed automaton equivalent to \mathcal{A} 

Properties of the location reduction

In case $\text{SymbDet}(R(\mathcal{A}^\infty))$ is γ -clock-bounded, we define $\mathcal{B}_{\mathcal{A},\gamma}$ obtained by merging the nodes of $\Gamma_\gamma(\text{SymbDet}(R(\mathcal{A}^\infty)))$ with “the same labels”.

Theorem

In case $\text{SymbDet}(R(\mathcal{A}^\infty))$ is γ -clock-bounded, $\mathcal{B}_{\mathcal{A},\gamma}$ is a **deterministic timed automaton** such that $\mathcal{L}(\mathcal{A}) = \mathcal{L}(\mathcal{B}_{\mathcal{A},\gamma})$.

Outline

1. General framework
2. Timed automata
3. Towards a determinization procedure for timed automata...
 - Unfolding
 - Region equivalence
 - Symbolic determinization
 - Reducing the number of clocks
 - Reducing the number of locations
4. When can we apply the procedure?
5. Conclusion

When can we apply our procedure?

We need to have that $\text{SymbDet}(R(\mathcal{A}^\infty))$ is γ -clock bounded.

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The p -assumption

Given $p \in \mathbb{N}$, \mathcal{A} satisfies the **p -assumption** if for every $n \geq p$, for every run

$$\varrho = (\ell_0, v_0) \xrightarrow{\tau_1, a_1} (\ell_1, v_1) \dots \xrightarrow{\tau_n, a_n} (\ell_n, v_n)$$

for every clock $x \in X$, either x is reset along ϱ or $v_n(x) > M$.

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Classes to which the procedure applies

- Event-clock timed automata (with $\gamma = |\Sigma|$)
- **Strongly non-Zeno** timed automata (since they satisfy the p -assumption)
- timed automata with integer resets [SPKM08]

Hardness issues

We can prove **EXSPACE-hardness** of:

- the **universality problem** for timed automata satisfying the ρ -assumption and for timed automata with integer resets;
- the **inclusion problem** for strongly non-Zeno timed automata.

The results

Summary of the complexity results

	size of the det. TA	universality problem	inclusion problem
TA_p	doubly exp.	EXPSPACE-compl.	EXPSPACE-compl.
SnZTA	doubly exp.	trivial	EXPSPACE-compl.
ECTA [AFH94]	exp.	PSPACE-compl.	PSPACE-compl.
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Remark

In case \mathcal{A} has one clock, $\text{SymbDet}(R(\mathcal{A}^\infty))$ allows to recover the decidability of the universality problem in one-clock TA [OW04].

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- We have described a procedure to determinize timed automata...
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What we will do now

- We want to see whether other determinizable classes (open timed automata) could fit our framework.
- We will extend to infinite timed words (with a Safra-like construction mixed with our procedure?)