

On the Model Checking of Timed and Weighted Temporal Logics

Patricia Bouyer

LSV, CNRS & ENS Cachan, France

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Outline

1. Introduction
2. Definition of the logics
3. The timed automaton model
4. The model-checking problem
5. Some interesting fragments
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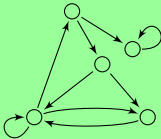


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Model-checking

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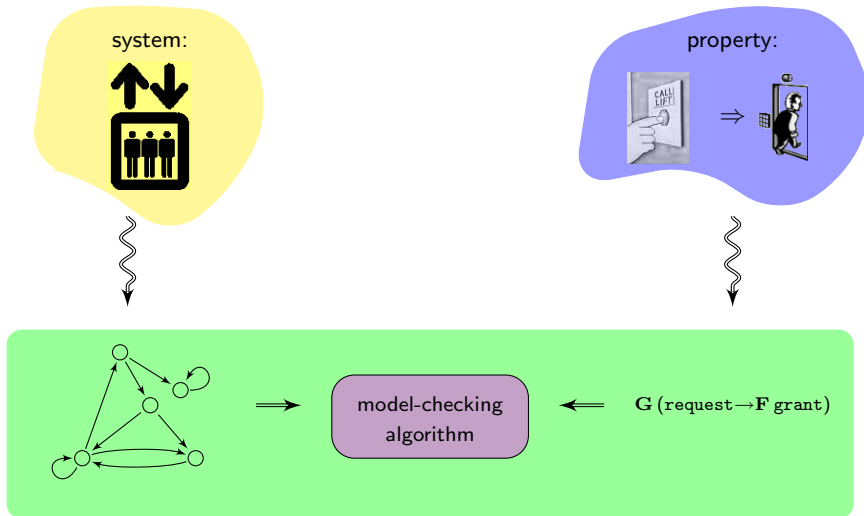


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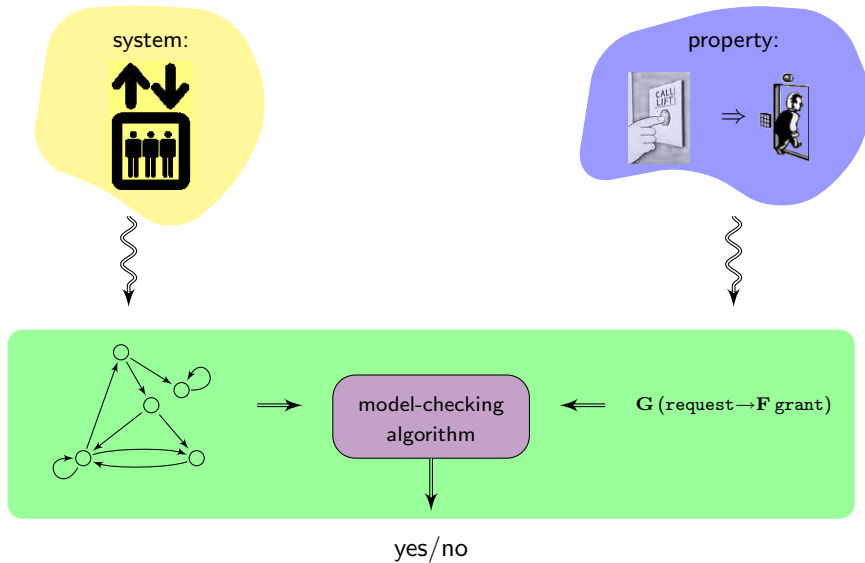


$G (\text{request} \rightarrow F \text{ grant})$

Model-checking



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The untimed (linear-time) framework

Linear-time temporal logic [Pnu77]

$$\text{LTL} \ni \varphi ::= p \mid \varphi \wedge \varphi \mid \varphi \vee \varphi \mid \neg \varphi \mid \mathbf{X} \varphi \mid \varphi \mathbf{U} \varphi$$

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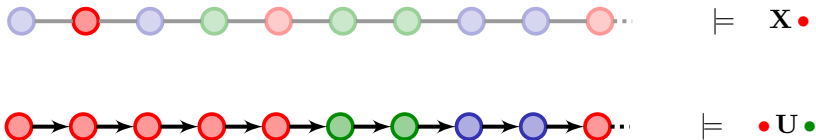
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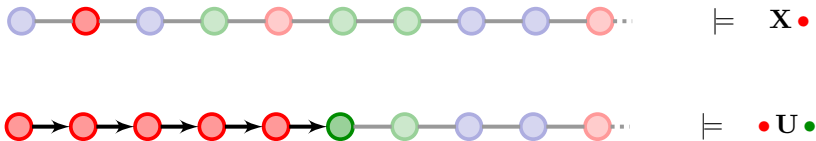
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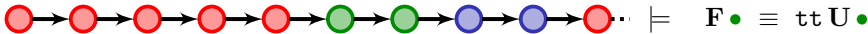
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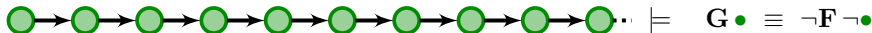
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- a more complex property:

$$(\bullet \wedge (\mathbf{F} \bullet \vee \mathbf{G} \bullet)) \mathbf{U} \bullet$$

Adding timing requirements

- Need for **timed models**
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 - faithful modelling has to take time into account.
- ☞ timed automata, time(d) Petri nets, timed process algebras...

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☞ **timed automata, time(d) Petri nets, timed process algebras...**
- Need for **timed specification languages**
 - the behaviour of most systems depends on time;
 - untimed specifications are not sufficient
(for instance, bounded response time, etc...)

☞ **TCTL, MTL, TPTL, timed μ -calculus...**

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Metric Temporal Logic (MTL)

[Koy90]

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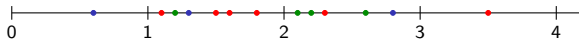
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- Can be interpreted over **finite** or **infinite** behaviours
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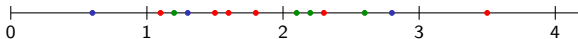
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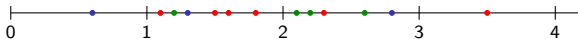


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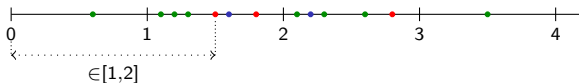
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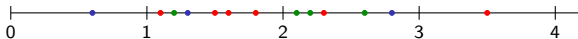
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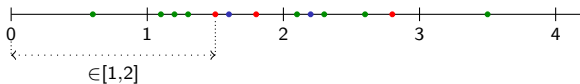
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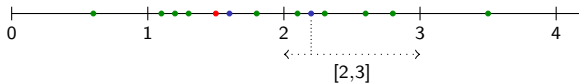


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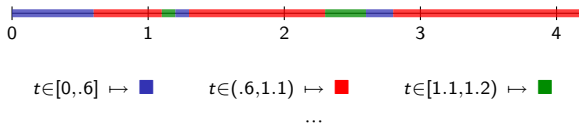
$\models \bullet U_{[1,2]} \bullet$



$\not\models G_{[2,3]} \bullet$

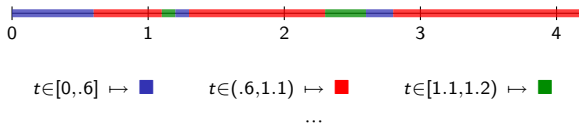
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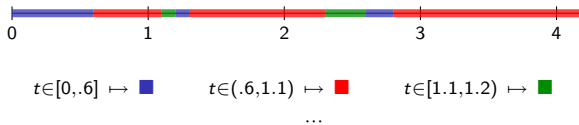
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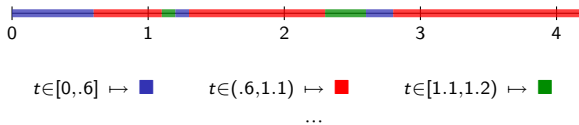


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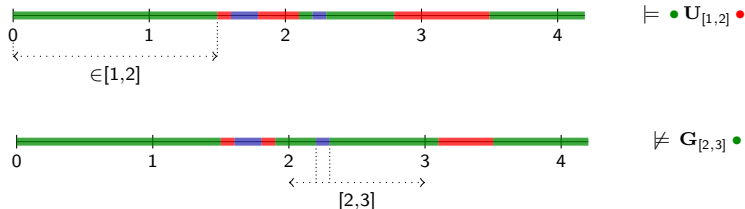


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Some examples

- “Every problem is followed within 56 time units by an alarm”

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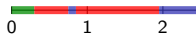
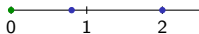
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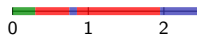
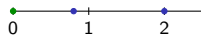
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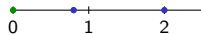
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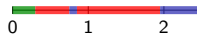
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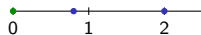
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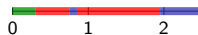
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- in the pointwise semantics, $F_{=2} \bullet \not\equiv F_{=1} F_{=1} \bullet$
- in the continuous semantics, $F_{=2} \bullet \equiv F_{=1} F_{=1} \bullet$

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LTL+Past is as expressive as **LTL** [Kam68,GPSS80].

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- This is wrong in the **continuous** semantics!

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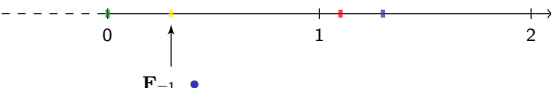


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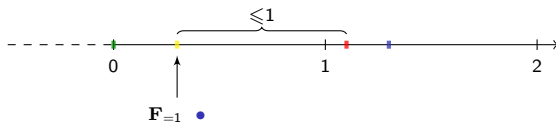


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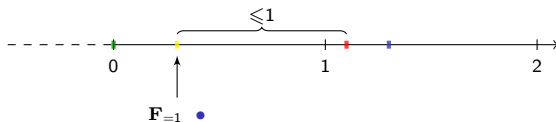


$$\mathbf{G} \bullet \rightarrow \left\{ \begin{array}{l} \mathbf{F}_{\leq 1} \bullet \wedge \mathbf{F}_{[1,2]} \bullet \\ \vee \\ \mathbf{F}_{\leq 1} (\bullet \wedge \mathbf{F}_{\leq 1} \bullet) \end{array} \right.$$

The TPTL formula

$$\mathbf{G} (\bullet \rightarrow x.\mathbf{F} (\bullet \wedge \mathbf{F} (\bullet \wedge x \leq 2)))$$

can be expressed in MTL in the continuous semantics

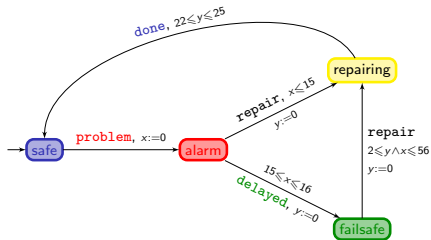


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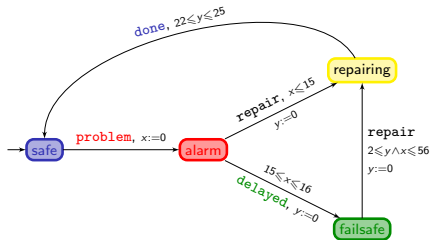
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Timed automata

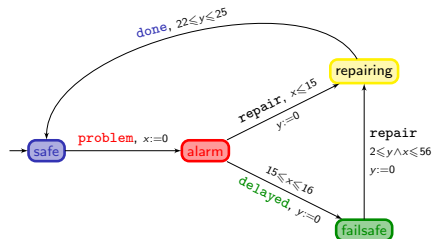


Timed automata



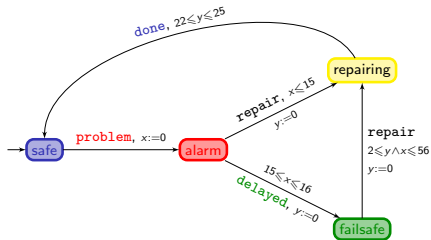
safe
x 0
y 0

Timed automata



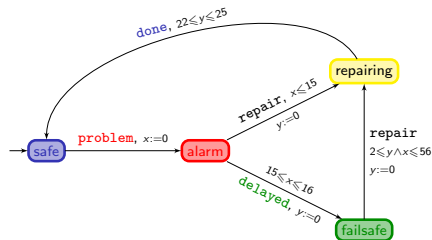
	safe	$\xrightarrow{23}$	safe
x	0		23
y	0		23

Timed automata



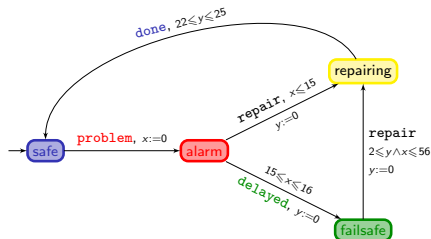
	safe	$\xrightarrow{23}$	safe	$\xrightarrow{\text{problem}}$	alarm
x	0		23		0
y	0		23		23

Timed automata



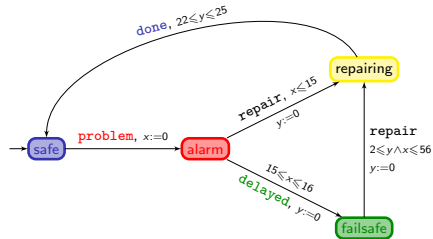
	safe	23	safe	problem	alarm	15.6	alarm
x	0		23		0		15.6
y	0		23		23		38.6

Timed automata



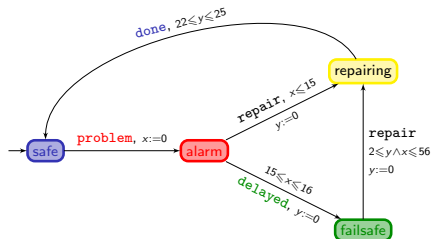
	safe	$\xrightarrow{23}$	safe	$\xrightarrow{\text{problem}}$	alarm	$\xrightarrow{15.6}$	alarm	$\xrightarrow{\text{delayed}}$	failsafe
x	0		23		0		15.6		15.6
y	0		23		23		38.6		0

Timed automata



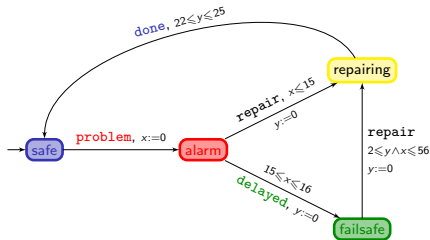
	safe	$\xrightarrow{23}$	safe	$\xrightarrow{\text{problem}}$	alarm	$\xrightarrow{15.6}$	alarm	$\xrightarrow{\text{delayed}}$	failsafe	$\xrightarrow{2.3}$	failsafe
x	0		23		0		15.6		15.6		17.9
y	0		23		23		38.6		0		2.3

Timed automata



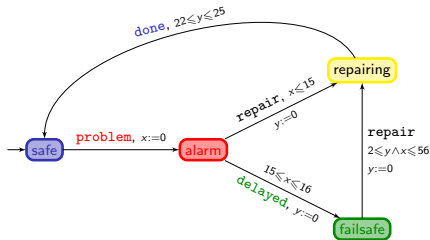
	safe	23	safe	problem	alarm	15.6	alarm	delayed	failsafe	2.3	failsafe	repair	reparation
x	0	23	23	0	15.6	15.6	15.6	0	15.6	17.9	17.9	17.9	17.9
y	0	23	23	0	23	38.6	38.6	0	0	2.3	2.3	0	0

Timed automata



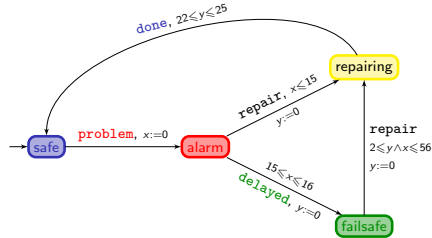
	safe	$\xrightarrow{23}$	safe	$\xrightarrow{\text{problem}}$	alarm	$\xrightarrow{15.6}$	alarm	$\xrightarrow{\text{delayed}}$	failsafe	$\xrightarrow{2.3}$	failsafe	$\xrightarrow{\text{repair}}$	reparation	$\xrightarrow{22.1}$	reparation
x	0		23		0		15.6		15.6		17.9		17.9		40
y	0		23		23		38.6		0		2.3		0		22.1

Timed automata



	safe	$\xrightarrow{23}$	safe	$\xrightarrow{\text{problem}}$	alarm	$\xrightarrow{15.6}$	alarm	$\xrightarrow{\text{delayed}}$	failsafe	$\xrightarrow{2.3}$	failsafe	$\xrightarrow{\text{repair}}$	reparation	$\xrightarrow{22.1}$	reparation	$\xrightarrow{\text{done}}$	safe
x	0		23		0		15.6		15.6		17.9		17.9		40		40
y	0		23		23		38.6		0		2.3		0		22.1		22.1

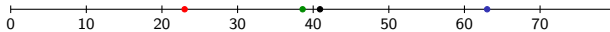
Timed automata



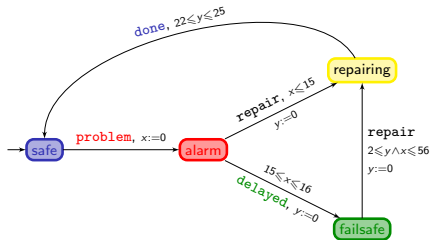
	safe	$\xrightarrow{23}$	safe	$\xrightarrow{\text{problem}}$	alarm	$\xrightarrow{15.6}$	alarm	$\xrightarrow{\text{delayed}}$	failsafe	$\xrightarrow{2.3}$	failsafe	$\xrightarrow{\text{repair}}$	reparation	$\xrightarrow{22.1}$	reparation	$\xrightarrow{\text{done}}$	safe
x	0		23		0		15.6		15.6		17.9		17.9		40		40
y	0		23		23		38.6		0		2.3		0		22.1		22.1

Can be viewed:

- as the timed word $(\text{problem}, 23)(\text{delayed}, 38.6)(\text{repair}, 40.9)(\text{done}, 63)$



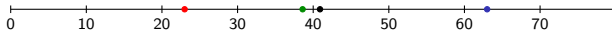
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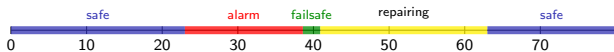
	safe	$\xrightarrow{23}$	safe	$\xrightarrow{\text{problem}}$	alarm	$\xrightarrow{15.6}$	alarm	$\xrightarrow{\text{delayed}}$	failsafe	$\xrightarrow{2.3}$	failsafe	$\xrightarrow{\text{repair}}$	reparation	$\xrightarrow{22.1}$	reparation	$\xrightarrow{\text{done}}$	safe
x	0		23		0		15.6		15.6		17.9		17.9		40		40
y	0		23		23		38.6		0		2.3		0		22.1		22.1

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- as the signal



Basic result on timed automata

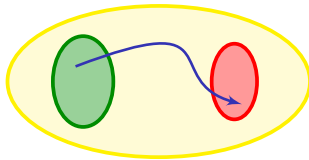
Theorem

The reachability problem is decidable (and **PSPACE**-complete) for timed automata [AD94].


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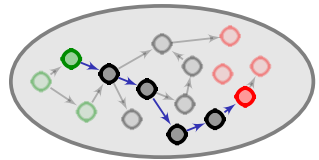
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timed automaton

finite bisimulation


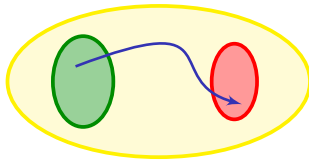


large (but finite) automaton
 (region automaton)


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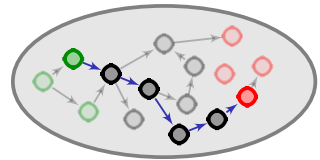
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☞ It can be extended to model-check **TCTL** [ACD93].

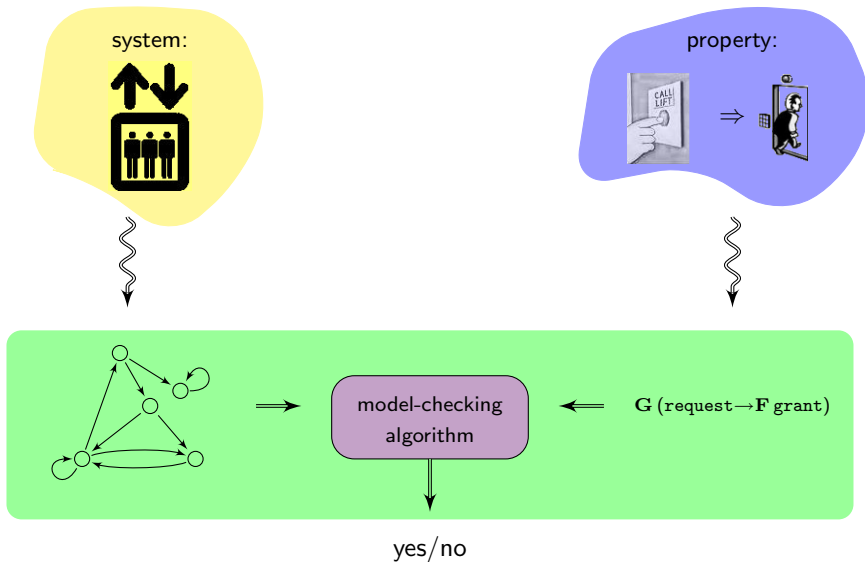
[AD94] Alur, Dill. A theory of timed automata (TCS, 1994).

[ACD93] Alur, Courcoubetis, Dill. Model-checking in dense real-time (I&C, 1993).

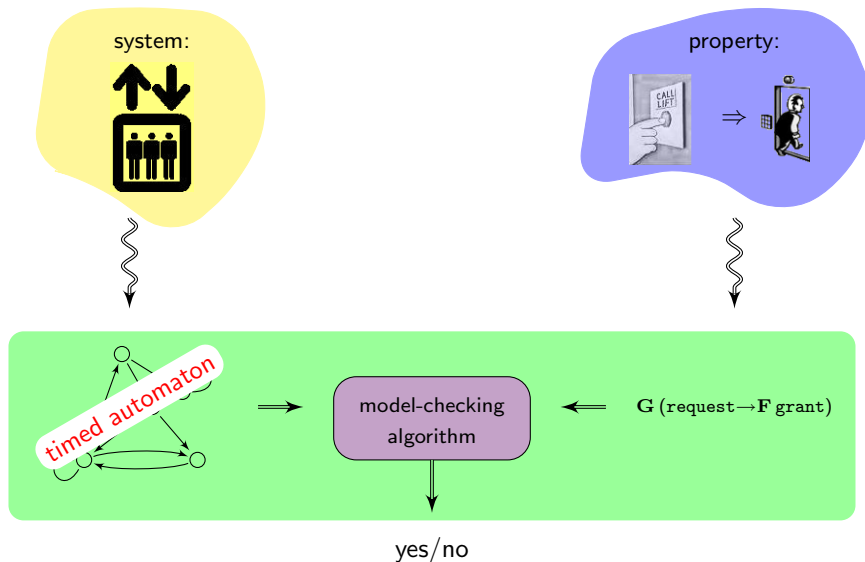
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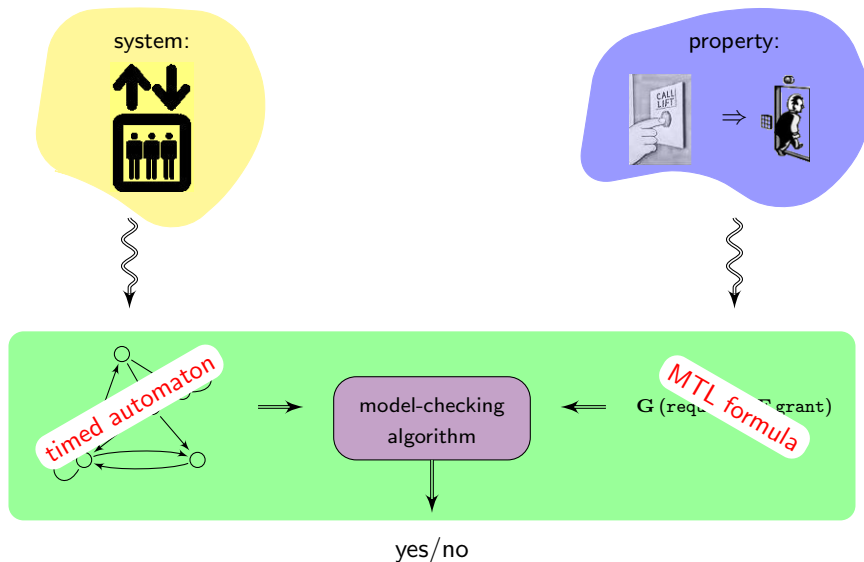
Back to the model-checking problem



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Results

Theorem

Over **finite runs**, the model-checking problem is:

	pointwise sem.	continuous sem.
MTL	decidable, NPR [OW05]	undecidable [AFH96]
MTL+Past	undecidable	undecidable
TPTL	undecidable [AH94]	undecidable [AH94]

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👉 we will explain this high complexity, following [Che07]

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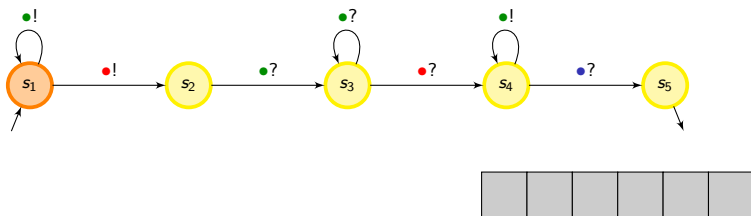
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[Che07] Chevalier. Logiques pour les systèmes temporisés : contrôle et expressivité (PhD Thesis ENS Cachan, June 2007).

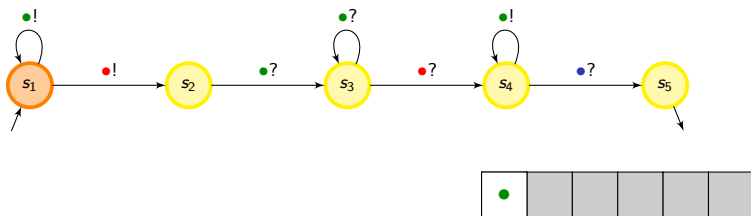
A short visit to channel machines (1)

A channel machine = a finite automaton + a FIFO channel



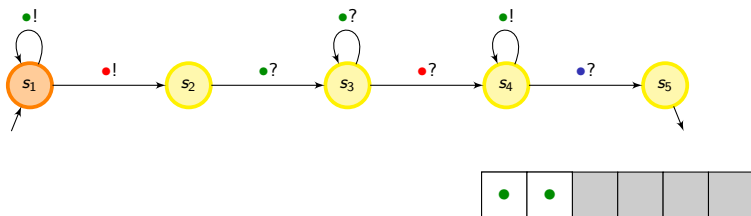
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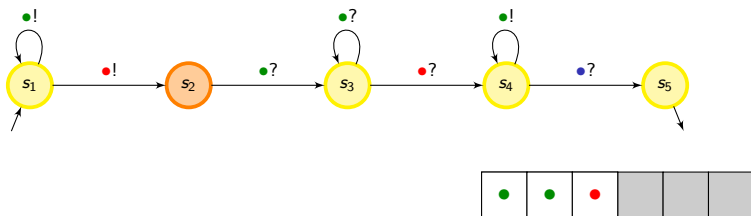
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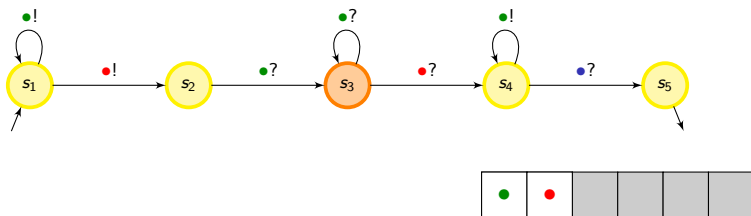
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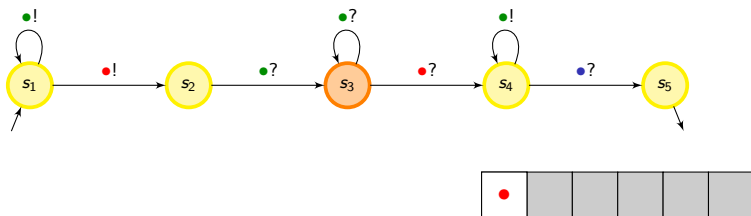
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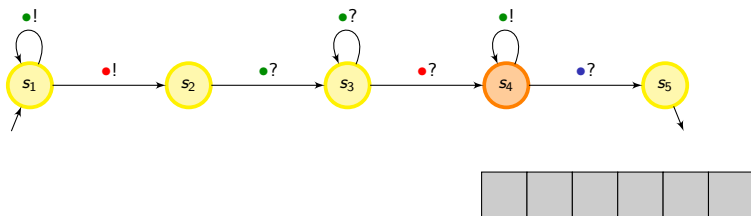
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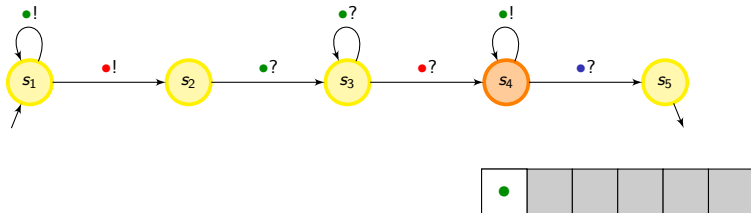
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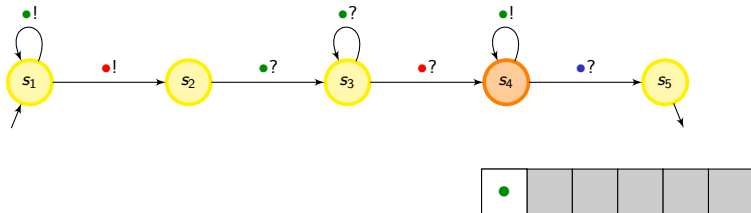
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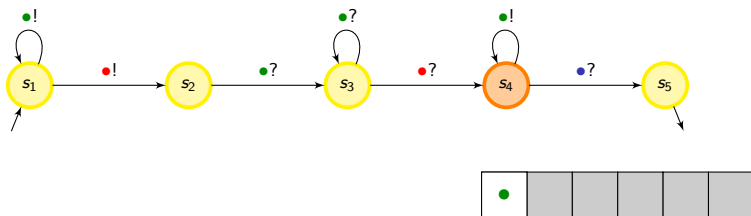
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s_5 is not reachable

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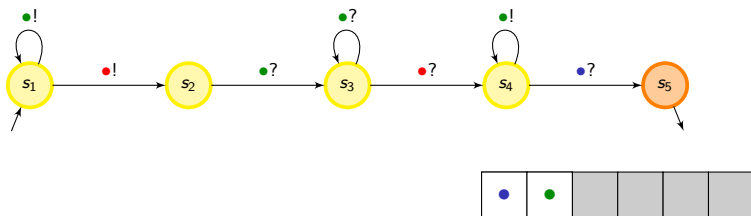
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- insertion errors: any letter can appear on the channel at any time

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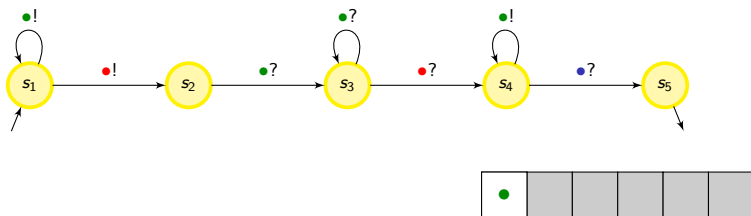
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A short visit to channel machines (2)

Halting problem: is there an execution ending in a halting state?

[BZ83] Brand, Zafiropulo. On communicating finite-state machines (Journal of the ACM, 1983).

[Sch02] Schnoebelen. Verifying lossy channel systems has non-primitive recursive complexity (IPL, 2002).

A short visit to channel machines (2)

Halting problem: is there an execution ending in a halting state?

Proposition

- The halting problem is undecidable for channel machines [BZ83].
- The halting problem is decidable but NPR for channel machines with insertion errors [Sch02].

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Channel machines and timed words

We encode an execution of a channel machine as a timed word:

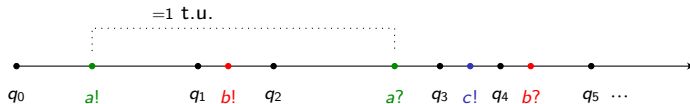
$$(q_0, \varepsilon) \xrightarrow{a!} (q_1, a) \xrightarrow{b!} (q_2, ab) \xrightarrow{a?} (q_3, b) \xrightarrow{c!} (q_4, bc) \xrightarrow{b?} (q_5, c) \cdots$$



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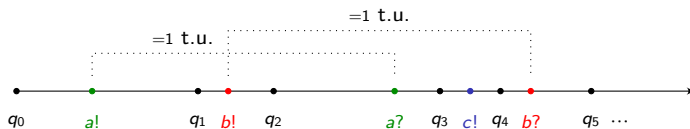
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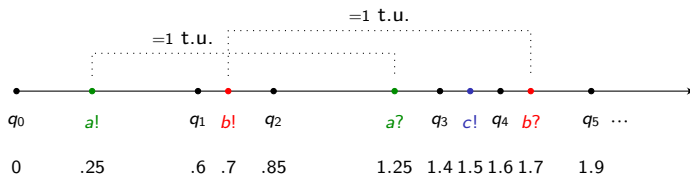
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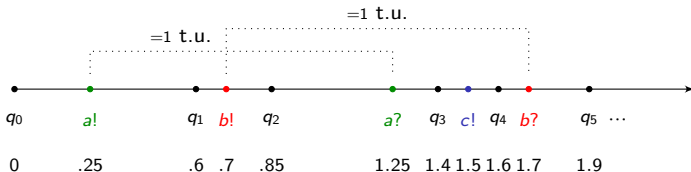
the channel machine^{*} halts iff the formula φ is satisfiable

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- states and actions alternate, and the sequence satisfies the rules of the channel machine: **LTL formula**

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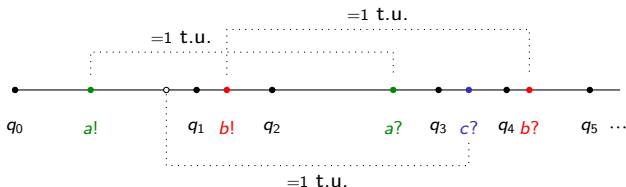
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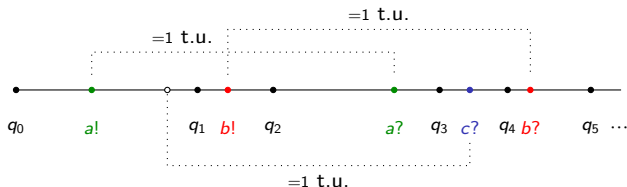
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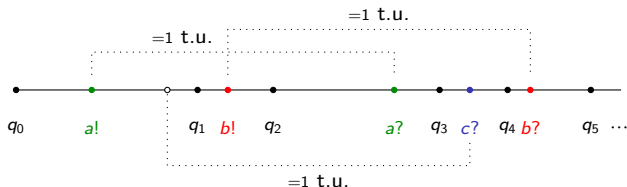
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☞ model-checking **MTL** is NPR

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“Every $a?$ -event is preceded one time unit earlier by an $a!$ -event”

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 - not correct in the pointwise semantics
- Why not look back in the past?

$$G (a? \rightarrow F_{=1}^{-1} a!)$$

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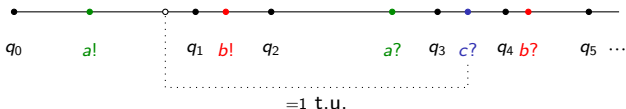
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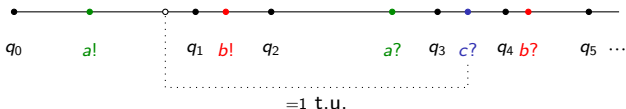
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- this formula is in **TPTL** (pointwise sem.), not in **MTL**

What we have proved so far

Theorem

Over **finite runs**, the model-checking problem is:

	pointwise sem.	continuous sem.
MTL	NPR [OW07]	undecidable [AFH96]
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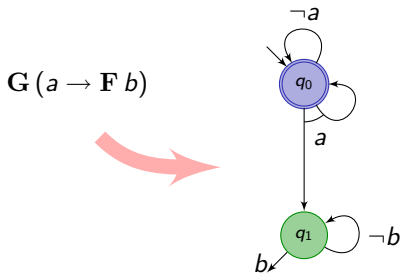
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LTL formulas can be turned into alternating (Büchi) automata

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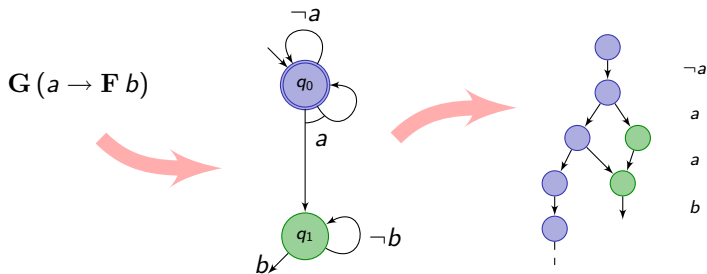
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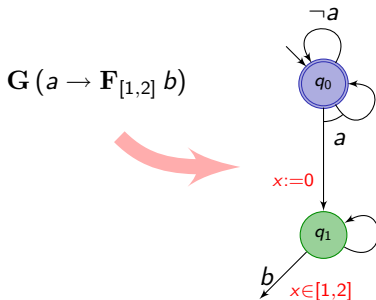
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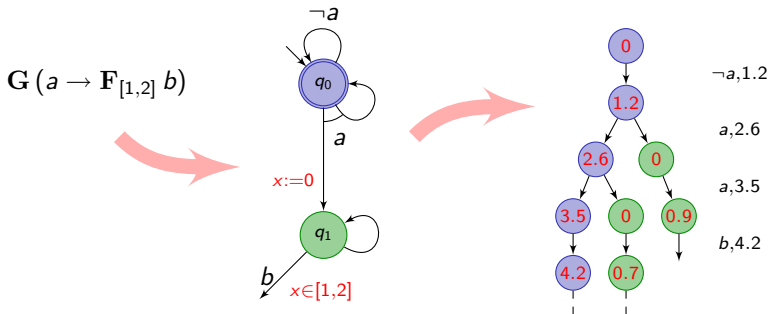
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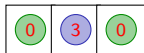
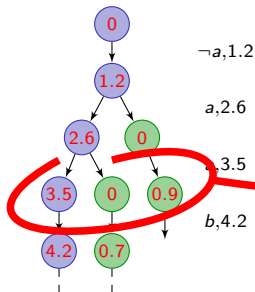


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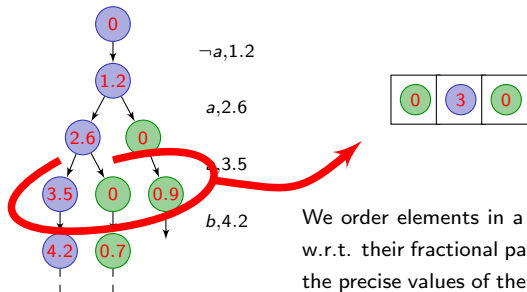


An abstract transition system



We order elements in a slice of the tree w.r.t. their fractional part, and we forget the precise values of the fractional parts.

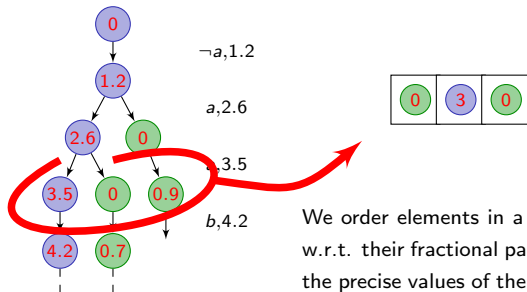
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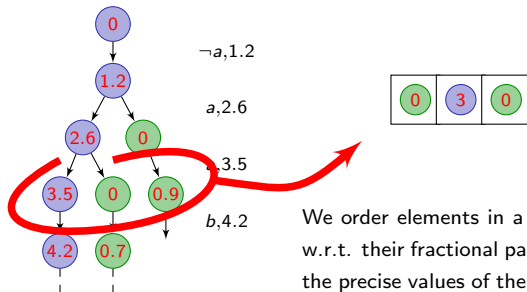
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higman \sqsubseteq highmountain

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What about infinite behaviours?

- the previous algorithm cannot be lifted to the infinite behaviours framework

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Outline

1. Introduction
2. Definition of the logics
3. The timed automaton model
4. The model-checking problem
5. Some interesting fragments
6. Conclusion

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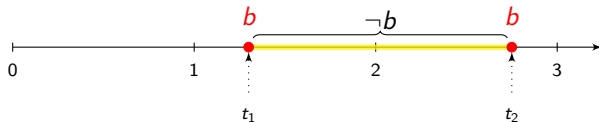
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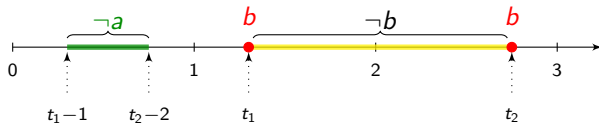
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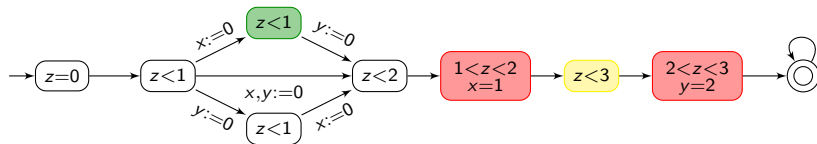
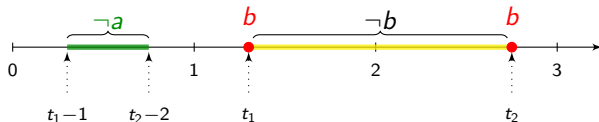
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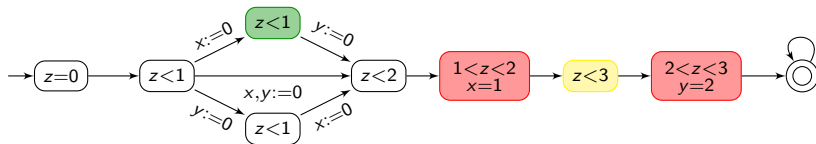
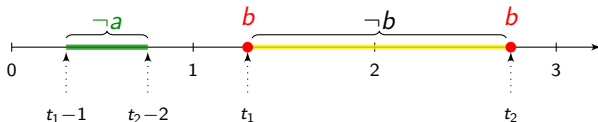
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👉 This idea can be extended to any formula in **MITL**

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 - $\text{coFlat-MTL}_{\text{LTL}}$ contains Bounded-MTL (all modalities are time-bounded)

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- A Bounded-MTL formula may define a non timed-regular language:

$$\mathbf{G}_{\leq 1} (\bullet \rightarrow \mathbf{F}_{=1} \bullet) \wedge \mathbf{G}_{\leq 1} \bullet \wedge \mathbf{G}_{(1,2]} \bullet$$

defines the context-free language $\{\bullet^n \bullet^m \mid n \leq m\}$.

Algorithm for Bounded-MTL

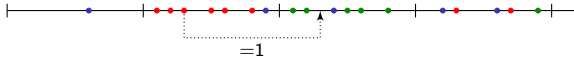
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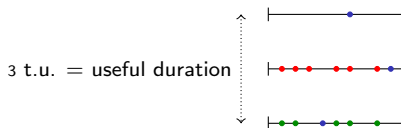
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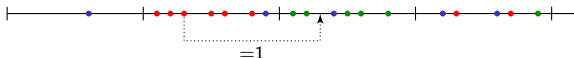
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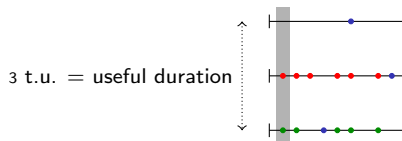
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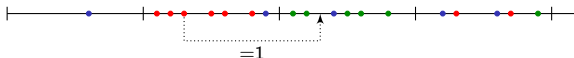
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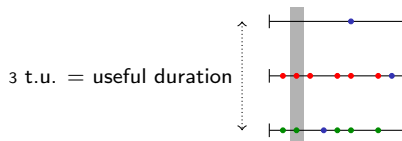
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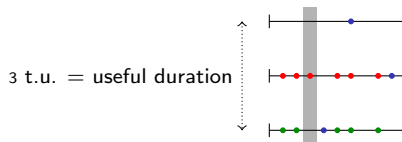
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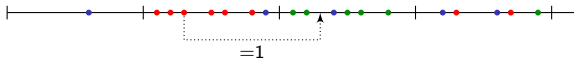
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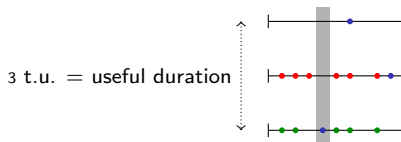
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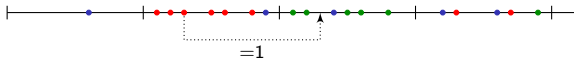
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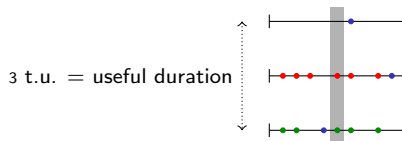
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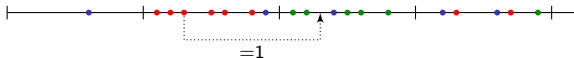
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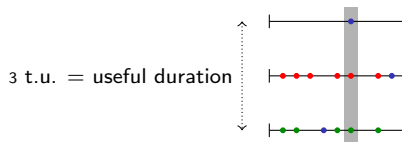
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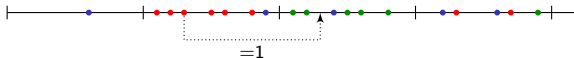
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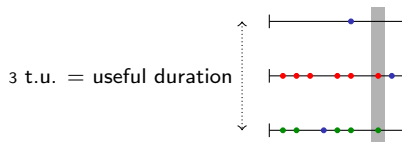
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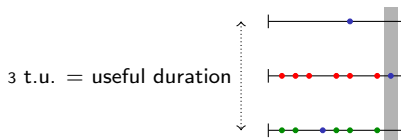
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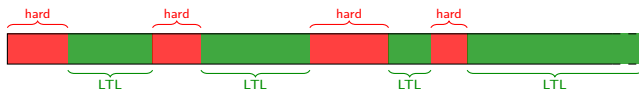
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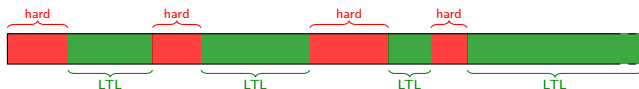


- where - the number of hard fragments is at most exponential
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where - the number of hard fragments is at most exponential
 - the total duration of hard fragments is at most exponential

- **hard** fragment = sliding window algorithm
- **LTL** fragment = finite automaton computation

The ultimate fragment?

- MITL and coFlat-MTL_{LTL} have 'low' complexities for pretty different reasons...

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We define coFlat-MTL_{MITL}:

[BMOW08]

$$\text{coFlat-MTL}_{\text{MITL}} \ni \varphi ::= a \mid \neg a \mid \varphi \vee \varphi \mid \varphi \wedge \varphi \mid \varphi \mathbf{U}_{/} \psi \mid \psi \tilde{\mathbf{U}}_{/} \varphi$$

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- Examples:
 - $\mathbf{F G}_{\leq 1} \bullet$ is in $\text{coFlat-MTL}_{\text{MITL}}$
 - $\mathbf{F G}_{=1} \bullet$ is not in $\text{coFlat-MTL}_{\text{MITL}}$
 - $\text{coFlat-MTL}_{\text{MITL}}$ generalizes both logics MITL and $\text{coFlat-MTL}_{\text{LTL}}$

Model-checking $\text{coFlat-MTL}_{\text{MITL}}$

Theorem

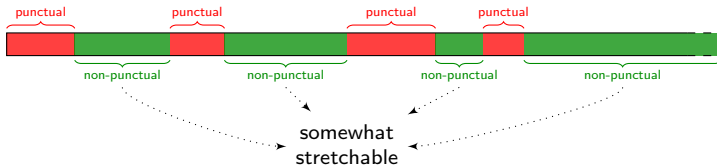
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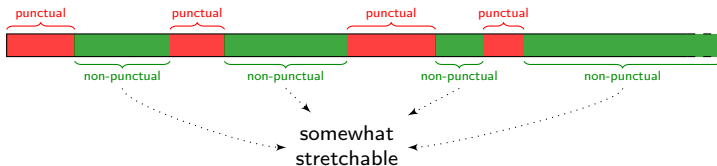


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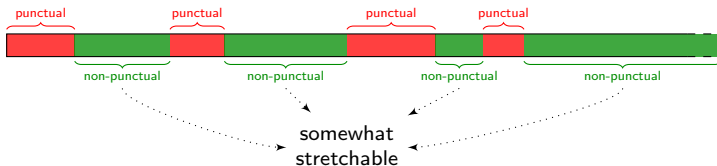


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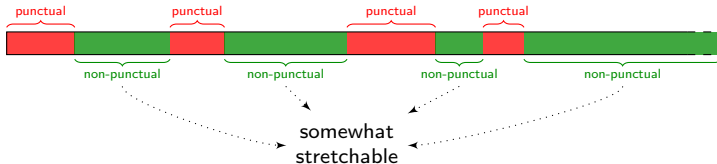
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Stretchable signal:



- Any model of an **LTL** formula is stretchable.
- Any model of an **MITL** formula is *somewhat* stretchable.

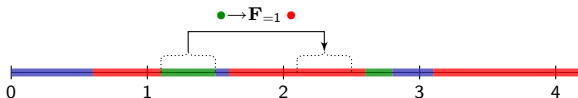
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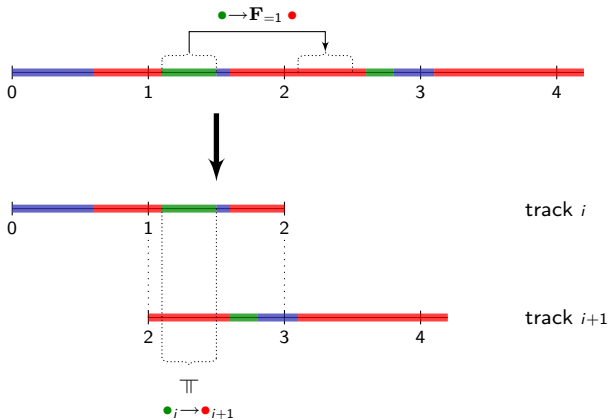
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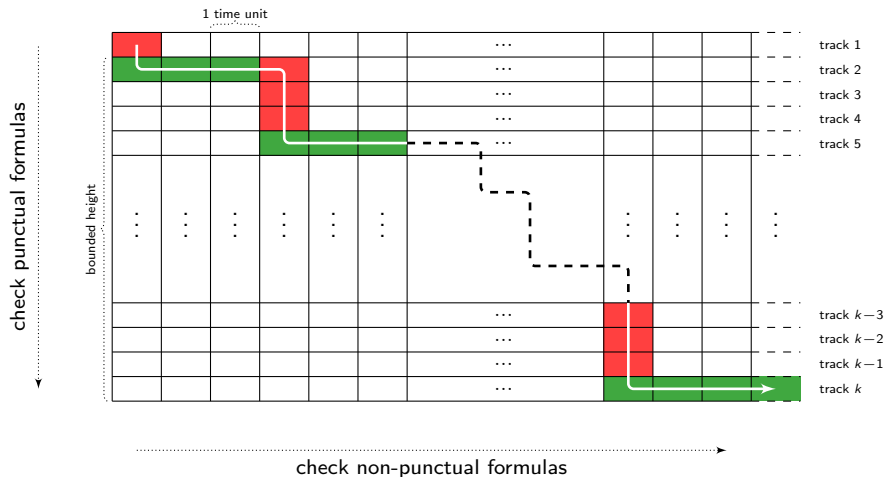


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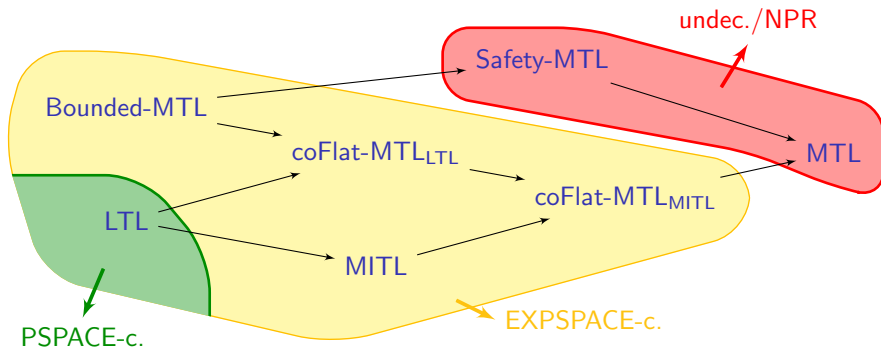


A tableau satisfiability problem

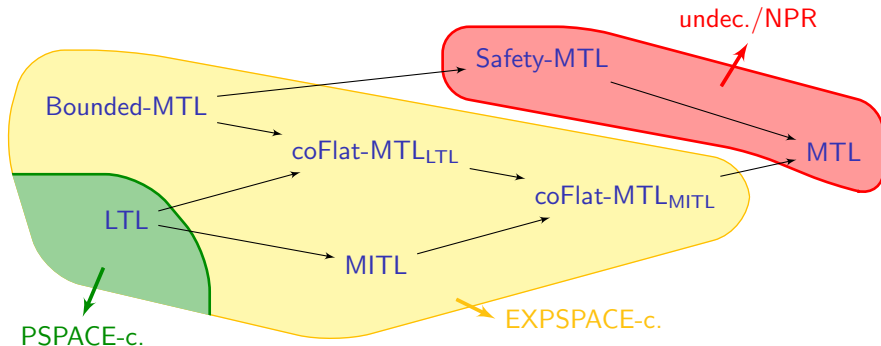


☞ transform into sat. prob. for **LTL+Past** over \mathbb{R}_+ (PSPACE: [Rey04])

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+ add **Positive-MTL** [PW09]

Outline

1. Introduction
2. Definition of the logics
3. The timed automaton model
4. The model-checking problem
5. Some interesting fragments
6. Conclusion

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- Recent advances have raised a new interest for **linear-time timed temporal logics**
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 - Some rather 'efficient' subclasses
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- No real data structures do exist for these logics.

Going further with quantities...

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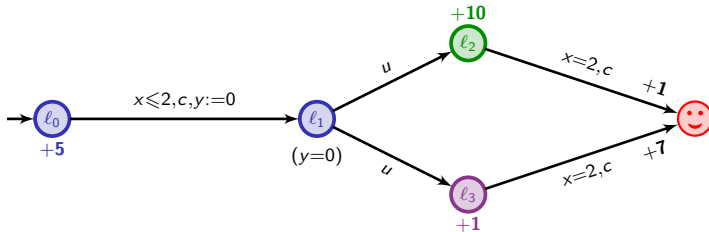
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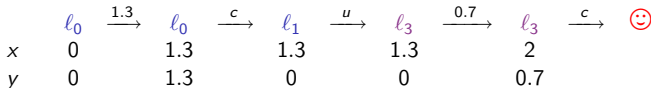
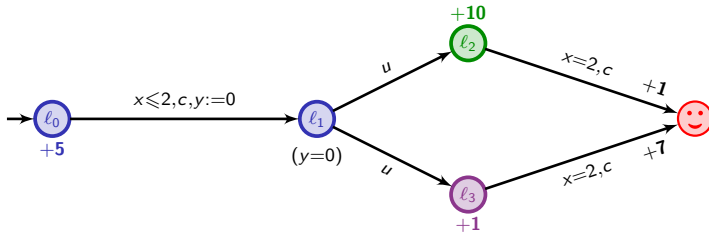
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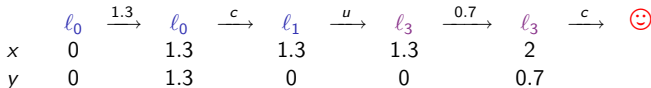
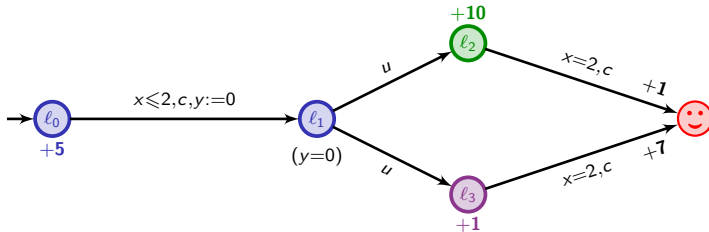
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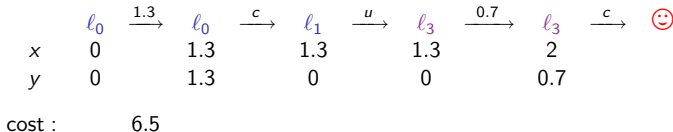
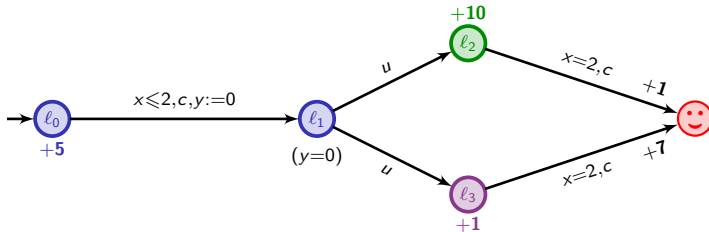


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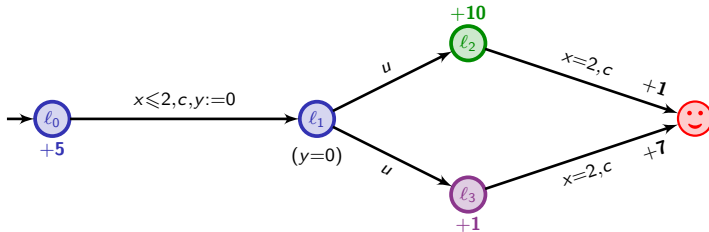
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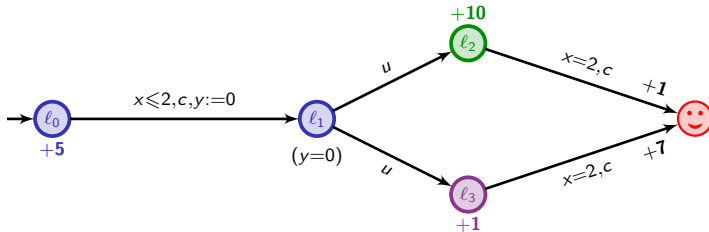


	l_0	$\xrightarrow{1.3}$	l_0	\xrightarrow{c}	l_1	\xrightarrow{u}	l_3	$\xrightarrow{0.7}$	l_3	\xrightarrow{c}	😊
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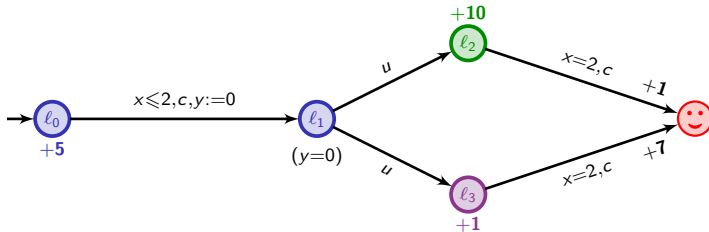


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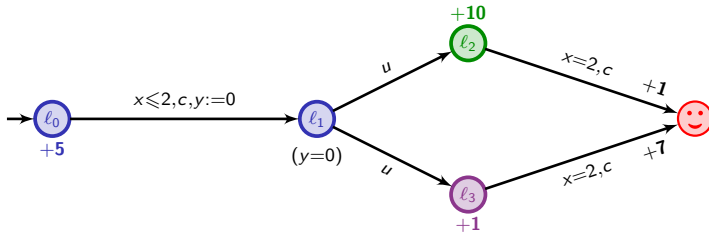


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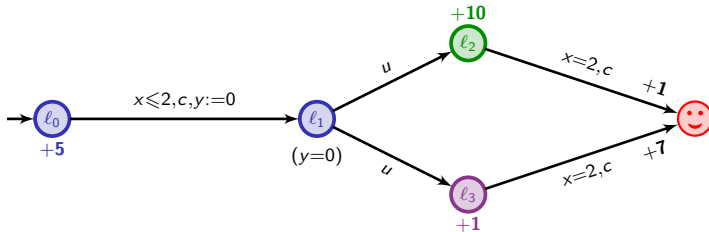


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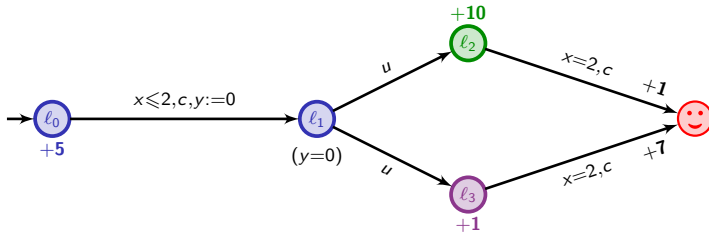


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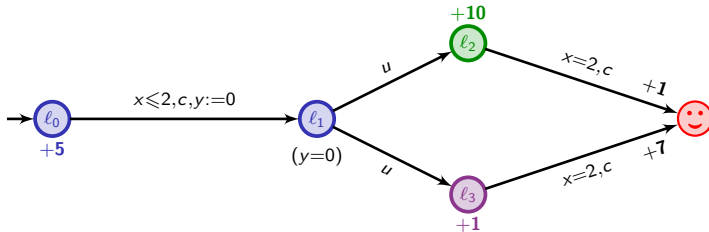


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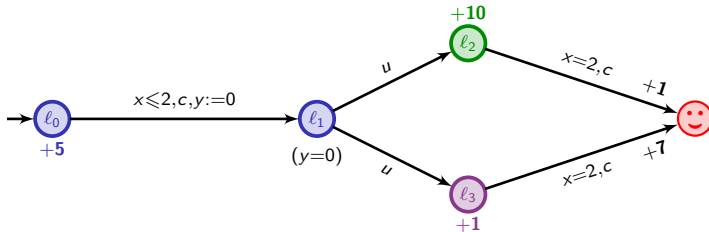
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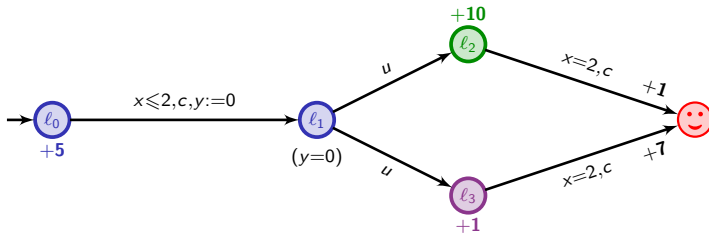
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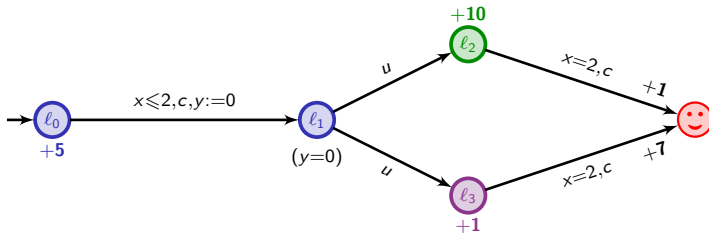
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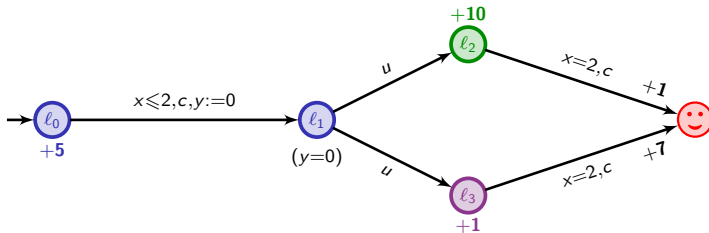
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↪ *strategy:* leave immediately l_0 , go to l_3 , and wait there 2 t.u.

[ALP01] Alur, La Torre, Pappas. Optimal paths in weighted timed automata (*HSCC'01*).

[BFH+01] Behrmann, Fehnker, Hune, Larsen, Pettersson, Romijn, Vaandrager. Minimum-cost reachability in priced timed automata (*HSCC'01*).